

YiCHiP YC1021 Bluetooth 4.1 BR+EDR+BLE Dual-Mode Module User Manual

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YiCHiP YC1021 Bluetooth 4.1 BR+EDR+BLE Dual-Mode Module



General Description

The YC1021 is a very low power, high performance, and highly integrated Bluetooth 4.1 BR/EDR + BLE + 2.4G Proprietary triple-mode solution, designed for operation over the 2400MHz to 2483.5Mhz ISM frequency band. YC1021 is manufactured using an advanced 55nm CMOS low leakage process, which offers the highest integration, lowest power consumption, lowest leakage current, and reduced BOM cost while simplifying the overall system design. Rich peripherals including an 8-channel general purpose ADC, power-on-reset (POR), Arithmetic Accelerators, USB2.0, 3axis Q-decoder, ISO7816, UART/SPI/I2C, and up to 32 GPIOs, which further reduce overall system cost and size. YC1021 operates with a power supply range from 1.8 to 5.5V and has very low power consumption in both Tx and Rx modes, enabling long lifetimes in battery-operated systems while maintaining excellent RF performance. The device can enter an ultra low power sleep mode in which the registers and retention memory content are retained while low power Oscillator and sleep timer is ON. Different package from QFN4x4_32L(up to 17 GPIOs) to QFN7x7_56L (up to 32 GPIOs) is available.

Key Features

- Bluetooth 4.1 BR/EDR+BLE+2.4GHz-Proprietary triple-mode RF SOC
- Very Low Power Consumption
 - 10nA shut down mode (external interrupts)
 - 620nA sleep mode (32kHz RC OSC, sleep timer, and register ON)
 - 2uA retention mode (32kHz RC OSC, sleep timer, 2k retention memory, and register ON)
 - Rx peak current @3V (ideal DCDC)
 - 6.75mA in BLE/2.4G mode
 - 7.25mA in 3.0(EDR) mode
 - Tx peak current @3V (-2dBm, ideal DCDC)
 - 16.5mA in BLE/2.4G mode
 - 17mA in 3.0(EDR) mode
 - Rx peak current w/o DCDC
 - 16mA in BLE/2.4G mode
 - 17mA in 3.0(EDR) mode
 - Tx peak current w/o DCDC @ -2dBm

- 22mA in BLE/2.4G mode
- 23mA in 3.0(EDR) mode
- <25uA avg, 500ms sniff hold connection
- 2.4GHz Transceiver
 - Single-end RFIO
 - -93dBm in BLE mode
 - support 250kbps, 1/2/3Mbps data rates
 - Tx Power up to +6dBm
- Oscillators
 - 16M/24M/32M XTAL supported (default 24M)
 - 50M RC oscillator
 - Low Jitter 32K RC oscillator
- Dual-Core Digital Architecture
 - 8051 Core for application
 - 16kB code RAM
 - 32bit-RISC Core for link management
 - 80kB code ROM
 - 8kB code RAM
 - All RAMs can be set to retention mode
- Arithmetic Accelerators [Accuracy : (sign, 15b.16b)]
 - sin/cos/tan/sin-1 /cos-1 /tan-1 / multi/div/sqrt
- Analog Peripherals
 - 8 channel ADC with 10-bit accuracy/3Msps
- Digital Peripherals
 - USB 2.0 full speed (12Mbps)
 - Two-wire Master (I2C compatible), up to 400kbps; UART(RTS/CTS) with HCI-H5 protocol, upto 3.25Mbps; SPI Master, up to 24Mbps
 - ISO7816
 - AES128 HW encryption
 - LED drive capability
 - PWM
 - 20×8 keyscan
 - 3 axis Q-decoder

Package Variants

- QFN 7×7 56L
- QFN 6×6 48L
- QFN 4×4 32L
- Die

Applications

- Sports & Fitness
- Healthcare & medical
- Remote control
- PC peripherals (mouse, keyboard)
- Game control
- Mobile phone accessories
- TV Setup Boxes
- SPP
- 3D Glasses

FCC

This device complies with part 15 of the FCC Rules. Operation is subject to the following two conditions:

1. this device may not cause harmful interference, and
2. this device must accept any interference received, including interference that may cause undesired operation.


Any changes or modifications not expressly approved by the party responsible for compliance could void the user's authority to operate the equipment.

NOTE: This equipment has been tested and found to comply with the limits for a Class B digital device, pursuant to Part 15 of the FCC Rules. These limits are designed to provide reasonable protection against harmful interference in a residential installation. This equipment generates, uses, and can radiate radio frequency energy and, if not installed and used in accordance with the instructions, may cause harmful interference to radio communications. However, there is no guarantee that interference will not occur in a particular installation. If this equipment does cause harmful interference to radio or television reception, which can be determined by turning the equipment off and on, the user is encouraged to try to correct the interference by one or more of the following measures:

- Reorient or relocate the receiving antenna.
- Increase the separation between the equipment and receiver.
- Connect the equipment into an outlet on a circuit different from that to which the receiver is connected.
- Consult the dealer or an experienced radio/TV technician for help.

The device has been evaluated to meet general RF exposure requirements. The device can be used in portable exposure conditions without restriction

Documents / Resources

 <p>YiCHIP</p> <p>YC1021</p> <p>Bluetooth 4.1 - 1.0 (BT) - 1.0 (BT) - 1.0 (BT)</p> <p>Preliminary Brief</p> <p>Yichip Microelectronics</p> <p>©2013</p>	<p>YiCHIP YC1021 Bluetooth 4.1 BR+EDR+BLE Dual-Mode Module [pdf] User Manual UCSI2100, 2A6AC-UCSI2100, 2A6ACUCSI2100, YC1021 Bluetooth 4.1 BR EDR BLE Dual-Mode Module, YC1021, Bluetooth 4.1 BR EDR BLE Dual-Mode Module</p>
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