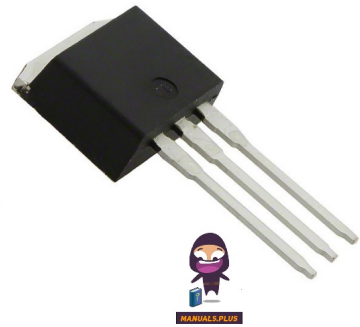


VISHAY®
IRFBC40
Siliconix
Discrete



IRFBC40 Vishay Siliconix Discrete Owner's Manual

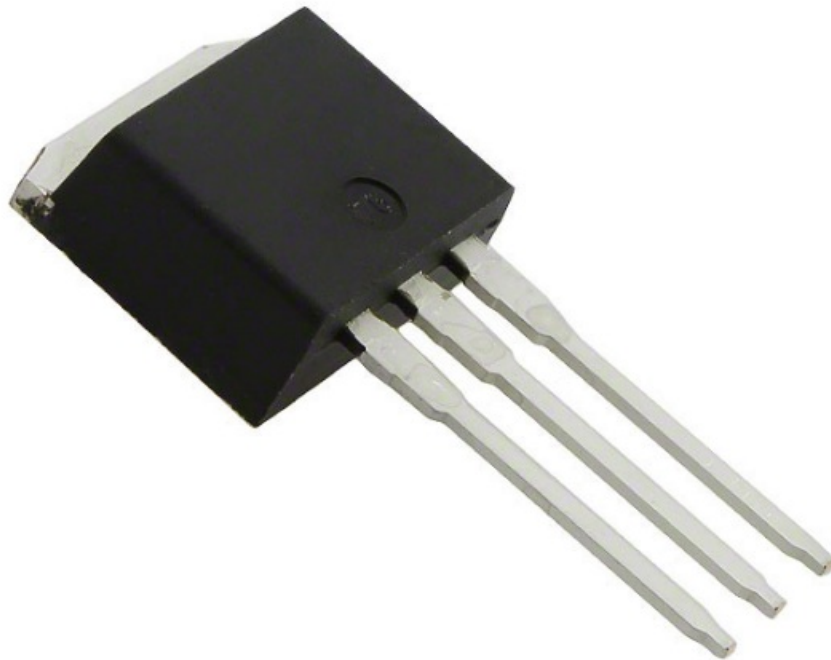
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IRFBC40 Vishay Siliconix Discrete



Specifications

- **Brand:** Vishay Siliconix
- **Model:** IRFBC40
- **Type:** Power MOSFET
- **Package Type:** TO-220AB
- **Channel Type:** N-Channel
- **Drain-Source Voltage (VDS):** 600V
- **On-Resistance (RDS(on)):** 1.2 Ω
- **Total Gate Charge (Qg max.):** 60nC
- **Gate-Source Charge (Qgs):** 8.3nC
- **Gate-Drain Charge (Qgd):** 30nC

Description

Third-generation power MOSFETs from Vishay provide the designer with the best combination of fast switching, ruggedized device design, low on-resistance, and cost-effectiveness. The TO-220AB package is widely used in commercial-industrial applications due to its low thermal resistance and cost.

Ordering Information

- **Lead (Pb)-free:** IRFBC40PbF
- **Lead (Pb)-free and halogen-free:** IRFBC40PbF-BE3

Thermal Resistance Ratings

- Maximum Junction-to-Ambient: 62°C/W
- Case-to-sink, flat, greased surface: 1.0°C/W
- Maximum Junction-to-Case (Drain): 0.50°C/W

Product Usage Instructions

1. **Mounting:** Use a 6-32 or M3 screw for mounting the device with the specified torque.
2. **Operating Temperature:** Ensure the operating temperature range is between -55°C to +150°C.
3. **Soldering:** Follow the soldering recommendations with a peak temperature not exceeding the specified value for 10 seconds.

FEATURES

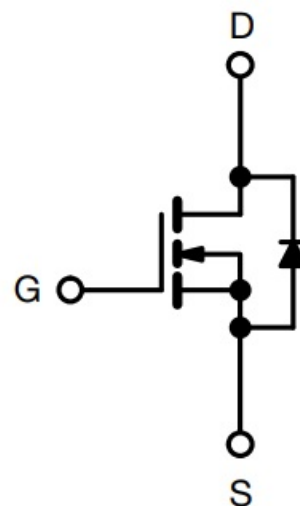
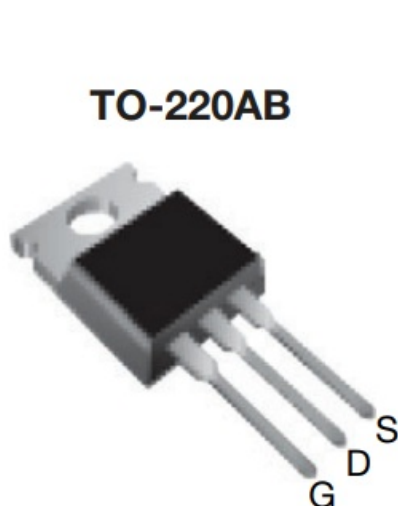
- Dynamic dV/dt rating
- Repetitive avalanche rated
- Fast switching
- Ease of paralleling
- Simple drive requirements
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

Note

This datasheet provides information about parts that are RoHS-compliant and / or parts that are non-RoHS-compliant. For example, parts with lead (Pb) terminations are not RoHS-compliant. Please see the information/tables in this datasheet for details

DESCRIPTION

Third generation power MOSFETs from Vishay provide the designer with the best combination of fast switching, ruggedized device design, low on-resistance and cost-effectiveness. The TO-220AB package is universally preferred for all commercial-industrial applications at power dissipation levels to approximately 50 W. The low thermal resistance and low package cost of the TO-220AB contribute to its wide acceptance throughout the industry.



N-Channel MOSFET

PRODUCT SUMMARY		
V _{DS} (V)	600	
RDS(on) (W)	V _{GS} = 10 V	1.2
Q _g max. (nC)	60	
Q _{gs} (nC)	8.3	
Q _{gd} (nC)	30	
Configuration	Single	

ORDERING INFORMATION	
Package	TO-220AB
Lead (Pb)-free	IRFBC40PbF
Lead (Pb)-free and halogen-free	IRFBC40PbF-BE3

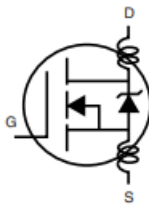
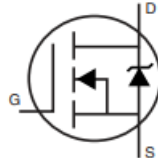
ORDERING INFORMATION						
Package			TO-220AB			
Lead (Pb)-free			IRFBC40PbF			
Lead (Pb)-free and halogen-free			IRFBC40PbF-BE3			
ABSOLUTE MAXIMUM RATINGS (T _C = 25 °C, unless otherwise noted)						
PARAMETER				SYMBOL	LIMIT	UNIT
Drain-source voltage				V _{DS}	600	V
Gate-source voltage				V _{GS}	± 20	
Continuous drain current	V _{GS} at 10 V	T _C = 25 °C		I _D	6.2	A
		T _C = 100 °C			3.9	
Pulsed drain current a				I _{DM}	25	
Linear derating factor					1.0	W/°C
Single pulse avalanche energy b				E _{AS}	570	mJ
Repetitive avalanche current a				I _{AR}	6.2	A
Repetitive avalanche energy a				E _{AR}	13	mJ
Maximum power dissipation	T _C = 25 °C			P _D	125	W
Peak diode recovery dV/dt c				dV/dt	3.0	V/ns
Operating junction and storage temperature range				T _J , T _{stg}	-55 to +150	°C
Soldering recommendations (peak temperature) d	For 10 s				300	
Mounting torque	6-32 or M3 screw				10	lbf · in
					1.1	N · m

Notes

1. Repetitive rating; pulse width limited by maximum junction temperature (see fig. 11)
2. $V_{DD} = 50\text{ V}$, starting $T_J = 25\text{ }^{\circ}\text{C}$, $L = 27\text{ mH}$, $R_g = 25\text{ }\Omega$, $I_{AS} = 6.2\text{ A}$ (see fig. 12)
3. $I_{SD} \leq 6.2\text{ A}$, $dI/dt \leq 80\text{ A}/\mu\text{s}$, $V_{DD} \leq V_{DS}$, $T_J \leq 150\text{ }^{\circ}\text{C}$
4. 1.6 mm from the case

THERMAL RESISTANCE RATINGS				
PARAMETER	SYMBOL	TYP.	MAX.	UNIT
Maximum junction-to-ambient	RthJA	–	62	°C/W
Case-to-sink, flat, greased surface	RthCS	0.50	–	
Maximum junction-to-case (drain)	RthJC	–	1.0	

SPECIFICATIONS (T _J = 25 °C, unless otherwise noted)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNIT
Static							
Drain-source breakdown voltage	V _{DS}	V _{GS} = 0 V, I _D = 250 μA		600	–	–	V
V _{DS} temperature coefficient	DV _{DS} /T _J	Reference to 25 °C, I _D = 1 mA		–	0.7	–	V/°C
Gate-source threshold voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = 250 μA		2.0	–	4.0	V
Gate-source leakage	I _{GSS}	V _{GS} = ± 20 V		–	–	± 10 0	nA
Zero gate voltage drain current	I _{DSS}	V _{DS} = 600 V, V _{GS} = 0 V		–	–	100	μA
		V _{DS} = 480 V, V _{GS} = 0 V, T _J = 125 °C		–	–	500	
Drain-source on-state resistance	R _{DS(on)}	V _{GS} = 10 V	I _D = 3.7A b	–	–	1.2	W
Forward transconductance	g _{fs}	V _{DS} = 100 V, I _D = 3.7 A b		4.7	–	–	S
Dynamic							
Input capacitance	C _{iss}	V _{GS} = 0 V, V _{DS} = 25 V, f = 1.0 MHz, see fig. 5		–	1300	–	pF
Output capacitance	C _{oss}			–	160	–	
Reverse transfer capacitance	C _{rss}			–	30	–	
Total gate charge	Q _g	V _{GS} = 10 V	I _D = 6.2 A, V _{DS} = 360 V, see fig. 6 and 13 b	–	–	60	nC
Gate-source charge	Q _{gs}			–	–	8.3	
Gate-drain charge	Q _{gd}			–	–	30	
Turn-on delay time	t _{d(on)}	V _{DD} = 300 V, I _D = 6.2 A, R _g = 9.1 W, R _D = 47 W, see fig. 10 b		–	13	–	ns
Rise time	t _r			–	18	–	
Turn-off delay time	t _{d(off)}			–	55	–	
Fall time	t _f			–	20	–	
Gate input resistance	R _g	f = 1 MHz, open drain		0.3	–	3.9	W

Internal drain inductance	L _D	Between lead, D	–	4.5	–	nH
Internal source inductance	L _S	6 mm (0.25") from package and center of G die contact	–	7.5	–	
						
Drain-Source Body Diode Characteristics						
Continuous source-drain diode current	I _S	MOSFET symbol D	–	–	6.2	A
Pulsed diode forward current	ISM	showing the integral reverse p – n junction diode G	–	–	25	
						
Body diode voltage	VSD	T _J = 25 °C, I _S = 6.2 A, V _{GS} = 0 V	–	–	1.5	V
Body diode reverse recovery time	trr	T _J = 25 °C, I _F = 6.2 A, dI/dt = 100 A/μs	–	450	940	ns
Body diode reverse recovery charge	Qrr		–	3.8	7.9	μC
Forward turn-on time	ton	Intrinsic turn-on time is negligible (turn-on is dominated by L _S and L _D)				

Notes

1. Repetitive rating; pulse width limited by maximum junction temperature (see fig. 11)
2. Pulse width $\leq 300\text{ }\mu\text{s}$; duty cycle $\leq 2\%$

TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

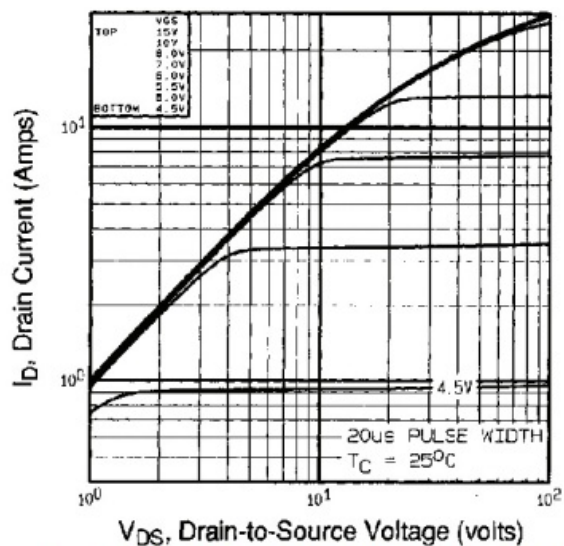


Fig. 1 - Typical Output Characteristics, $T_C = 25^\circ\text{C}$

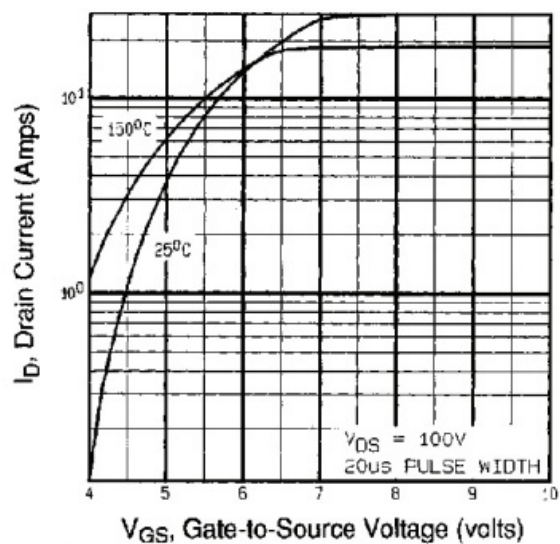


Fig. 3 - Typical Transfer Characteristics

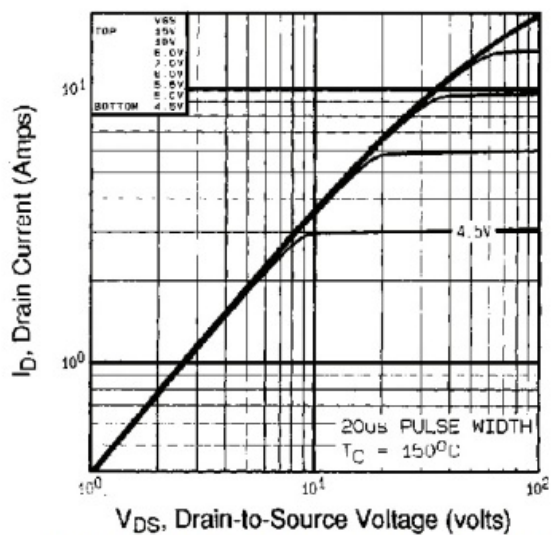


Fig. 2 - Typical Output Characteristics, $T_C = 150^\circ\text{C}$

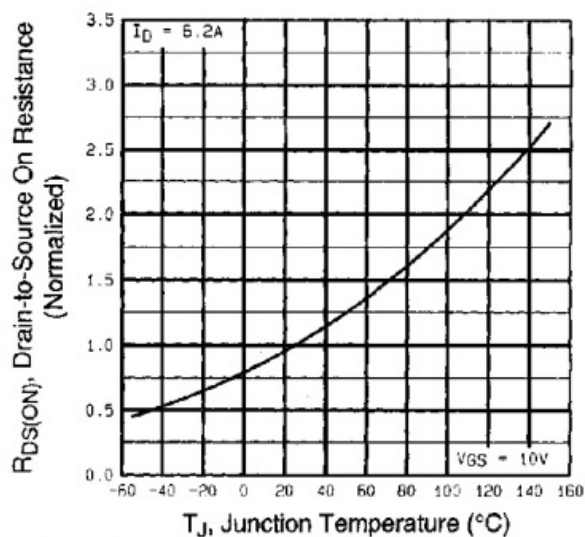


Fig. 4 - Normalized On-Resistance vs. Temperature

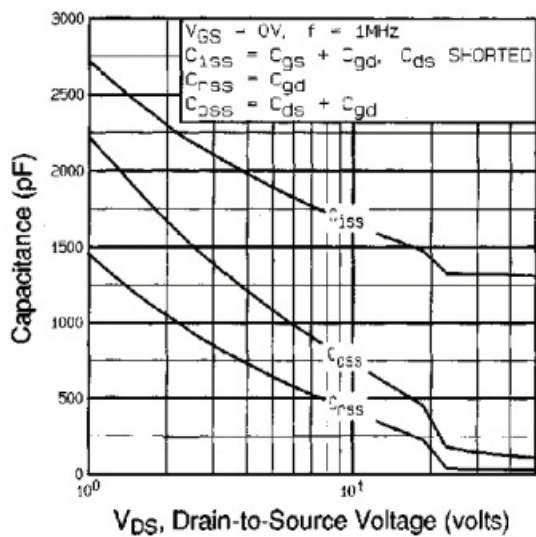


Fig. 5 - Typical Capacitance vs. Drain-to-Source Voltage

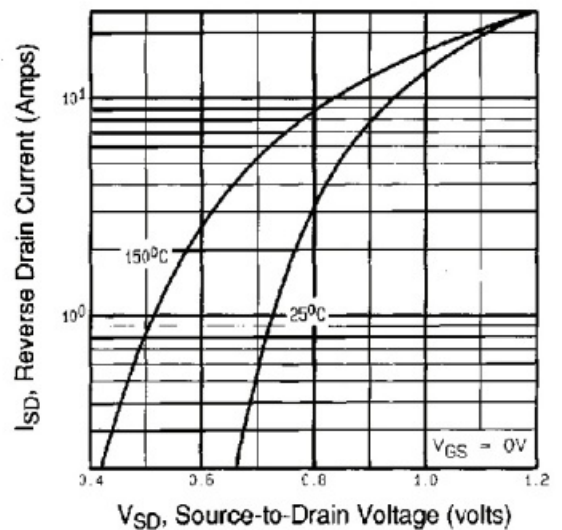


Fig. 7 - Typical Source-Drain Diode Forward Voltage

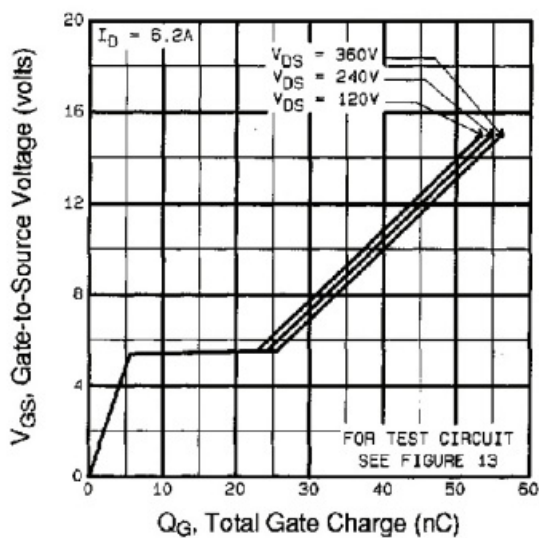


Fig. 6 - Typical Gate Charge vs. Gate-to-Source Voltage

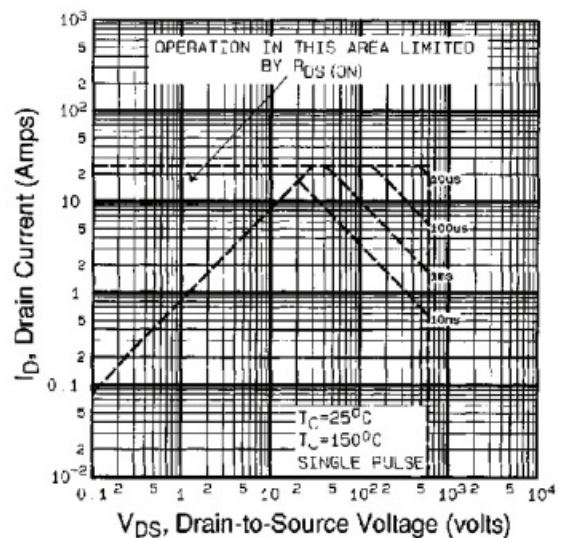


Fig. 8 - Maximum Safe Operating Area

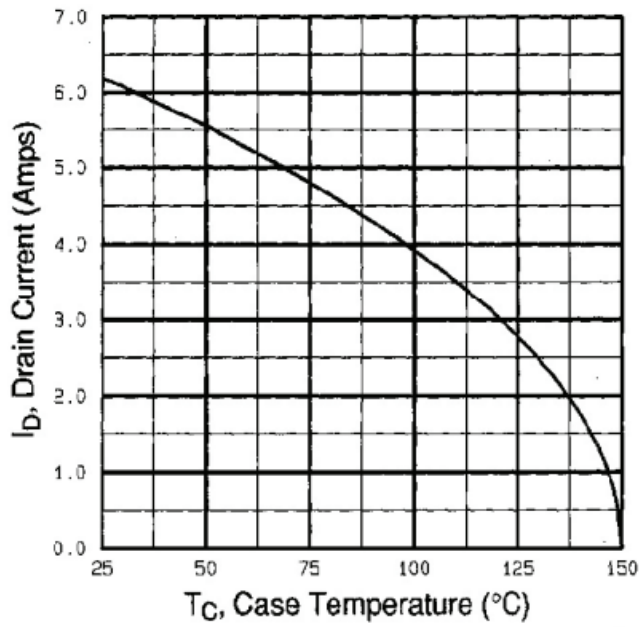


Fig. 9 - Maximum Drain Current vs. Case Temperature

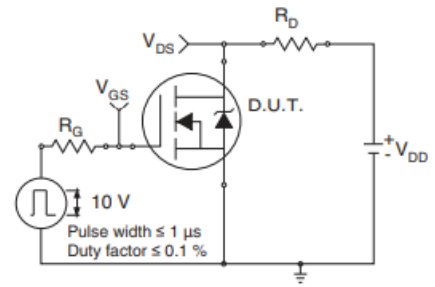


Fig. 10a - Switching Time Test Circuit

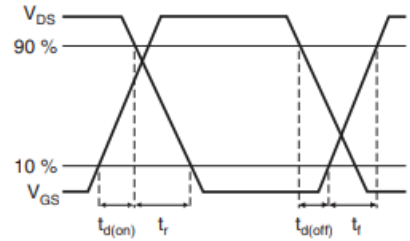


Fig. 10b - Switching Time Waveforms

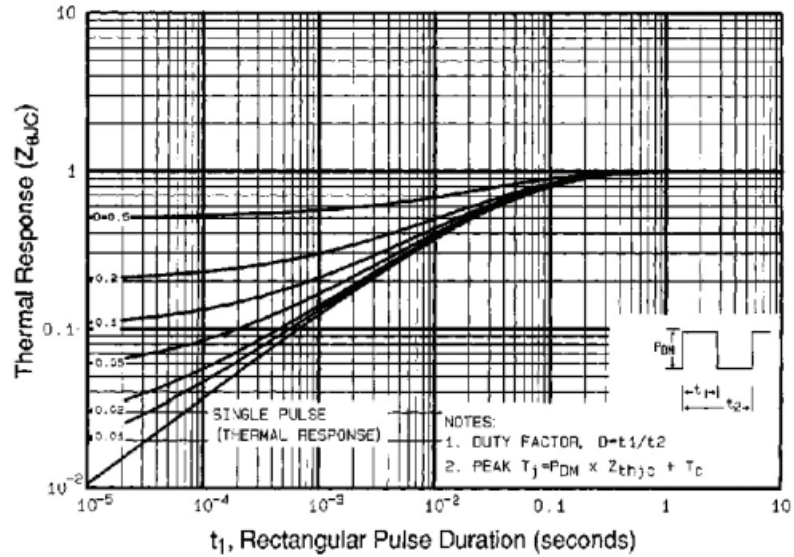


Fig. 11 - Maximum Effective Transient Thermal Impedance, Junction-to-Case

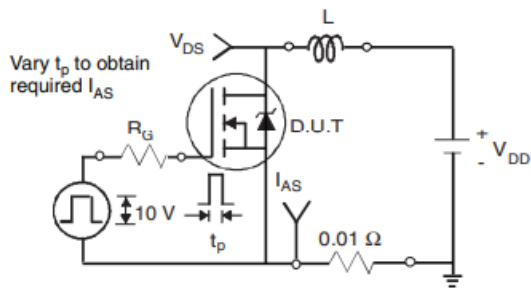


Fig. 12a - Unclamped Inductive Test Circuit

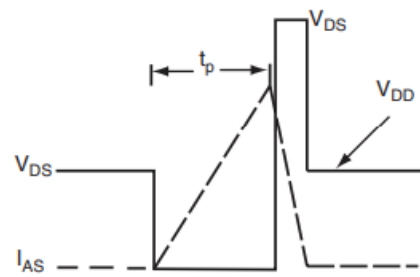


Fig. 12b - Unclamped Inductive Waveforms

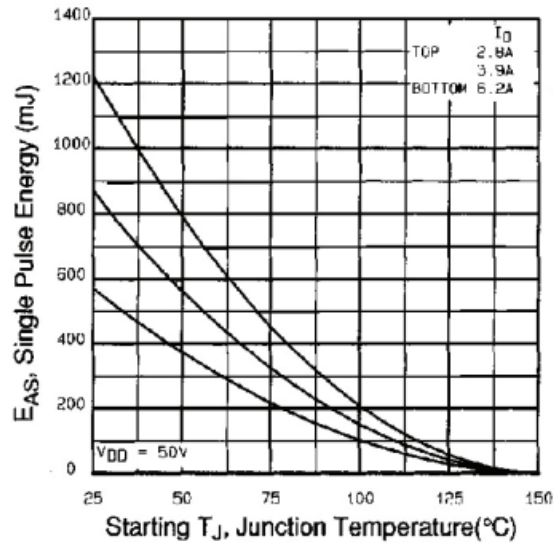


Fig. 12c - Maximum Avalanche Energy vs. Drain Current

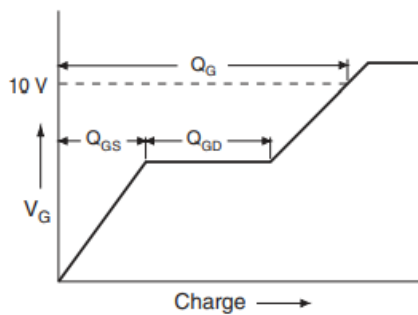


Fig. 13a - Basic Gate Charge Waveform

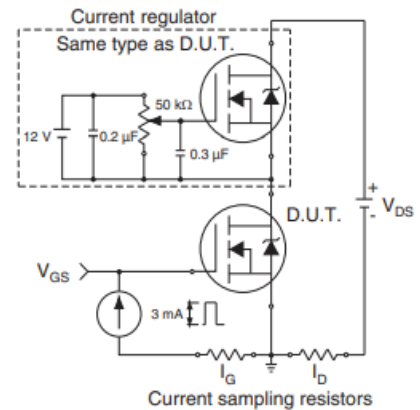


Fig. 13b - Gate Charge Test Circuit

Peak Diode Recovery dV/dt Test Circuit

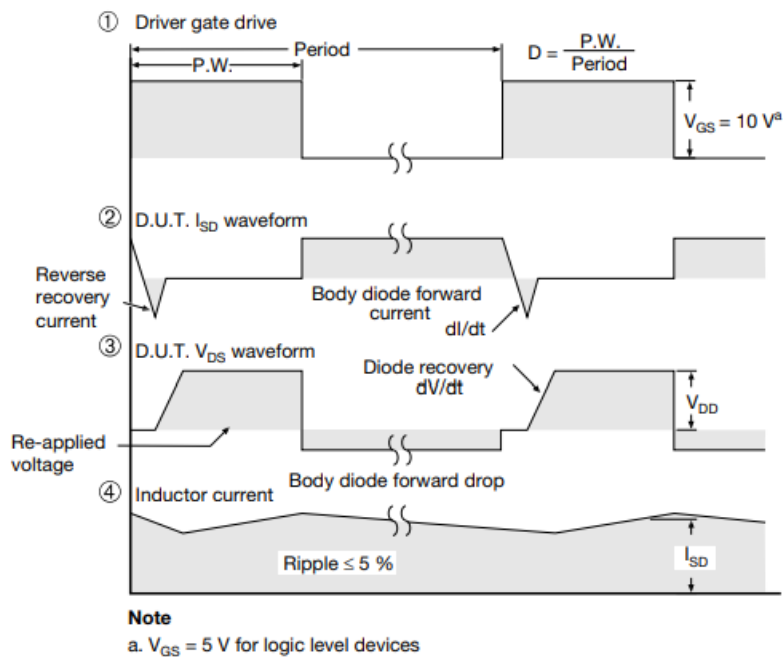
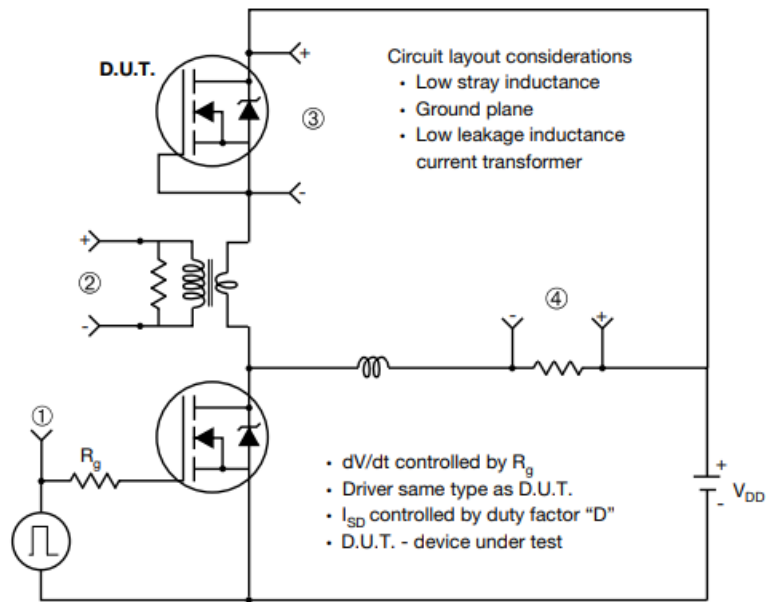


Fig. 14 - For N-Channel

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Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see www.vishay.com/ppg?91115.

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FAQ

Is this product RoHS-compliant?

This datasheet provides information on RoHS-compliant and non-RoHS-compliant parts. Please refer to the datasheet for specific details.

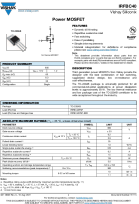
What is the maximum drain-source voltage?

The maximum drain-source voltage is 600V.

What is the package type of this MOSFET?

The package type is TO-220AB.

Documents / Resources



[VISHAY IRFBC40 Vishay Siliconix Discrete](#) [pdf] Owner's Manual
IRFBC40, IRFBC40PbF, IRFBC40PbF-BE3, IRFBC40 Vishay Siliconix Discrete, IRFBC40, Vishay Siliconix Discrete, Siliconix, Discrete

References

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- [User Manual](#)

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