

# The Retro Web XI321TCN XINETRON Network Interface Card Instructions

Home » The Retro Web » The Retro Web XI321TCN XINETRON Network Interface Card Instructions

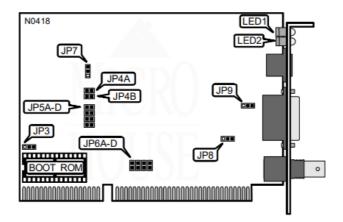


#### Contents

- 1 The Retro Web XI321TCN XINETRON Network **Interface**
- **2 Product Usage Instructions**
- 3 THE NETWORK INTERFACE CARD TECHNICAL **GUIDE**
- 4 Documents / Resources
  - 4.1 References
- **5 Related Posts**



## The Retro Web XI321TCN XINETRON Network Interface



# **Product Specifications**

• NIC Type: Ethernet

• Transfer Rate: 10Mbps

• Data Bus: 16-bit ISA

• Topology: Linear Bus

Wiring Type: Unshielded twisted pair RG58A/U 50ohm coaxial

• Boot ROM: Available

# **Product Usage Instructions**

#### **Jumper Settings**

Refer to the user manual for specific jumper settings based on your system requirements.

## **LED Diagnostic Indicators:**

• **LED1:** Twisted pair network connection status

• LED2: Network collision detection

• LED3: Data transmission status

• LED4: Data reception status

#### **Compatibility Mode Setting:**

To enable compatibility mode, close Pins 2 & 3 of JP3. For non-standard timing systems, enable by closing Pins 1 & 2 of JP3.

#### I/O Base Address Configuration:

Set the I/O base address by configuring JP4A and JP4B according to your system requirements.

## **Boot ROM Address Configuration:**

Configure the Boot ROM address using JP5A, JP5B, JP5C, and JP5D as per the provided guidelines.

## Interrupt Request (IRQ) Settings:

Se the IRQ by configuring JP6A, JP6B, JP6C, and JP6D based on the desired interrupt request.

### **Cable Type Configuration:**

Configure the cable type using JP7 to ensure proper connectivity.

#### Frequently Asked Questions (FAQ):

· Q: What should I do if my system does not work with the NIC card?

A: If your system does not work with the card, enable compatibility mode by closing Pins 1 & 2 of JP3.

• Q: How can I check the network connection status?

**A:** LED1 indicates the twisted pair network connection status. If it is on, the connection is good; if off, there may be a connection issue.

· Q: What does LED3 indicate?

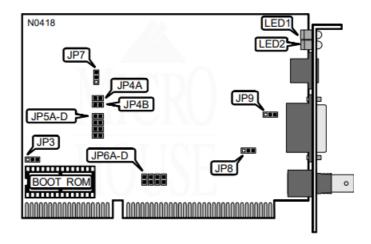
A: LED3 shows the data transmission status. If it is on, data is being transmitted; if off, data is not being transmitted

#### THE NETWORK INTERFACE CARD TECHNICAL GUIDE

# XINETRON, INC.

# XI321TCNREV.A

- NIC Type Ethernet
- Transfer Rate 10Mbps
- Data Bus 16-bit ISA
- Topology Linear Bus
- Wiring Type Unshielded twisted pair RG58A/U 50ohm coaxial AUI transceiver via DB-15 port
- Boot ROM Available



| COMPATIBILITY MODE  |                   |  |  |  |
|---|-------------------|--|--|--|
| Setting   | JP3               |  |  |  |
| íDisabled   | Pins 2 & 3 closed |  |  |  |
| Enabled   | Pins 1 & 2 closed |  |  |  |
| <b>Note:</b> Some systems use non-standard timing. If your system does not work with this card set this option to ena bled (Pins 1 & 2 closed). |                   |  |  |  |
|   |                   |  |  |  |

|    | I/O BASE ADDRESS |        |        |
|----|------------------|--------|--------|
| Ad | ddress           | JP4A   | JP4B   |
| 30 | 00h              | Closed | Closed |
| í3 | 20h              | Closed | Open   |
| 34 | 10h              | Open   | Closed |
| 36 | 60h              | Open   | Open   |
|    |                  |        |        |

| BOOT ROM ADDRESS |      |        |      |        |
|------------------|------|--------|------|--------|
| Address          | JP5A |        | JP5B | JP5C   |
| C8000h           |      | Open   | Open | Open   |
| íCC000h          |      | Closed | Open | Closed |
| D0000h           |      | Open   | Open | Closed |

| BOOT F    | OM     |
|-----------|--------|
| Setting   | JP5D   |
| íDisabled | Open   |
| Enabled   | Closed |
|           |        |

| INTERRUPT REQU | INTERRUPT REQUEST |        |        |        |
|----------------|-------------------|--------|--------|--------|
| IRQ            | JP6A              | JP6B   | JP6C   | JP6D   |
| 2/9            | Closed            | Open   | Open   | Open   |
| 3              | Open              | Closed | Open   | Open   |
| 4              | Open              | Open   | Closed | Open   |
| í5             | Open              | Open   | Open   | Closed |
|                |                   |        |        |        |

| CABLE TYPE                     |     |                       |                       |                   |
|--------------------------------|-----|-----------------------|-----------------------|-------------------|
| Туре                           | JP7 | JP8                   | JF                    | <b>-</b> 9        |
| íRG58A/U 50ohm coaxial         |     | Pins 1 & 2 cl<br>osed | Pins 1 & 2 cl<br>osed | Pins 1 & 2 closed |
| Unshielded twisted pair        |     | Pins 2 & 3 cl<br>osed | Pins 1 & 2 cl<br>osed | Pins 1 & 2 closed |
| AUI transceiver via DB-15 port |     | N/A                   | Pins 2 & 3 cl<br>osed | Pins 2 & 3 closed |



| DIAGNOSTIC LED(S) |        |   |  |  |
|-------------------|--------|---|--|--|
| LED               | Status | Condition                                 |  |  |
| LED1              | On     | Twisted pair network connection is good   |  |  |
| LED1              | Off    | Twisted pair network connection is broken |  |  |
| LED2              | On     | Collision detected on network             |  |  |
| LED2              | Off    | No collisions detected on network         |  |  |
| LED3              | On     | Data is being transmitted                 |  |  |
| LED3              | Off    | Data is not being transmitted             |  |  |
| LED4              | On     | Data is being received                    |  |  |
| LED4              | Off    | Data is not being received                |  |  |
|                   |        |   |  |  |

## **Documents / Resources**



The Retro Web XI321TCN XINETRON Network Interface Card [pdf] Instructions
REV. A, XI321TCN XINETRON Network Interface Card, XI321TCN XINETRON, XI321TCN, XIN
ETRON, XI321TCN Network Interface Card, XINETRON Network Interface Card, Network Interface Card, Network Card, Interface Card

## References

## • User Manual

Manuals+, Privacy Policy

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