




Surenno STP0144C-128128 Series TFT LCD Panel User Manual

[Home](#) » [Surenno](#) » Surenno STP0144C-128128 Series TFT LCD Panel User Manual 

Contents

- 1 Surenno STP0144C-128128 Series TFT LCD Panel
- 2 General Specifications
 - 2.1 ABSOLUTE MAXIMUM RATINGS
 - 2.2 ELECTRICAL CHARACTERISTICS
 - 2.3 BACKLIGHT CHARACTERISTICS
 - 2.4 DIMENSIONAL DRAWING
 - 2.5 INTERFACE PIN CONNECTIONS
 - 2.6 TIMING CHARACTERISTICS
 - 2.7 Reset Input Timing
 - 2.8 BLOCK DIAGRAM OF LCM
- 3 ELECTRO-OPTICAL CHARACTERISTICS
 - 3.1 ELECTRO-OPTICAL CHARACTERISTICS TEST METHOD
 - 3.2 DEFINITION OF OPERATING VOLTAGE, VOP
 - 3.3 DEFINITION OF OPTICAL RESPONSE TIME
 - 3.4 DEFINITION OF VIEWING ANGLE Θ AND
 - 3.5 DEFINITION OF CONTRAST RATIO, CR
- 4 INSPECTION CRITERIA
 - 4.1 Inspection Conditions
 - 4.2 LIGHT METHOD
 - 4.3 Classification of defects
- 5 RELIABILITY
 - 5.1 MTBF
 - 5.2 TESTS
- 6 PRECAUTIONS FOR USING LCD MODULE
 - 6.1 Handling precautions
 - 6.2 STORAGE PRECAUTIONS
 - 6.3 OTHERS
- 7 Documents / Resources
 - 7.1 References
- 8 Related Posts

Surennoo STP0144C-128128 Series TFT LCD Panel



General Specifications

STP0144C-128128 is a color active matrix LCD module incorporating amorphous silicon TFT (Thin Film Transistor). It is composed of a color TFT-LCD panel, driver IC, FPC and a back light unit. The module display area contains 128X128 pixels and can display up to 262K colors. This product accords with RoHS environmental criterion.

Item	Contents	Unit
LCD Type	TFT TRANSMISSIVE	/
Viewing direction	12:00	O' Clock
Module outline (W x HxD)	27.90 x32.64 x1.90	mm
Active area (WxH)	25.50 x26.50	mm
Number of Dots	128(RGB) x128	/
Driver IC	ST7735S	/
Colors	262K	/
Backlight Type	2 LED Parrallel	/
Interface Type	4 SPI Serial interface	/
Input voltage	2.8~3.3V	V

ABSOLUTE MAXIMUM RATINGS

Parameter	Symbol	Min	Max	Unit
Supply voltage for logic	Vcc1,Vcc2	-0.3	4.6	V
Input voltage	Vin	-0.3	VCC+ 0.3	V
Operating temperatur	Top	-20	70	°C
Storage temperature	Tst	-30	80	°C
Humidity	RH	—	90%(Max60C)	RH

ELECTRICAL CHARACTERISTICS

Parameter	Symbol	Min	Typ	Max	Unit
Supply voltage for logic	Vcc -Vss	2.6	2.8	3.2	V
Input Current	Idd	—	10	—	mA
Input voltage ‘ H ’ level	Vih	0.7Vdd	—	Vdd	V
Input voltage ‘ L ’ level	Vil	-Vss	—	0.2 Vdd	V
Output voltage ‘ H ’ level	Voh	0.8 Vcc	—	Vcc	V
Output voltage ‘ L ’ level	Vol	0	0	0.2 Vcc	V

BACKLIGHT CHARACTERISTICS

Item	Symbol	Min	Typ	Max	Unit	Condition
Forward voltage	Vf	2.8	3.0	3.3	V	—
Luminance	Lv	—	200	—	cd/m2	If=40mA
Number of LED	—	2			Piece	—
Connection mode	P	Parallel			—	—

Using condition: constant current driving method If= 20 mA(+/-10%)

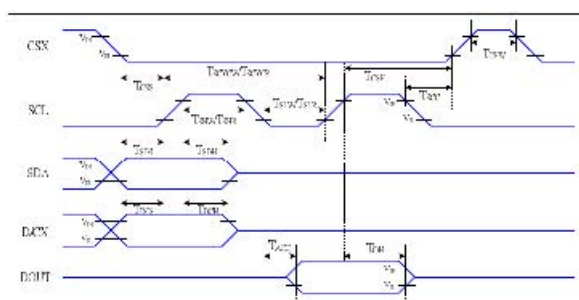
DIMENSIONAL DRAWING



Pin.No	Symbol	Function
1	LEDK	back light power supply negative
2	GND	Ground
3	FMARK	Tearing effect output pin to synchronies MCU to frame rate
4	SDA	the serial input/output signal in serial interface mode.
5	RS	Display data/command selection pin in 4-line serial interface
6	SCK	used to be serial interface clock.
7	CS	chip select signal input
8	RESET	A reset pin.
9	IOVCC18	Power Supply for I/O system.(1.8V or VCC)
10	VCC28	Power Supply for Analog, Digital System and Booster Circuit.
11	GND	Ground
12	LEDA	back light power supply positive
13~15	NC	No Connect

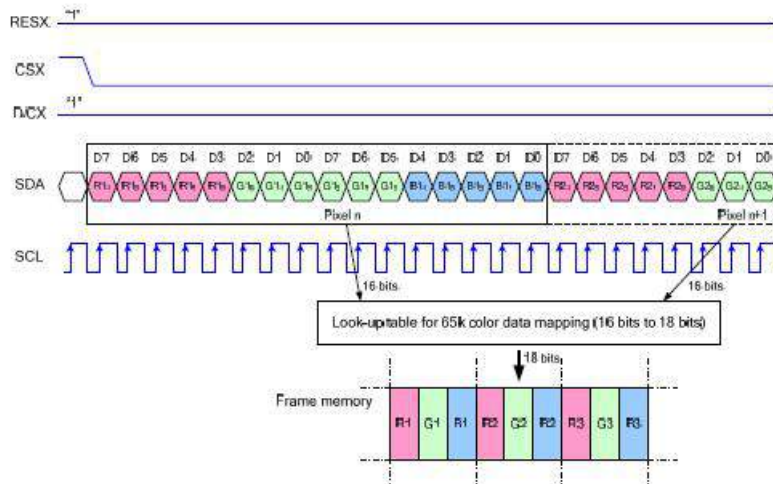
TIMING CHARACTERISTICS

Serial Interface Characteristics (4-line Serial)

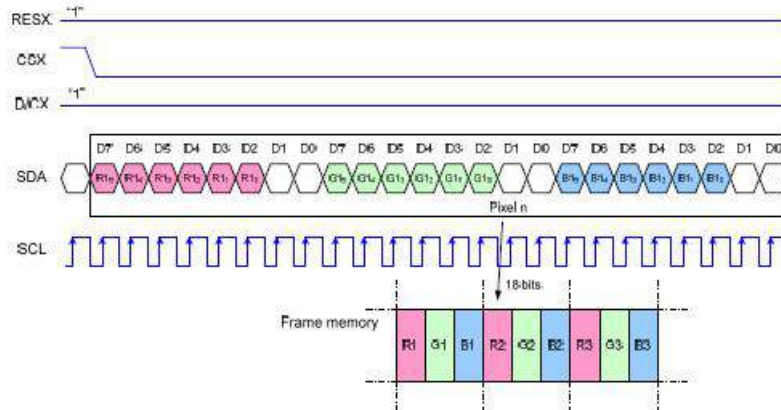


Signal	Symbol	Parameter	MIN	MAX	Unit	Description
CSX	T _{CS}	Chip select setup time (write)	15		ns	
	T _{CSH}	Chip select hold time (write)	15		ns	
	T _{CS}	Chip select setup time (read)	60		ns	
	T _{CSH}	Chip select hold time (read)	65		ns	
	T _{CHW}	Chip select "H" pulse width	40		ns	
SCL	T _{SCYCW}	Serial clock cycle (Write)	66		ns	-write command & data ram
	T _{SHW}	SCL "H" pulse width (Write)	15		ns	
	T _{SLW}	SCL "L" pulse width (Write)	15		ns	
	T _{SCYCR}	Serial clock cycle (Read)	150		ns	-read command & data ram
	T _{SHR}	SCL "H" pulse width (Read)	60		ns	
	T _{SLR}	SCL "L" pulse width (Read)	60		ns	
D/CX	T _{DCS}	D/CX setup time	10		ns	
	T _{DCH}	D/CX hold time	10		ns	
SDA (DIN)	T _{SOS}	Data setup time	10		ns	
	T _{SOH}	Data hold time	10		ns	
DOUT	T _{ACC}	Access time	10	50	ns	For maximum CL=30pF
	T _{OH}	Output disable time	15	50	ns	For minimum CL=8pF

8.8.42 Write data for 16-bit/pixel (RGB-5-6-5-bit input), 65K-Colors, 3Ah="05h"



8.8.43 Write data for 18-bit/pixel (RGB-6-6-6-bit input), 262K-Colors, 3Ah="06h"



Reset Input Timing

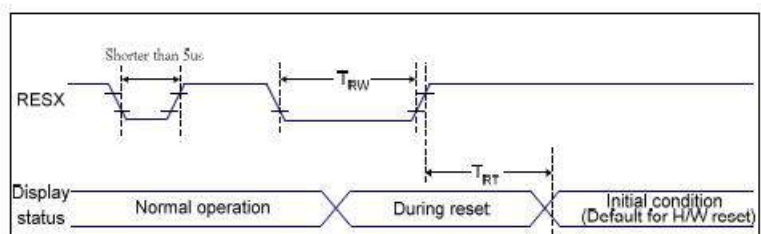


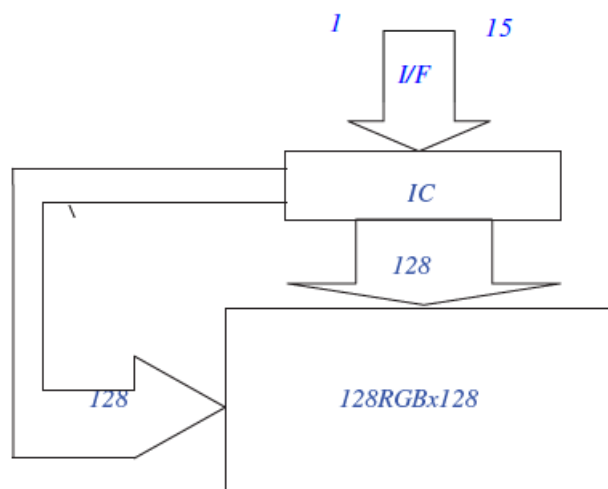
Figure 7 Reset Timing

VDD1=1.65 to 3.3V, VDD=2.4 to 3.3V, AGND=DGND=0V, Ta=-30 ~ 70 °C

Related Pins	Symbol	Parameter	MIN	MAX	Unit
RESX	TRW	Reset pulse duration	10	-	μ s
	TRT	Reset cancel	-	5 (Note 1, 5) 120 (Note 1, 6, 7)	ms

Table 8 Reset Timing

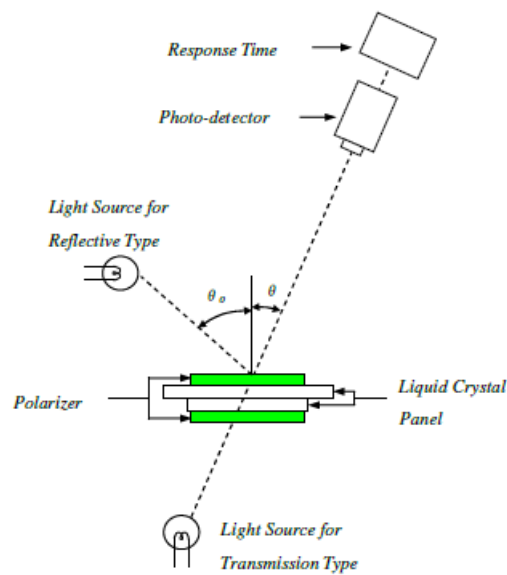
BLOCK DIAGRAM OF LCM



ELECTRO-OPTICAL CHARACTERISTICS

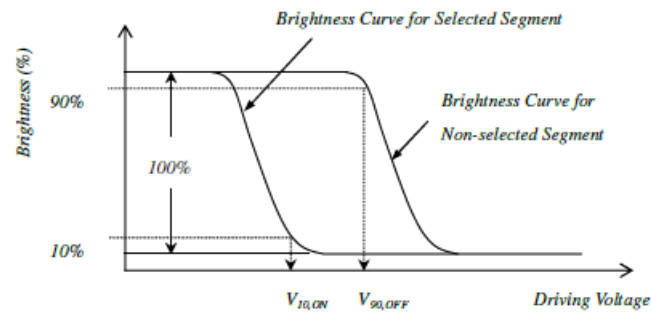
Item	Symbol	Condition	Temp	Min	Typ	Max	Unit s	Note
Operating Volt.	VLCD	$\theta=\psi=0$	-10°C	---	---	---	V	8.1
			25°C	---	8.5	---		
			60°C	---	---	---		
Response Time	Rise Time (Tr)	$\theta=\psi=0$	-10°C	---	---	---	msec	8.2
	Decay Time (Td)			---	---	---		
	Rise Time (Tr)		25°C	---	---	240		
	Decay Time (Td)			---	---	240		
	Rise Time (Tr)		60°C	---	---	---		
	Decay Time (Td)			---	---	---		
Viewing Angle Range	θ	$\psi=0^\circ$	25°C	---	---	30	Deg	8.3 $CR \geq 2$
		$\psi=90^\circ$		---	---	30		
		$\psi=180^\circ$		---	---	30		
		$\psi=270^\circ$		---	---	30		
Contrast Ratio	Cr	$\theta=\psi=0$	25°C	3	4.5	---	—	8.4

ELECTRO-OPTICAL CHARACTERISTICS TEST METHOD

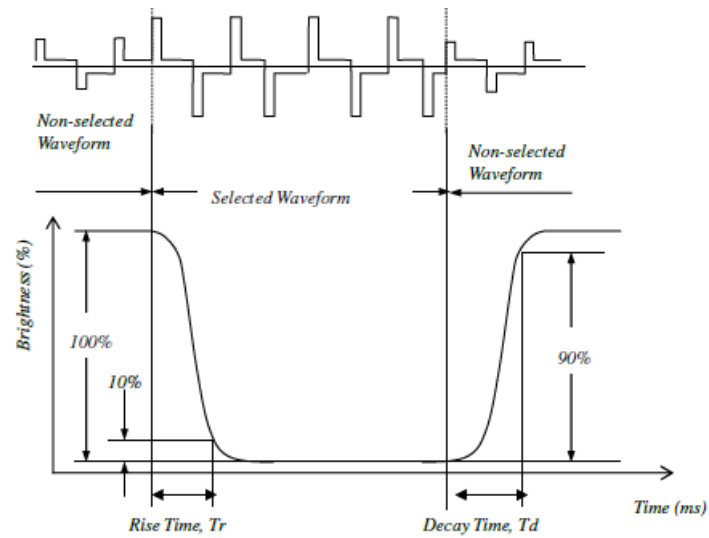


DEFINITION OF OPERATING VOLTAGE, VOP

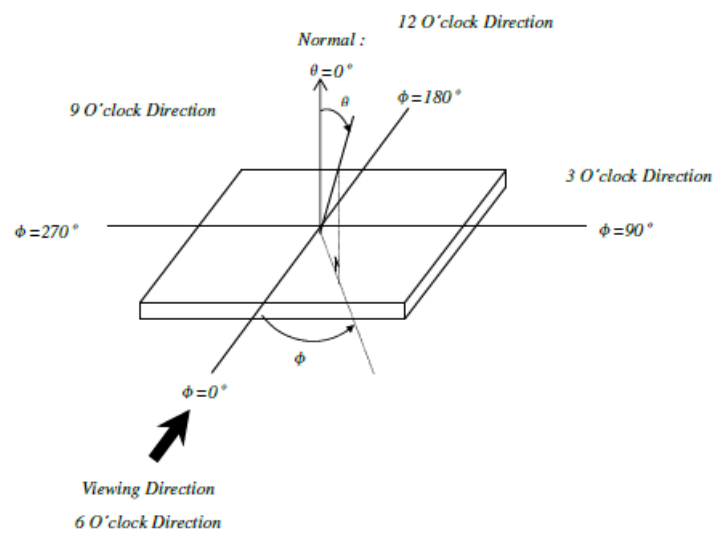
$$V_{op} = (V_{10,ON} + V_{90,OFF})/2$$



DEFINITION OF OPTICAL RESPONSE TIME

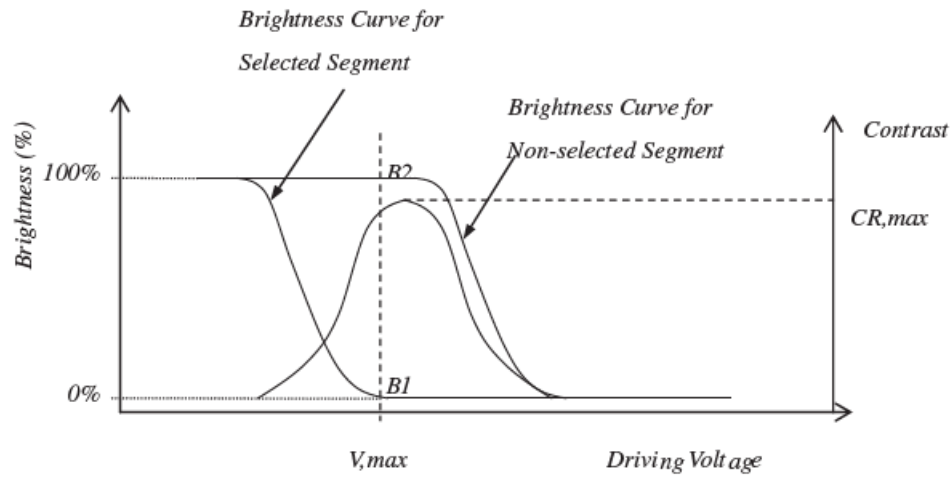


DEFINITION OF VIEWING ANGLE θ AND ϕ



DEFINITION OF CONTRAST RATIO, CR

CR = Brightness of Non-selected Segment (B_2)
 Brightness of Selected Segment (B_1)



INSPECTION CRITERIA

Inspection Conditions

Environmental conditions

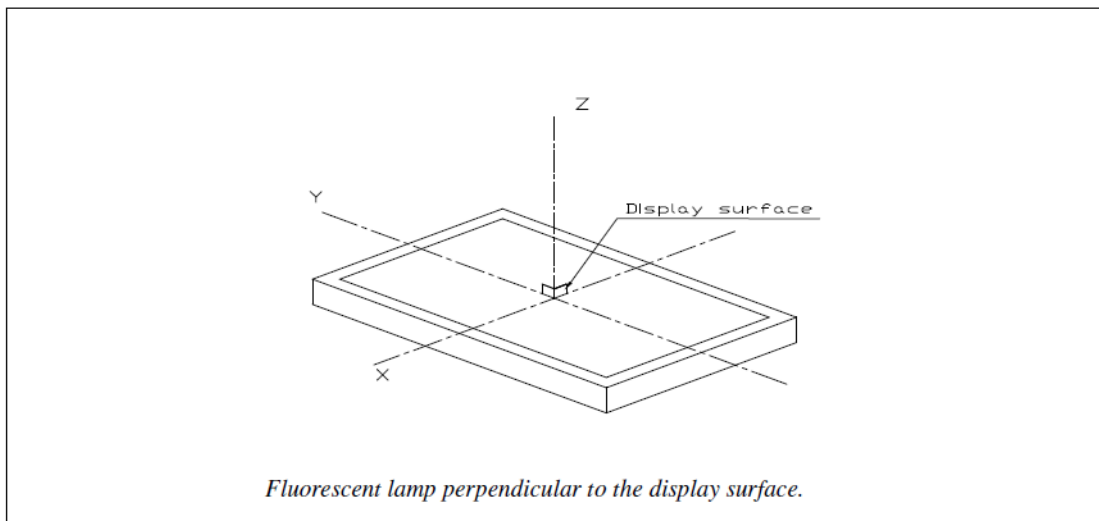
The environmental conditions for inspection shall be as follows

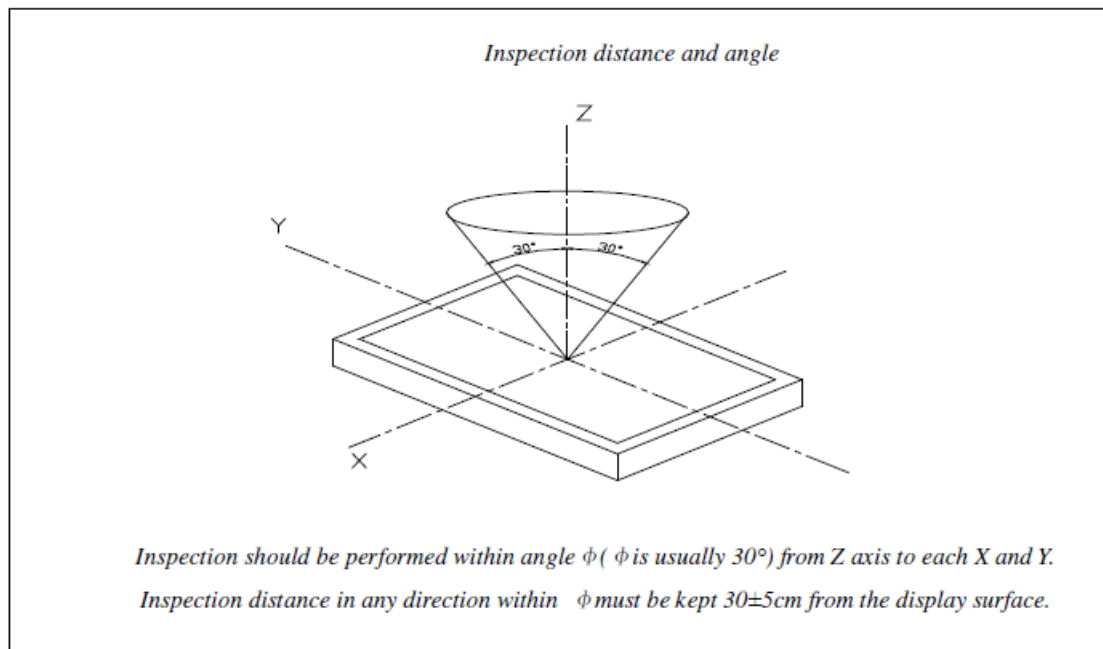
- Room temperature: $20 \pm 3^\circ\text{C}$
- Humidity: $65 \pm 20\% \text{RH}$

The external visual inspection

With a single 20-watt fluorescent lamp as the light source, the inspection was in the distance of 30cm or more from the LCD to the inspector's eyes .

LIGHT METHOD





Classification of defects

Major defect

A major defect refers to a defect that may substantially degrade usability for product applications.

Minor defect

A minor defect refers to a defect which is not considered to be able substantially degrade the product application or a defect that deviates from existing standards almost unrelated to the effective use of the product or its operation.

RELIABILITY

MTBF

The LCD module shall be designed to meet a minimum MTBF value of 50000 hours with normal. (25°C in the room without sunlight)

TESTS

NO.	Test Item	Test condition	Criterion
1	High Temperature Storage	$80^\circ\text{C}\pm 2^\circ\text{C}$ 96H Restore 2H at 25°C Power off	
2	Low Temperature Storage	$-30^\circ\text{C}\pm 2^\circ\text{C}$ 96H Restore 2H at 25°C Power off	

3	High Temperature Operation	70°C±2°C 96H Restore 2H at 25°C Power on	After testing, cosmetic and electrical defects should not happen.
4	Low Temperature Operation	-20°C±2°C 96H Restore 4H at 25°C Power on	
5	High Temperature & Humidity Operation	60°C±2°C 90%RH 96H Power on	
6	Temperature Cycle	-30°C←→25°C←→80°C 30min 5min 30min after 10cycle, Restore 2H at 25°C Power off	
7	Vibration Test	10Hz~150Hz, 100m/s ² , 120 min	
8	Shock Test	Half-sinewave, 300m/s ² , 11ms	
9	Drop Test(package state)	800mm, concrete floor, 1 corner, 3 edges, 6 sides each time	<p>1. After testing, cosmetic and electrical defects should not happen.</p> <p>2. the product should remain at initial place 3. Product uncovered or package broken is not permitted.</p>

PRECAUTIONS FOR USING LCD MODULE

Handling precautions

1. =The display panel is made of glass. Do not subject it to a mechanical shock or impact by dropping it.
2. If the display panel is damaged and the liquid crystal substance leaks out, be sure not to get any in your mouth.
If the substance contacts your skin or clothes, wash it off using soap and water.
3. Do not apply excessive force to the display surface or the adjoining areas since this may cause the color tone to vary.
4. The polarizer covering the display surface of the LCD module is soft and easily scratched. Handle this polarizer carefully.
5. If the display surface becomes contaminated, breathe on the surface and gently wipe it with a soft dry cloth. If it is heavily contaminated, moisten a cloth with one of the following solvents:
 - Isopropyl alcohol
 - Ethyl alcohol(6) Solvents other than those above mentioned may damage the polarizer.
Especially, do not use the following:
 - Water
 - Ketone
 - Aromatic solvents
6. Extra care to minimize corrosion of the electrode. Water droplets, moisture condensation or a current flow in a high-humidity environment accelerates corrosion of the electrode.
7. Install the LCD Module by using the mounting holes. When mounting the LCD Module, make sure it is free of twisting, warping and distortion. In particular, do not forcibly pull or bend the I/O cable or the backlight cable.
8. Do not attempt to disassemble or process the LCD Module.
9. NC terminal should be open. Do not connect anything.
10. If the logic circuit power is off, do not apply the input signals.
11. To prevent destruction of the elements by static electricity, be careful to maintain an optimum work environment.
 - Be sure to ground the body when handling the LCD Module.
 - Tools required for assembling, such as soldering irons, must be properly grounded.
 - To reduce the amount of static electricity generated, do not conduct assembling and other work under dry conditions.
 - The LCD Module is coated with a film to protect the display surface. Exercise care when peeling off this protective film since static electricity may be generated.

STORAGE PRECAUTIONS

When storing The LCD Module, avoid exposure to direct sunlight of fluorescent lamps. Keep the modules in bags (avoid high temperature/ high humidity and low temperatures below 0°C). Whenever possible, the LCD Module should be stored in the same conditions in which they were shipped from our company.

OTHERS

Liquid crystals solidify under low temperature (below the storage temperature range) leading to defective orientation or the generation of air bubbles (black or white). Air bubbles may also be generated if the module is subject to a low temperature.


If the LCD Module have been operating for a long time showing the same display patterns the display patterns may remain on the screen as ghost images and a slight contrast irregularity may also appear. A normal operating status can be recovered by suspending use for some time. It should be noted that this phenomenon does not adversely affect performance reliability.

To minimize the performance degradation of the LCD Module resulting from destruction caused by static electricity etc. exercise care to avoid holding the following sections when handling the modules.

- Exposed area of the printed circuit board.
- Terminal electrode sections.

www.surenno.com

Documents / Resources

	<p>Surenoo STP0144C-128128 Series TFT LCD Panel [pdf] User Manual STP0144C-128128 Series TFT LCD Panel, STP0144C-128128 Series, STP0144C-128128, TF T LCD Panel, TFT Panel, LCD Panel</p>
---	---

References

- DWG [DWG.no - DWG Cad Software | DWG View | DWG til PDF | Norsk nettside som går gjennom nyheter og muligheter innen 2D og 3D design](#)
- [pin.no](#)
- [Surenoo Tech: Professional LCD Module Supplier Since 2005](#)

[Manuals+.](#)