



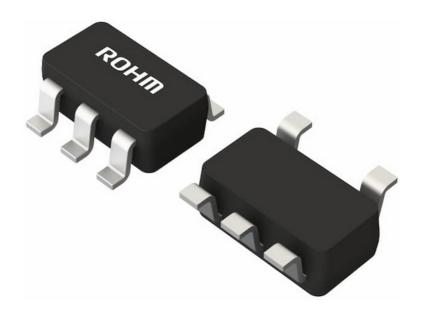
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ROHM TLR728G-LB Noise Rail to Rail Input and Output High Speed CMOS Amplifier



Product Specifications

Product Name: ROHM Solution Simulator

 Features: Low Offset & Low Noise Rail-to-Rail Input/Output High Speed CMOS Operational Amplifiers

Product Usage Instructions

Simulation Schematic

Figure 1 shows the simulation schematic for the frequency response with an Op-Amp as a non-inverting amplifier.

How to Simulate

The simulation settings can be configured in the 'Simulation Settings' menu as shown in Figure 2. Table 1 displays the default setup of the simulation.

Simulation Settings

• Simulation Type: Frequency-Domain

• Start Frequency – End Frequency: 0 Hz – 100 MHz

• Advanced Options: Simulation Resolution, Convergence Assist

• Manual Options: .temp 27

Simulation Conditions

Table 2 lists the simulation condition parameters, including instance name, type, and parameters for voltage sources and the Op-Amp.

Op-Amp Model

Table 3 displays the model pin functions for the Op-Amp simulation, specifying the input/output characteristics.

Peripheral Components

Bill of Materials

Table 4 lists the components used in the simulation schematic, including resistors and capacitors with their default values and variable ranges.

Capacitor Equivalent Circuits

Figure 3 illustrates the capacitor property editor and equivalent circuit, noting that the default value of ESR is 2m.

INTRODUCTION

- This circuit simulates the frequency response with an Op-Amp as a non-inverting amplifier. You can observe the AC gain and phase of the ratio of output to input voltage when the input source voltage AC frequency is changed. You can customize the parameters of the components shown in blue, such as VSOURCE or peripheral components, and simulate the non-inverting amplifier with the desired operating condition.
- In the published application note, you can simulate the circuit: Operational amplifier,
 Comparator (Tutorial). [JP] [EN] [CN] [KR]

General Cautions

- Caution 1: The values from the simulation results are not guaranteed. Please use these results as a guide for your design.
- Caution 2: These model characteristics are specifically at Ta=25°C. Thus, the simulation result with temperature variances may significantly differ from the result with the one done at the actual application board (actual measurement).
- Caution 3: Please refer to the Application Note of Op-Amps for details of the technical information.
- Caution 4: The characteristics may change depending on the actual board design, and ROHM strongly recommends double-checking those characteristics with the

Simulation Schematic

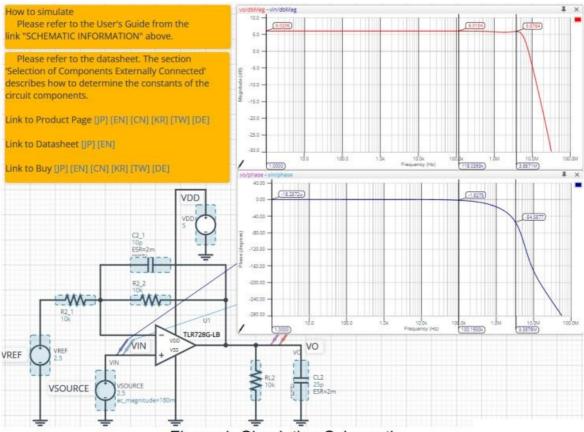


Figure 1. Simulation Schematic

How to simulate

- The simulation settings, such as parameter sweep or convergence options, are configurable from the 'Simulation Settings' shown in Figure 2, and Table 1 shows the default setup of the simulation.
- In case of a simulation convergence issue, you can change the advanced options to solve it. The temperature is set to 27 °C in the default statement in 'Manual Options'.
 You can modify it.

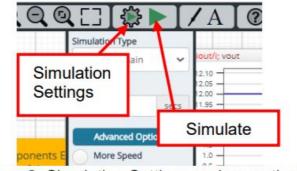


Figure 2. Simulation Settings and execution

Parameters	Default	Note		
Simulation Type	Frequency-Domain	Do not change Simulation Type		
Start Frequency	0 Hz	Simulate the frequency response		
End Frequency	100Meg Hz	for the frequency range from 0 Hz to 100 MHz.		
Advanced option	Simulation Resolution	1e-7		
S	Convergence Assist	_		
Manual Options	.temp 27	_		

Simulation Conditions

Instance Name	Туре	Parameters	Defaul t Valu e	Variable Range		Unit
				Min	Max	s
VSOURC E	Voltage Sour ce	Voltage_level	2.5	0	5.5	V
		AC_magnitude	180m	free		V
		AC_phase	0.0	fixed		0
VDD	Voltage Sour ce for Op-A mp	Voltage_level	5	2.5(No te1)	5.5(N ote1)	V
		AC_magnitude	0.0	fixed		V
		AC_phase	0.0	fixed		0
VREF	Voltage Sour ce	Voltage_level	2.5	VSS	VDD	V
		AC_magnitude	0.0	fixed		V
		AC_phase	0.0	fixed		0

(Note 1) Set it to the guaranteed operating range of the Op-Amps.

Op-Amp model

Table 3 shows the model pin function implemented. Note that the Op-Amp model is the behavioral model for its input/output characteristics, and neither protection circuits nor functions unrelated to the purpose are implemented.

Pin Name	Description
+IN	Non-inverting input
-IN	Inverting input
VDD	Positive power supply
VSS	Negative power supply / Ground
OUT	Output

Peripheral Components

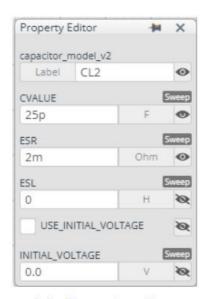
Bill of Materials

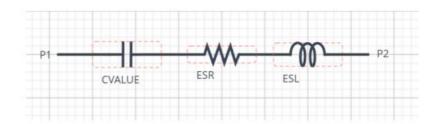
Table 4 shows the list of components used in the simulation schematic. Each of the capacitors has the parameters of the equivalent circuit shown below. The default values of equivalent components are set to zero except for the ESR of C. You can modify the values of each component.

Type	Instance Name	Default Value	Variable Range		Units
			Min	Max	Units
	R2_1	10k	1k	1M	Ω
	R2_2	10k	1k	1M	Ω
Resistor	RL2	10k	1k	1M, N C	Ω

Capacitor	C2_1	10	0.1	100	pF
	CL2	25	free, NC		pF

Capacitor Equivalent Circuits





(a) Property editor

(b) Equivalent circuit

Figure 3. Capacitor property editor and equivalent circuit

The default value of ESR is 2 m Ω .

(Note 2) These parameters can take any positive value or zero in simulation, but it does not guarantee the operation of the IC in any condition. Refer to the datasheet to determine the adequate value of parameters.

Recommended Products

Op-Amp

- TLR728G-LB: Low Offset & Low Noise Rail-to-Rail Input/Output High Speed CMOS Op-Amps. [JP] [EN] [CN] [KR] [TW] [DE]
- Technical Articles and Tools can be found in the Design Resources on the product web page.

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Frequently Asked Questions (FAQ)

- Can I modify the parameters of the components in the simulation?
 Yes, you can customize the parameters of components shown in blue, such as
 VSRCE or peripheral components, to simulate the non-inverting amplifier with desired operating conditions.
- What is the default simulation type?
 The default simulation type is Frequency-Domain to analyze the frequency response from 0 Hz to 100 MHz.

Documents / Resources



ROHM TLR728G-LB Noise Rail to Rail Input and Output High Speed CM
OS Amplifier [pdf] User Guide

TLR728G-LB, TLR728G-LB Noise Rail to Rail Input and Output High Spe ed CMOS Amplifier, Noise Rail to Rail Input and Output High Speed CMOS Amplifier, Input and Output High Speed CMOS Amplifier, High Speed CMOS Amplifier, CMOS Amplifier

References

- User Manual
- ROHM
- CMOS Amplifier, High Speed CMOS Amplifier, Input and Output High Speed CMOS Amplifier, Noise Rail to Rail Input and Output High Speed CMOS Amplifier, ROHM, TLR728G-LB, TLR728G-LB Noise Rail to Rail Input and Output High Speed CMOS Amplifier

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