

# ROHM TLR377YG-C Non Inverting Amplifier Frequency Response Simulation User Guide

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**ROHM TLR377YG-C Non Inverting Amplifier Frequency Response Simulation**

## How to simulate

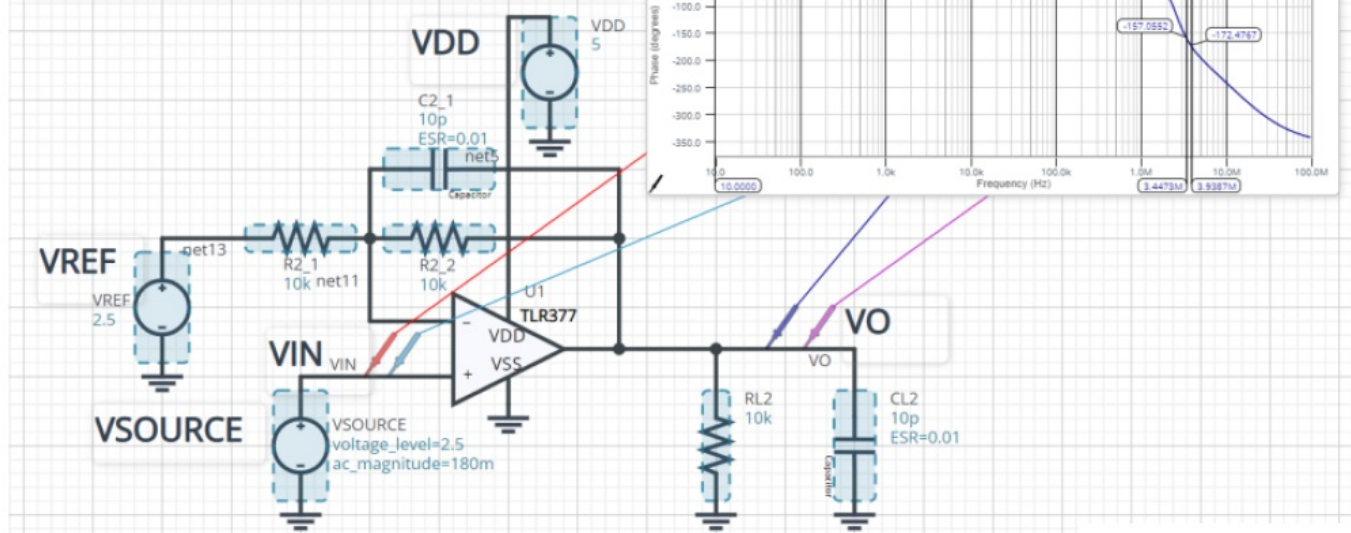
Please refer to the User's Guide from the link "SCHEMATIC INFORMATION" above.

Please refer to the datasheet. The section 'Selection of Components Externally Connected' describes how to determine the constants of the circuit components.

[Link to Product Page](#) [JP] [EN] [CN] [KR] [TW] [DE]

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This circuit simulates the frequency response with Op-Amp as a non-inverting amplifier. You can observe the AC gain and phase of the ratio of output to input voltage when the input source voltage AC frequency is changed. You can customize the parameters of the components shown in blue, such as  $V_{SOURCE}$ , or peripheral components, and simulate the non-inverting amplifier with the desired operating condition. You can simulate the circuit in the published application note: Operational amplifier, Comparator (Tutorial). [JP] [EN] [CN] [KR]

## General Cautions

**Caution 1:** The values from the simulation results are not guaranteed. Please use these results as a guide for your design.

**Caution 2:** These model characteristics are specifically at  $T_a=25^{\circ}\text{C}$ . Thus, the simulation result with temperature variances may significantly differ from the result with the one done at actual application board (actual measurement).

**Caution 3:** Please refer to the Application note of Op-Amps for details of the technical information.

**Caution 4:** The characteristics may change depending on the actual board design and ROHM strongly recommend to double check those characteristics with actual board where the chips will be mounted on.

## Simulation Schematic

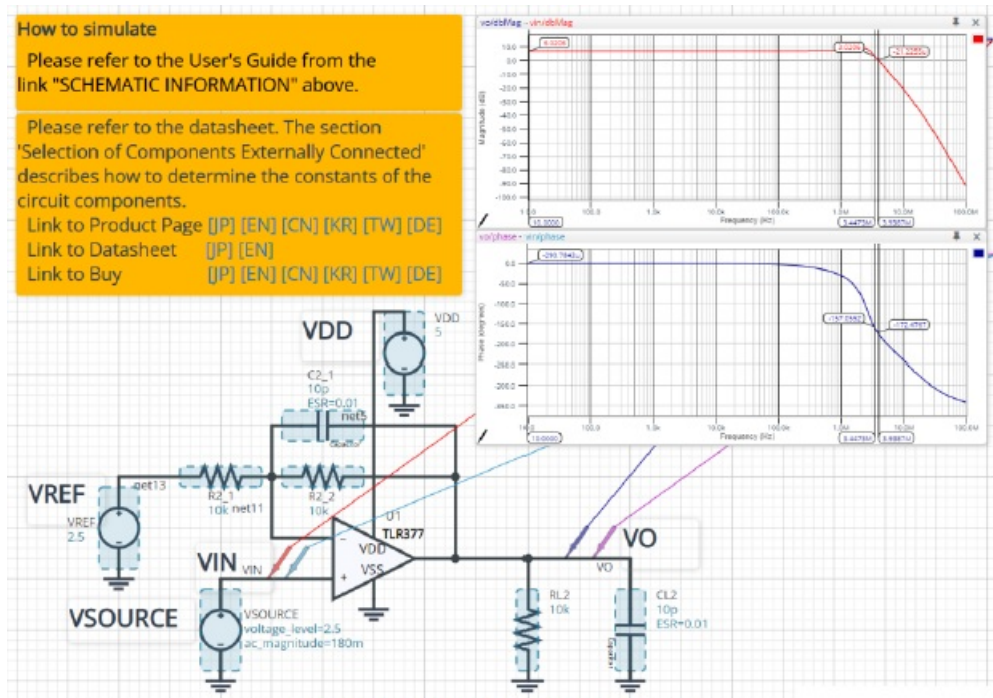


Figure 1. Simulation Schematic

## How to simulate

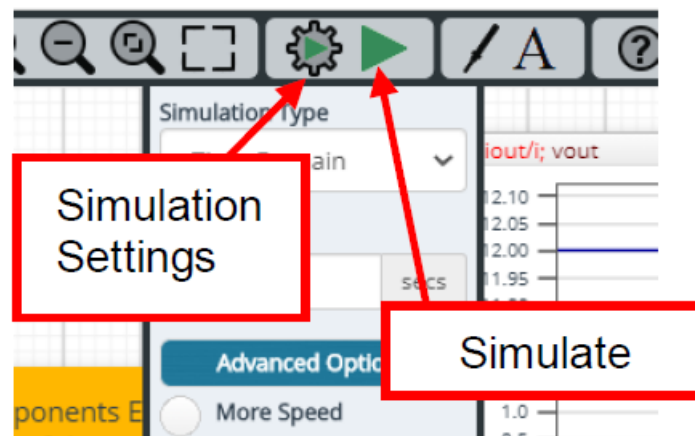


Figure 2. Simulation Settings and execution

The simulation settings, such as parameter sweep or convergence options, are configurable from the 'Simulation Settings' shown in Figure 2, and Table 1 shows the default setup of the simulation. In case of simulation convergence issue, you can change advanced options to solve. The temperature is set to 27 °C in the default statement in 'Manual Options'. You can modify it.

Table 1. Simulation settings default setup

Parameters	Default	Note
Simulation Type	Frequency-Domain	Do not change Simulation Type
Start Frequency	10 Hz	Simulate the frequency response for the frequency range from 10 Hz to 100 MHz.
End Frequency	100Meg Hz	
Advanced options	More Accuracy	-
	Time Resolution Enhancement Convergence Assist	-
Manual Options	.temp 27	-

## Simulation Conditions

Table 2. List of the simulation condition parameters

Instance Name	Type	Parameters	Default Value	Variable Range		Units
				Min	Max	
VSOURCE	Voltage Source	Voltage_level	2.5	0	5.5	V
		AC_magnitude	180m	free		V
		AC_phase	0.0	fixed		°
VDD	Voltage Source for Op-Amp	Voltage_level	5	2.5 <sup>(Note1)</sup>	5.5 <sup>(Note1)</sup>	V
		AC_magnitude	0.0	fixed		V
		AC_phase	0.0	fixed		°
VREF	Voltage Source	Voltage_level	2.5	VSS	VDD	V
		AC_magnitude	0.0	fixed		V
		AC_phase	0.0	fixed		°

(Note 1) Set it to the guaranteed operating range of the Op-Amps.

## Op-Amp model

Table 3 shows the model pin function implemented. Note that the Op-Amp model is the behavior model for its input/output characteristics, and no protection circuits or the functions not related to the purpose are not implemented.

Table 3. Op-Amp model pins used for the simulation

Pin Name	Description
+IN	Non-inverting input
-IN	Inverting input
VDD	Positive power supply
VSS	Negative power supply / Ground
OUT	Output

## Peripheral Components

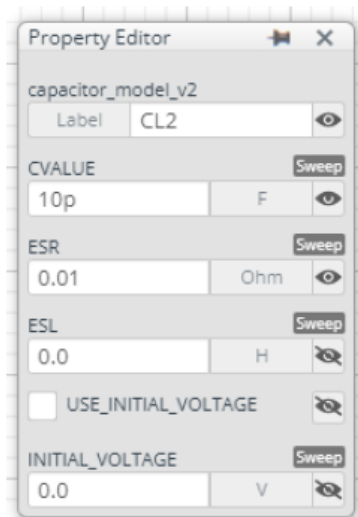
### Bill of Material

Table 4 shows the list of components used in the simulation schematic. Each of the capacitors has the parameters of equivalent circuit shown below. The default values of equivalent components are set to zero except for the ESR of C. You can modify the values of each component.

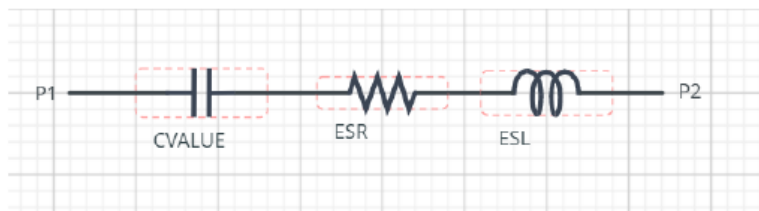
Table 4. List of capacitors used in the simulation circuit

Type	Instance Name	Default Value	Variable Range		Units
			Min	Max	
Resistor	R2_1	10k	1k	1M	Ω
	R2_2	10k	1k	1M	Ω
	RL2	10k	1k	1M, NC	Ω
Capacitor	C2_1	10	10	100	pF
	CL2	10	free, NC		pF

## Capacitor Equivalent Circuits



(a) Property editor



(b) Equivalent circuit

Figure 3. Capacitor property editor and equivalent circuit

The default value of ESR is 0.01  $\Omega$ .

**(Note 2)** These parameters can take any positive value or zero in simulation but it does not guarantee the operation of the IC in any condition. Refer to the datasheet to determine adequate value of parameters.

## Recommended Products

Op-Amp TLR377YG-C : Automotive High Precision & Input/Output Rail-to-Rail CMOS Operational Amplifier. [JP] [EN] [CN] [KR] [TW] [DE] TLR2377YFVM-C : Automotive High Precision & Input/Output Rail-to-Rail CMOS Operational Amplifier (Dual Op-Amp). [JP] [EN] [CN] [KR] [TW] [DE] LMR1802G-LB : Low Noise, Low Input Offset Voltage CMOS Operational Amplifier. [JP] [EN] [CN] [KR] [TW] [DE] Technical Articles and Tools can be found in the Design Resources on the product web page.

## Notes

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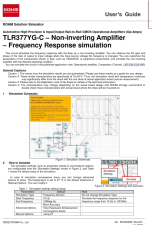
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## Documents / Resources

	<p><a href="#">ROHM TLR377YG-C Non Inverting Amplifier Frequency Response Simulation</a> [pdf] User Guide</p> <p>TLR377YG-C Non Inverting Amplifier Frequency Response Simulation, TLR377YG-C, TLR377YG-C Frequency Response Simulation, Non Inverting Amplifier Frequency Response Simulation, Frequency Response Simulation</p>
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## References

- [ROHM Semiconductor - ROHM Co., Ltd.](#)
- [contactus](#)
- [LMR1802G-LB - | - ROHM Semiconductor](#)
- [TLR2377YFVM-C - | - ROHM Semiconductor](#)
- [TLR377YG-C - | - ROHM Semiconductor](#)
- [LMR1802G-LB - ■■■■■■, ■■■■■■ | ROHM.co.kr](#)
- [TLR2377YFVM-C - ■■■■■■, ■■■■■■ | ROHM.co.kr](#)

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- [!\[\]\(5092e0d5a46a35d887ea84e10efda211\_img.jpg\) LMR1802G-LB - Data Sheet, Product Detail | ROHM.com](#)
- [!\[\]\(222624bfbcd41cb4b8e992f2fb4d1d5d\_img.jpg\) TLR2377YFVM-C - Data Sheet, Product Detail | ROHM.com](#)
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