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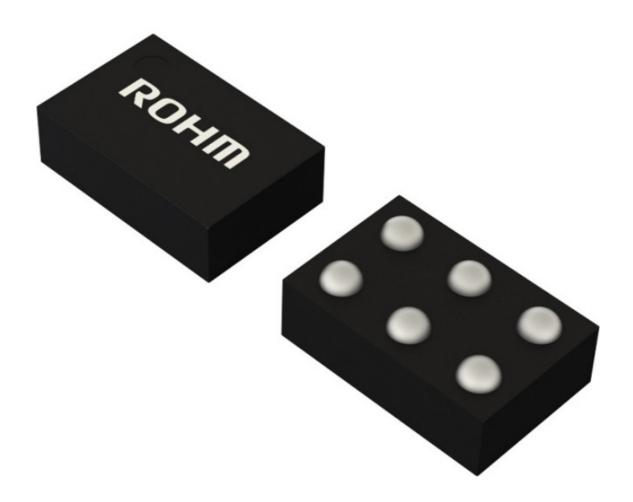
User Guide ™

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**ROHM TLR377GYZ Voltage Follower Pulse Input Transient CMOS Amplifier** 



This circuit simulates the transient response to pulse input with voltage follower configured Op-Amps. You can observe the fluctuation of the output voltage when the input voltage is abruptly changed. You can customize the parameters of the components shown in blue, such as VSOURCE, or peripheral components, and simulate the voltage follower with the desired operating condition.

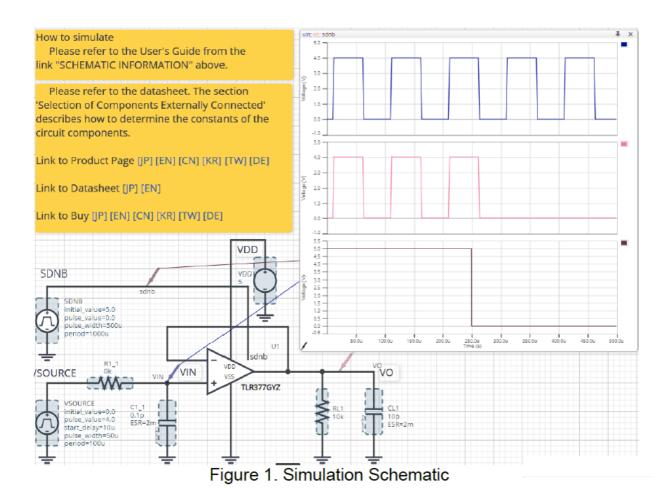
You can simulate the circuit in the published application note: Operational amplifier, Comparator (Tutorial). [JP] [EN] [CN] [KR]

#### **General Cautions**

- 1. Caution 1: The values from the simulation results are not guaranteed. Please use these results as a guide for your design.
- 2. Caution 2: These model characteristics are specifically at Ta=25°C. Thus, the simulation result with temperature variances may significantly differ from the result with the one done at actual application board (actual measurement).
- 3. Caution 3: Please refer to the Application note of Op-Amps for details of the technical information.
- 4. Caution 4: The characteristics may change depending on the actual board design and

ROHM strongly recommend to double check those characteristics with actual board where the chips will be mounted on.

### **Simulation Schematic**



### How to simulate

- The simulation settings, such as parameter sweep or convergence options, are configurable from the 'Simulation Settings' shown in Figure 2, and Table 1 shows the default setup of the simulation.
- In case of simulation convergence issue, you can change advanced options to solve.
   The temperature is set to 27 °C in the default statement in 'Manual Options'. You can modify it.

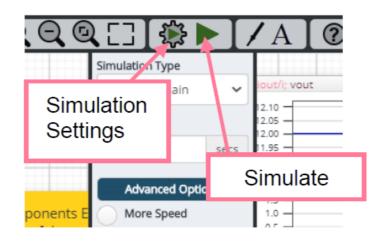


Figure 2. Simulation Settings and execution

Table 1. Simulation settings default setup

Parameters	Default	Note
Simulation Type	Time-Domain	Do not change Simulation Type
End Time	500 μs	_
Advanced option	Balanced	_
	Convergence Assist	_
Manual Options	.temp 27	_

# **Simulation Conditions**

Table 2. List of the simulation condition parameters

Instance Name	Туре	Parameters	Defaul t Valu e	Variable Range		Unit
				Min	Max	S
		Initial_value	0	0	5.5	V
		Pulse_value	4	0	5.5	V
			1		1	

	Voltage Sour ce	ramptime_initial_to_pul	2	free		μѕ
VSOURC E		ramptime_pulse_to_init	2	free		μs
		Start_delay	10	free		μs
		Pulse_width	50	free		μѕ
		Period	100	free		μѕ
	Voltage Sour ce For Op-A mp	Voltage_level	5	1.8 <i>(No te1)</i>	5.5(N ote1)	V
VDD		AC_magnitude	0.0	fixed		V
		AC_phase	0.0	fixed		0
		Initial_value	5	VSS	VDD	V
		Pulse_value	0	VSS	VDD	V
SDNB	Voltage Sour ce For Shutd own Setting	ramptime_initial_to_pul	100	free		ns
		ramptime_pulse_to_init	100	free		ns
		Start_delay	250	free		μѕ
		Pulse_width	500	free		μѕ
		Period	1	1	_	ms

(Note 1) Set it to the guaranteed operating range of the Op-Amps.

# VSOURCE parameter setup

Figure 3 shows how the VSOURCE parameters correspond to the VIN stimulus

waveform.

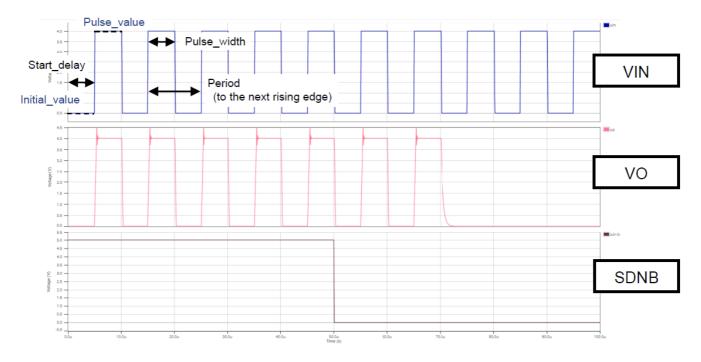


Figure 3. VSOURCE parameters and its waveform

# **Op-Amp model**

Table 3 shows the model pin function implemented. Note that the Op-Amp model is the behavioral model for its input/output characteristics, and neither protection circuits nor functions unrelated to the purpose are implemented.

Table 3. Op-Amp model pins used for the simulation

Pin Name	Description
+IN	Non-inverting input
-IN	Inverting input
VDD	Positive power supply
VSS	Negative power supply / Ground
OUT	Output
SDNB	Shutdown setting

# **Peripheral Components**

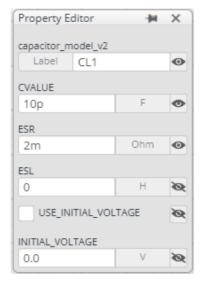
#### **Bill of Material**

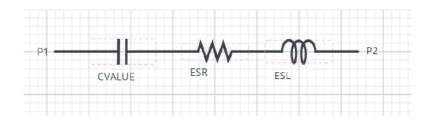
Table 4 shows the list of components used in the simulation schematic. Each of the capacitors has the parameters of equivalent circuit shown below. The default values of equivalent components are set to zero except for the ESR of C. You can modify the values of each component.

Table 4. List of capacitors used in the simulation circuit

Туре	Instance Name	Default Value	Variable Range		Units
			Min	Max	Units
Resistor	R1_1	0	0	10	kΩ
	RL1	10k	1k	1M, N	Ω
Capacitor	C1_1	0.1	0.1	22	pF
	CL1	10	free, NC		pF

# **Capacitor Equivalent Circuits**





(a) Property editor (b) Equivalent circuit Figure 4. Capacitor property editor and equivalent circuit

The default value of ESR is  $2m \Omega$ .

(Note 2) These parameters can take any positive value or zero in simulation but it does not guarantee the operation of the IC in any condition. Refer to the datasheet to determine adequate value of parameters.

#### **Recommended Products**

## **Op-Amp**

**TLR377GYZ**: Ultra Small Package & High Precision Rail-to-Rail Input/Output CMOS Operational Amplifier. [JP] [EN] [CN] [KR] [TW] [DE]

Technical Articles and Tools can be found in the Design Resources on the product web page.

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### **FAQ**

## Q: Can I modify the values of the components in the simulation circuit?

A: Yes, you can customize the parameters of components shown in blue or modify the values of each component in the simulation.

## Q: What is the purpose of the Op-Amp model in the simulation?

A: The Op-Amp model is a behavioral model for its input/output characteristics, ensuring accurate simulation results.

# **Documents / Resources**



ROHM TLR377GYZ Voltage Follower Pulse Input Transient CMOS Amplif ier [pdf] User Guide

TLR377GYZ, TLR377GYZ Voltage Follower Pulse Input Transient CMOS Amplifier, Voltage Follower Pulse Input Transient CMOS Amplifier, Pulse I nput Transient CMOS Amplifier, Transient CMOS Amplifier, CMOS Amplifier

### References

- User Manual
- ROHM
- ▶ CMOS Amplifier, Pulse Input Transient CMOS Amplifier, ROHM, TLR377GYZ, TLR377GYZ Voltage Follower Pulse Input Transient CMOS Amplifier, Voltage Follower Pulse Input Transient CMOS Amplifier

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