




[Home](#) » [ROHM](#) » **ROHM TLR377GYZ Non Inverting Pulse Input Transient Response Simulation**  
**Amplifier User Guide** 

## **ROHM TLR377GYZ Non Inverting Pulse Input Transient Response Simulation**

### **Amplifier User Guide**

This circuit simulates the transient response to pulse input with non-inverting amplifier configured Op Amps. You can observe the fluctuation of the output voltage when the input voltage is abruptly changed. You can customize the parameters of the components shown in blue, such as VSOURCE, or peripheral components, and simulate the non-inverting amplifier with the desired operating condition.

You can simulate the circuit in the published application note: Operational amplifier, Comparator (Tutorial). [\[JP\]](#) [\[EN\]](#) [\[CN\]](#) [\[KR\]](#)

#### **Contents** [ [hide](#) ]

- [1 General Cautions](#)
- [2 Simulation Schematic](#)
- [3 How to simulate](#)
- [4 Simulation Conditions](#)
- [5 VSOURCE parameter setup](#)
- [6 Op-Amp model](#)
- [7 Peripheral Components](#)
- [8 Capacitor Equivalent Circuits](#)
- [9 Recommended Products](#)
- [10 Documents / Resources](#)
  - [10.1 References](#)

## **General Cautions**

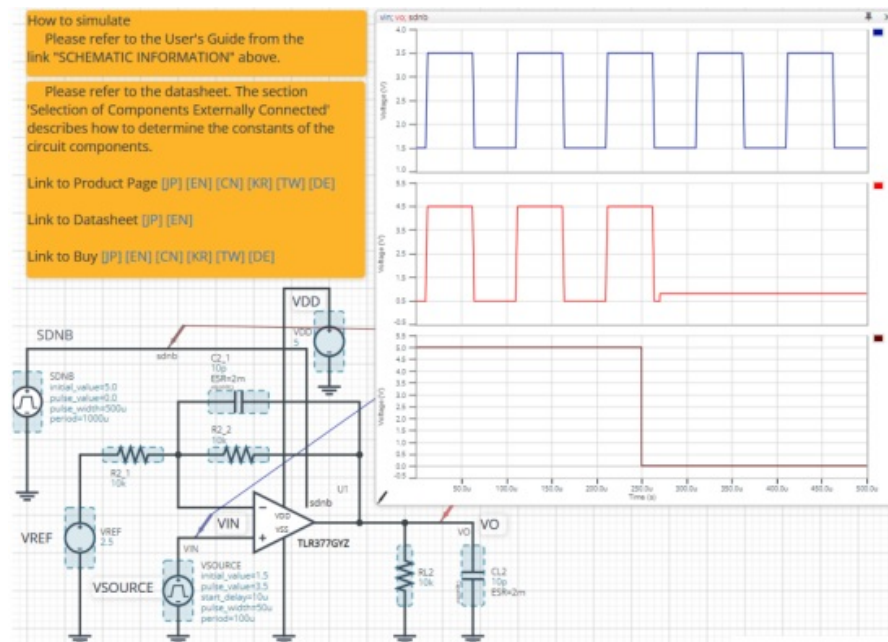
**Caution 1:** The values from the simulation results are not guaranteed. Please use these results as a guide for your design.

**Caution 2:** These model characteristics are specifically at  $T_a=25^{\circ}\text{C}$ . Thus, the simulation result with temperature variances may significantly differ from the result with the one done at actual application board (actual measurement).

**Caution 3:** Please refer to the Application note of Op-Amps for details of the technical information.

**Caution 4:** The characteristics may change depending on the actual board design and ROHM strongly recommend to double check those characteristics with actual board where the chips will be mounted on.

## Simulation Schematic

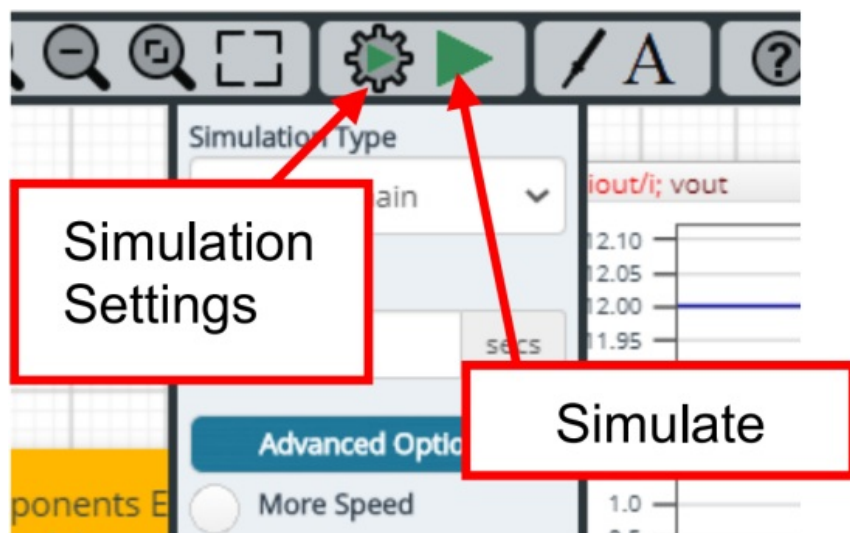


**Figure 1. Simulation Schematic**

## How to simulate

The simulation settings, such as parameter sweep or convergence options, are configurable from the 'Simulation Settings' shown in Figure 2, and Table 1 shows the default setup of the simulation.

In case of simulation convergence issue, you can change advanced options to solve. The temperature is set to  $27^{\circ}\text{C}$  in the default statement in 'Manual Options'. You can modify it.



**Figure 2. Simulation Settings and execution**

**Table 1. Simulation settings default setup**

Parameters	Default	Note
Simulation Type	Time-Domain	Do not change Simulation Type
End Time	500 $\mu$ s	–
Advanced options	Balanced	–
	Convergence Assist	–
Manual Options	.temp 27	–

## Simulation Conditions

**Table 2. List of the simulation condition parameters**

Instance Name	Type	Parameters	Default Value	Variable Range		Units
				Min	Max	
		Initial_value	1.5	0	5.5	V

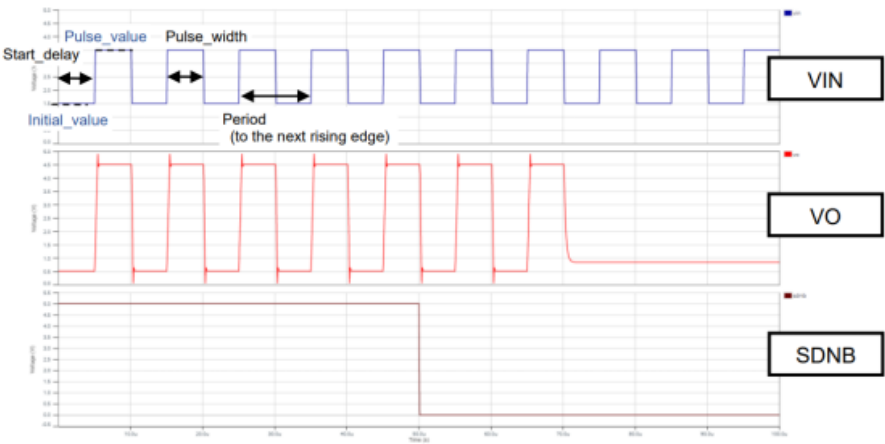
VSOURCE	Voltage Source	Pulse_value	3.5	0	5.5	V
		ramptime_initial_to_pulse	2	free		μs
		ramptime_pulse_to_initial	2	free		μs
		Start_delay	10u	free		μs
		Pulse_width	50	free		μs
		Period	100	free		μs
VDD	Voltage Source For Op-Amp	Voltage_level	5	1.8(Notes1)	5.5(Notes1)	V
		AC_magnitude	0.0	fixed		V
		AC_phase	0.0	fixed		°
VREF	Voltage Source	Voltage_level	2.5	VSS	VDD	V
		AC_magnitude	0.0	fixed		V
		AC_phase	0.0	fixed		°
SDNB	Voltage Source For Shutdown Setting	Initial_value	5	VSS	VDD	V
		Pulse_value	0	VSS	VDD	V
		ramptime_initial_to_pulse	100	free		ns
		ramptime_pulse_to_initial	100	free		ns
		Start_delay	250	free		μs

		Pulse_width	500	free		$\mu$ s
		Period	1	1	–	ms

**(Note 1)** Set it to the guaranteed operating range of the Op-Amps.

### VSOURCE parameter setup

Figure 3 shows how the VSOURCE parameters correspond to the VIN stimulus waveform.



**Figure 3. VSOURCE parameters and its waveform**

### Op-Amp model

Table 3 shows the model pin function implemented. Note that the Op-Amp model is the behavioural model for its input/output characteristics, and neither protection circuits nor functions unrelated to the purpose are implemented.

**Table 3. Op-Amp model pins used for the simulation**

Pin Name	Description
+IN	Non-inverting input
-IN	Inverting input
VDD	Positive power supply

VSS	Negative power supply / Ground
OUT	Output
SDNB	Shutdown setting

## Peripheral Components

### Bill of Material

Table 4 shows the list of components used in the simulation schematic. Each of the capacitors has the parameters of equivalent circuit shown below. The default values of equivalent components are set to zero except for the ESR of **C**. You can modify the values of each component.

**Table 4. List of capacitors used in the simulation circuit**

Type	Instance Name	Default Value	Variable Range		Units
			Min	Max	
Resistor	R2_1	10k	1k	1M	$\Omega$
	R2_2	10k	1k	1M	$\Omega$
	RL2	10k	1k	1M, NC	$\Omega$
Capacitor	C2_1	10	10	100	pF
	CL2	10	free, NC		pF

## Capacitor Equivalent Circuits

(a) Property editor

Property Editor

capacitor\_model\_v2

Label: CL2

CVALUE: 10p F

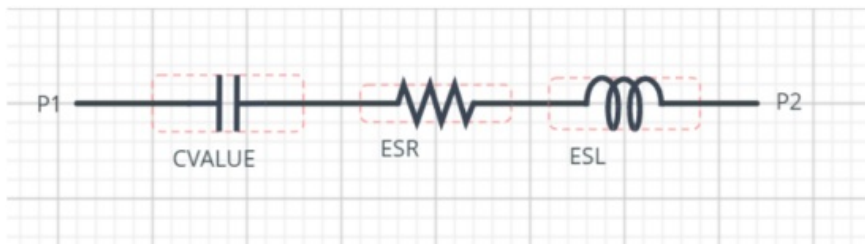
ESR: 2m Ohm

ESL: 0 H

☐ USE\_INITIAL\_VOLTAGE

INITIAL\_VOLTAGE: 0.0 V

(b) Equivalent circuit



**Figure 4.** Capacitor property editor and equivalent circuit

The default value of ESR is 2m  $\Omega$ .

**(Note 2)** These parameters can take any positive value or zero in simulation but it does not guarantee the operation of the IC in any condition. Refer to the datasheet to determine adequate value of parameters.

## Recommended Products

### Op-Amp

**TLR377GYZ** : Ultra Small Package & High Precision Rail-to-Rail Input/Output CMOS Operational Amplifier. [\[JP\]](#) [\[EN\]](#) [\[CN\]](#) [\[KR\]](#) [\[TW\]](#) [\[DE\]](#)

Technical Articles and Tools can be found in the Design Resources on the product web page.

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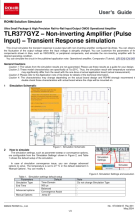
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## **Documents / Resources**



## [ROHM TLR377GYZ Non Inverting Pulse Input Transient Response Simulation Amplifier \[pdf\]](#) User Guide

TLR377GYZ, TLR377GYZ Non Inverting Pulse Input Transient Response Simulation Amplifier, Non Inverting Pulse Input Transient Response Simulation Amplifier, Input Transient Response Simulation Amplifier, Transient Response Simulation Amplifier, Response Simulation Amplifier, Amplifier

## References

- [User Manual](#)

ROHM

Amplifier, Input Transient Response Simulation Amplifier, Non Inverting Pulse Input Transient Response Simulation Amplifier, Response Simulation Amplifier, ROHM, TLR377GYZ, TLR377GYZ Non Inverting Pulse Input Transient Response Simulation Amplifier, Transient Response Simulation Amplifier

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