



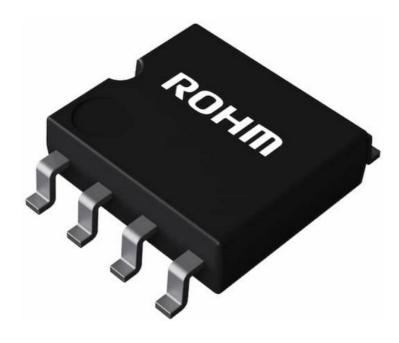
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ROHM LMR1001YF-C Non-Inverting Amplifier



Specifications

• Product Name: ROHM Solution Simulator

 Type: Automotive Zero Drift Low Offset Voltage Rail-to-Rail Input/Output CMOS Operational Amplifier

Product Usage Instructions

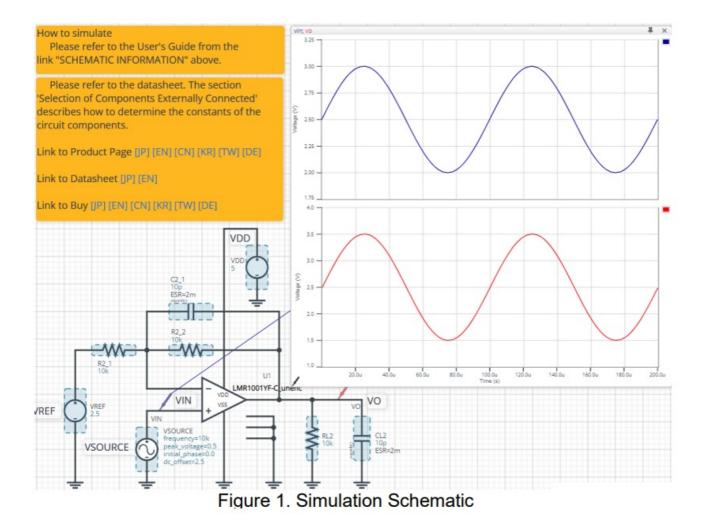
This circuit simulates the transient response to sine wave input with non-inverting amplifier configured Op-Amps. You can observe the output voltage and how faithfully the sine wave input voltage is reproduced. You can customize the parameters of the components shown in blue, such as VSOURCE, or peripheral components, and simulate the non-inverting amplifier with the desired operating condition.

You can simulate the circuit in the published application note: Operational amplifier, Comparator (Tutorial). [JP] [EN] [CN] [KR]

General Cautions

- Caution 1: The values from the simulation results are not guaranteed. Please use these results as a guide for your design.
- Caution 2: These model characteristics are specifically at Ta=25°C. Thus, the simulation result with temperature variances may significantly differ from the result with the one done at the actual application board (actual measurement).
- Caution 3: Please refer to the Application Note of Op-Amps for details of the technical information.
- Caution 4: The characteristics may change depending on the actual board design and ROHM strongly recommend to double check those characteristics with actual board where the chips will be mounted on.

Simulation Schematic



How to simulate

The simulation settings, such as parameter sweep or convergence options, are configurable from the 'Simulation Settings' shown in Figure 2, and Table 1 shows the default setup of the simulation. In case of a simulation convergence issue, you can change the advanced options to solve it. The temperature is set to 27 °C in the default statement in 'Manual Options'. You can modify it.

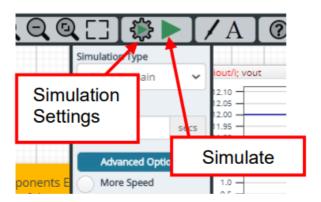


Figure 2. Simulation Settings and execution

 Table 1. Simulation settings default setup

Parameters	Default	Note
Simulation Type	Time-Domain	Do not change Simulation Type
End Time	200 us	_
Advanced option	Balanced	_
S	Convergence Assist	_
Manual Options	.temp 27	_

Simulation Conditions

Table 2. List of the simulation condition parameters

Instance Name	Туре	Parameters	Defaul t Valu e	Variable Min	Range Max	Unit
		Frequency	10k	10	10M	Hz
		Peak_voltage	0.5	0	5.5	V
		Initial_phase	0	free		0
		DC_offset	2.5	0	5.5	V
VSOURC	Voltage Sour	DF	0.0	fixed		1/s
E	ce	AC_magnitude	0.0	fixed		V
		AC_phase	0.0	fixed		0
	Voltage Sour	Voltage_level	5	2.7(No te1)	5.5(N ote1)	V
VDD	ce For Op-A	AC_magnitude	0.0	fixed		V

		AC_phase	0.0	fixed		0
		Voltage_level	2.5	VSS	VDD	V
VREF	Voltage Sour	AC_magnitude	0.0	fixed		V
		AC_phase	0.0	fixed		0

(Note 1) Set it to the guaranteed operating range of the Op-Amps.

VSOURCE parameter setup

Figure 3 shows how the VSOURCE parameters correspond to the VIN stimulus waveform.

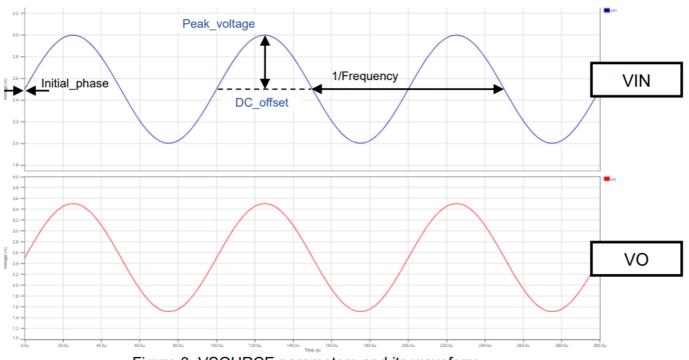


Figure 3. VSOURCE parameters and its waveform

Op-Amp model

Table 3 shows the model pin function implemented. Note that the Op-Amp model is the behavioural model for its input/output characteristics, and neither protection circuits nor functions unrelated to the purpose are implemented.

Table 3. Op-Amp model pins used for the simulation

Pin Name	Description
+IN	Non-inverting input
-IN	Inverting input
VDD	Positive power supply
VSS	Negative power supply / Ground
OUT	Output
NC1	No connection inside
NC2	No connection inside
NC3	No connection inside

Peripheral Components

Bill ofMaterials

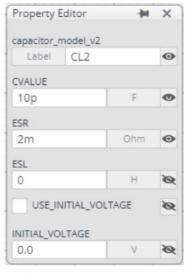
Table 4 shows the list of components used in the simulation schematic. Each of the capacitors has the parameters of equivalent circuit shown below. The default values of equivalent components are set to zero except for the ESR of C. You can modify the values of each component.

Table 4. List of capacitors used in the simulation circuit

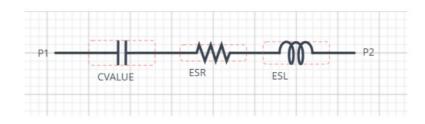
Туре	Instance Name	Default Value	Variable Range		Units
			Min	Max	Office
Resistor	R2_1	10k	1k	1M	Ω
	R2_2	10k	1k	1M	Ω
	RL2	10k	1k	1M, N	Ω
		1	1	1	1

Capacitor	C2_1	10	0.1	100	pF
	CL2	10	free, NC		pF

Capacitor Equivalent Circuits







(b) Equivalent circuit

The default value of ESR is $2 \text{ m}\omega$.

(Note 2) These parameters can take any positive value or zero in simulation, but it does not guarantee the operation of the IC in any condition. Refer to the datasheet to determine the adequate value of parameters.

Recommended Products

Op-Amp

- LMR1001YF-C: Automotive Zero Drift Low Offset Voltage Rail-to-Rail I/O CMOS Op-Amp. [JP] [EN] [CN] [KR] [TW] [DE]
- LMR1001YG-C: Automotive Zero Drift Low Offset Voltage Rail-to-Rail I/O CMOS Op-Amp. [JP] [EN] [CN] [KR] [TW] [DE]
- LMR1002F-LB: Automotive Zero Drift Low Offset Voltage Rail-to-Rail I/O CMOS Op-Amp. [JP] [EN] [CN] [KR] [TW] [DE]

Technical Articles and Tools can be found in the Design Resources on the product web page.

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Frequently Asked Questions

Q: Can I change the simulation type in the settings?

A: It is recommended not to change the Simulation Type from the default Time-Domain setting.

Q: What is the purpose of the Op-Amp model pins NC1, NC2, and NC3?

A: These pins signify no connection inside the Op-Amp model and are not used for any functions within the simulation.

Documents / Resources



ROHM LMR1001YF-C Non Inverting Amplifier [pdf] User Guide

LMR1001YF-C, LMR1001YF-C Non Inverting Amplifier, LMR1001YF-C, Non Inverting Amplifier, Inverting Amplifier, Amplifier

References

- User Manual
- ROHM
- ♠ Amplifier, inverting Amplifier, LMR1001YF-C, LMR1001YF-C Non Inverting Amplifier, Non-inverting Amplifier, BOHM

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