



Home » ROHM » ROHM BD7285FV-LB Non Inverting Amplifier User Guide 📆

Contents [hide]

- 1 ROHM BD7285FV-LB Non Inverting Amplifier
- 2 Specifications
- 3 Simulation Schematic
- 4 How to simulate
- 5 Simulation Conditions
- 6 Op-Amp model
- 7 Peripheral Components
- 8 Recommended Products
- 9 Notice
- 10 FAQ
- 11 Documents / Resources
 - 11.1 References



ROHM BD7285FV-LB Non Inverting Amplifier



Specifications

Product Name: ROHM Solution Simulator

 Description: Low Noise Rail-to-Rail Input/Output High Speed CMOS Operational Amplifier

This circuit simulates the transient response to a sine wave input with a non-inverting amplifier configured with Op-Amps. You can observe the output voltage and how faithfully the sine wave input voltage is reproduced. You can customize the parameters of the components shown in blue, such as VSOURCE or peripheral components, and simulate the non-inverting amplifier with the desired operating condition. You can simulate the circuit in the published application note: Operational amplifier, Comparator (Tutorial).

General Cautions

- Caution 1: The values from the simulation results are not guaranteed. Please use these results as a guide for your design.
- Caution 2: These model characteristics are specifically at Ta=25°C. Thus, the simulation result with temperature variances may significantly differ from the result with the one done at actual application board (actual measurement).
- Caution 3: Please refer to the Application note of Op-Amps for details of the technical information.
- Caution 4: The characteristics may change depending on the actual board design,

and ROHM strongly recommends to double check those characteristics with actual board where the chips will be mounted on.

Simulation Schematic

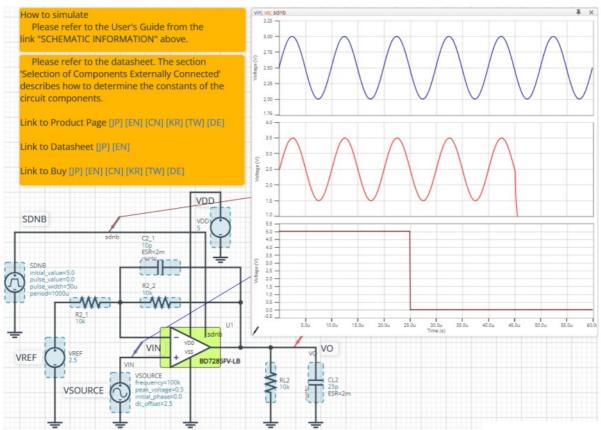


Figure 1. Simulation Schematic

How to simulate

The simulation settings, such as parameter sweep or convergence options, are configurable from the 'Simulation Settings' shown in Figure 2, and Table 1 shows the default setup of the simulation. In case of a simulation convergence issue, you can change the advanced options to solve. The temperature is set to 27 °C in the default statement in 'Manual Options'. You can modify it.

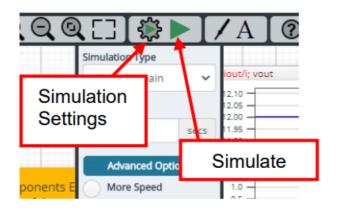


Figure 2. Simulation Settings and execution

Table 1. Simulation settings default setup

Parameters	Default	Note
Simulation Type	Time-Domain	Do not change Simulation Type
End Time	60 μs	_
Advanced option	Simulation Resolution	1e-7
S	_	_
Manual Options	.temp 27	_

Simulation Conditions

Table 2. List of the simulation condition parameters

Instance Name	_	Parameters	Defaul t Valu e	Variable Range		Unit
	lype			Min	Max	S
		Frequency	100k	10	10M	Hz
		Peak_voltage	0.5	0	5.5	V
		Initial_phase	0	free		0
			1	1		

	Voltage Sour	DC_offset	2.5	0	5.5	V
VSOURC E		DF	0.0	fixed		1/s
_		AC_magnitude	0.0	fixed		V
		AC_phase	0.0	fixed		0
	Voltage Sour	Voltage_level	5	2.5(No te1)	5.5(N ote1)	V
VDD	ce For Op-A	AC_magnitude	0.0	fixed		V
	·	AC_phase	0.0	fixed		0
		Voltage_level	2.5	VSS	VDD	V
VREF	Voltage Sour	AC_magnitude	0.0	fixed		V
	ce	AC_phase	0.0	fixed		o
		Initial_value	5	VSS	VDD	V
		Pulse_value	0	VSS	VDD	V
ce For S		ramptime_initial_to_pul	100	free		ns
	Voltage Sour ce For Shutd own Setting	ramptime_pulse_to_init	100	free		ns
		Start_delay	25	free		μs
		Pulse_width	50	free		μs
		Period	1	1	_	ms

Note 1
Set it to the guaranteed operating range of the Op-Amps.

VSOURCE parameter setup

Figure 3 shows how the VSOURCE parameters correspond to the VIN stimulus waveform.

Op-Amp model

Table 3 shows the model pin function implemented. Note that the Op-Amp model is the behavioral model for its input/output characteristics, and neither protection circuits nor functions unrelated to the purpose are implemented.

Table 3. Op-Amp model pins used for the simulation

Pin Name	Description
+IN	Non-inverting input
-IN	Inverting input
VDD	Positive power supply
VSS	Negative power supply / Ground
OUT	Output
SDNB	Shutdown setting

Peripheral Components

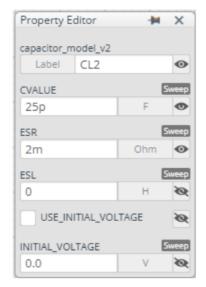
Bill of Materials

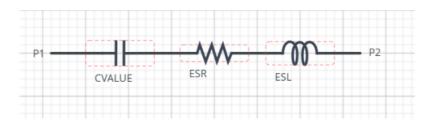
Table 4 shows the list of components used in the simulation schematic. Each of the capacitors has the parameters of equivalent circuit shown below. The default values of equivalent components are set to zero except for the ESR of C. You can modify the values of each component.

Table 4. List of capacitors used in the simulation circuit

Туре	Instance Name	Default Value	Variable Range		Units
	instance name		Min	Max	Offics
	R2_1	10k	1k	1M	Ω
Resistor	R2_2	10k	1k	1M	Ω
	RL2	10k	1k	1M, N C	Ω
Capacitor	C2_1	10	0.1	100	pF
Capacitor	CL2	25	free, NC		pF

Capacitor Equivalent Circuits





(a) Property editor (b) Equivalent circuit Figure 4. Capacitor property editor and equivalent circuit

The default value of ESR is $2m \Omega$.

Note 2

These parameters can take any positive value or zero in simulation but it does not guarantee the operation of the IC in any condition. Refer to the datasheet to determine adequate value of parameters.

Recommended Products

Op-Amp

BD7285FV-LB: Low Noise Rail-to-Rail Input/Output High Speed CMOS Operational Amplifier. Technical Articles and Tools can be found in the Design Resources on the product web page.

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FAQ

Q: Can I modify the default values of equivalent components?

A: Yes, you can modify the values of each component except for the ESR of C.

Documents / Resources



ROHM BD7285FV-LB Non Inverting Amplifier [pdf] User Guide BD7285FV-LB, BD7285FV-LB Non Inverting Amplifier, BD7285FV-LB, Non Inverting Amplifier, Inverting Amplifier, Amplifier



ROHM BD7285FV-LB Non Inverting Amplifier [pdf] User Guide BD7285FV-LB, BD7285FV-LB Non Inverting Amplifier, BD7285FV-LB, Non Inverting Amplifier, Inverting Amplifier, Amplifier

References

- User Manual
- ROHM

Name

Email

♠ Amplifier, BD7285FV-LB, BD7285FV-LB Non Inverting Amplifier, inverting Amplifier, Non-inverting Amplifier, ROHM

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