



# QUECTEL BC260Y Wireless Communication Module User Manual

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EPAT User manual

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## BC260Y Wireless Communication Module



## Foreword

### 1.1 Suitable Condition

Tools	Manufacturer Revision	Applicable Module Type
EPAT	AT+CGMI/EC616/EC616s/QCX212	BC28-F/BC95-GF/ BC260Y/BC300Y B C660K

### 1.2 (Related to Download

#### EPAT

[https://quectel123-my.sharepoint.cn/:u:/g/personal/aefae\\_dom\\_quectel\\_com\\_cn/EcMuW3gkJB1JoqtquZS0\\_wgB4PoOx-DSevqXo1kDrk8L0g?e=6avsvH](https://quectel123-my.sharepoint.cn/:u:/g/personal/aefae_dom_quectel_com_cn/EcMuW3gkJB1JoqtquZS0_wgB4PoOx-DSevqXo1kDrk8L0g?e=6avsvH)

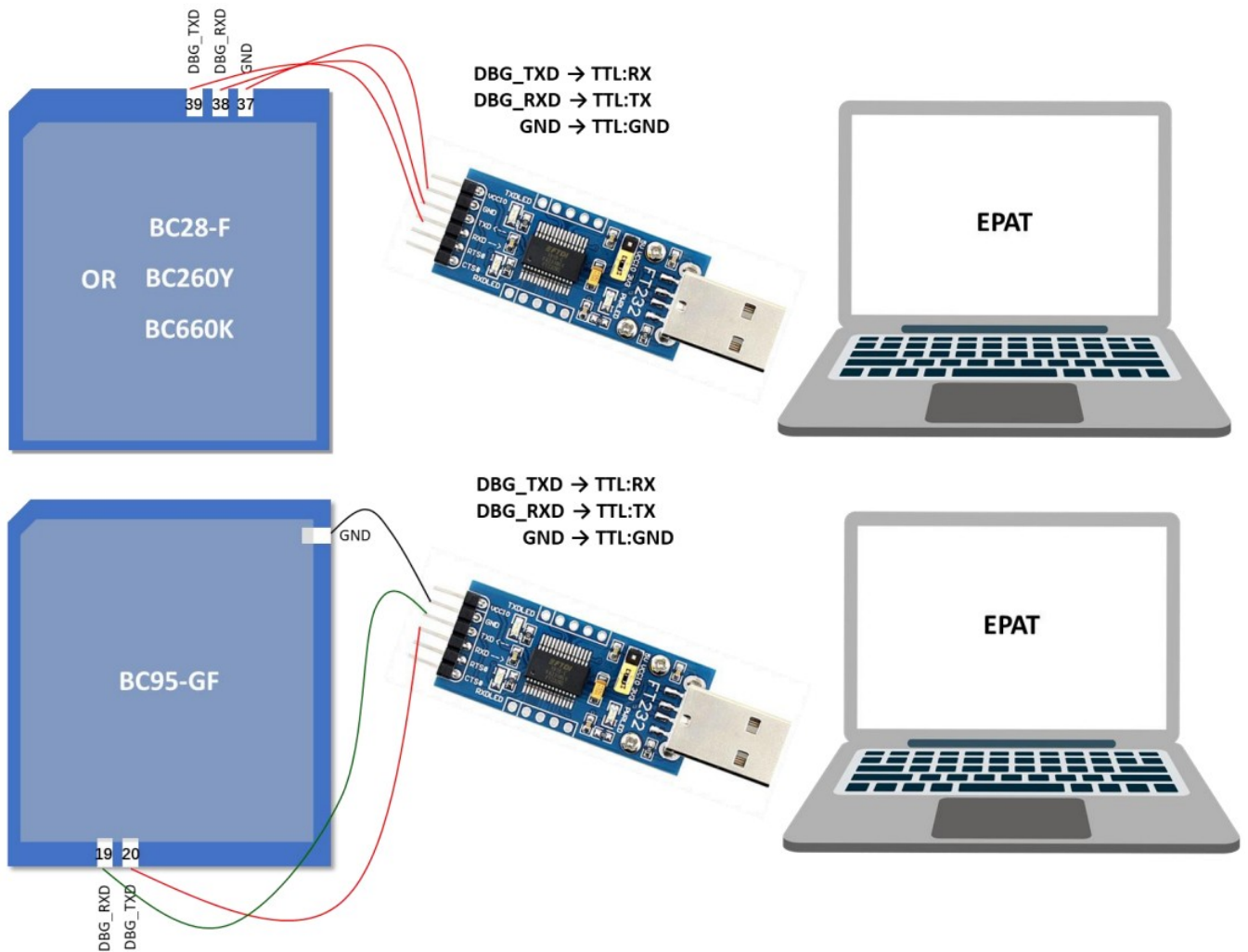
### 1.3 Device Connection

If the module has been welded or debugged separately, it is recommended that you connect to the EPAT and grab the log as shown below.

Converter (TTL)

Note: the Debug port of the EC616/EC616s/QCX212 requires a high output rate. You are advised to use uart-to-

usb Converter (TTL) that supports 3M or 6M.



If TE-B corresponding to EC616/QCS212 module is used, from "Device Manager → Ports" to select the third COM port.

## EPAT Installation

EPAT is installation-free and can be used after decompression. Run EPAT.exe in the EPAT\bin.

**Note:** EPAT requires VCMFCDLLs, if your first run of EPAT fails, install vc\_redistepat.x86.exe in the EPAT\bin directory.

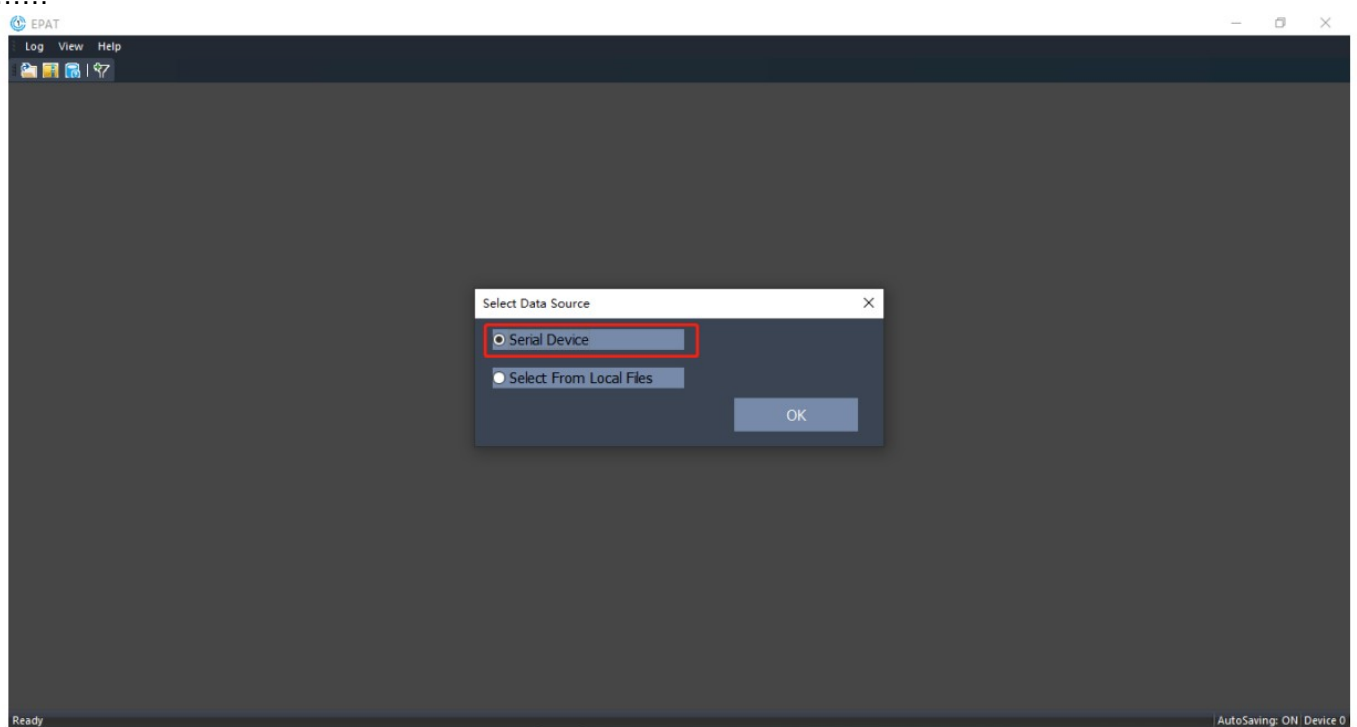


EPAT icon

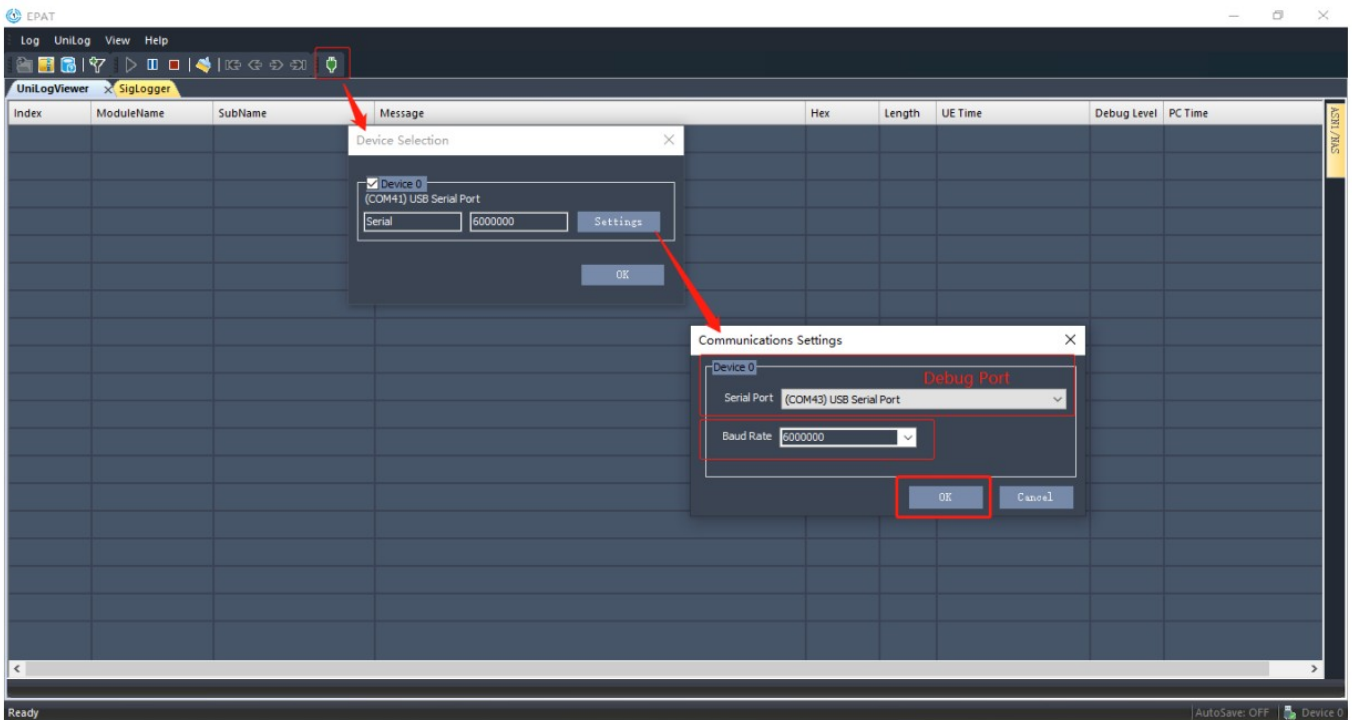
## EPAT Connection

To query the default baud rate of the corresponding module by AT + QCFG?  
AT+QCFG?

.....  
+QCFG: "logbaudrate",6000000  
.....



Click the icon in the toolbar as shown below, select Debug port and set baud rate (consistent with query baud rate of module);



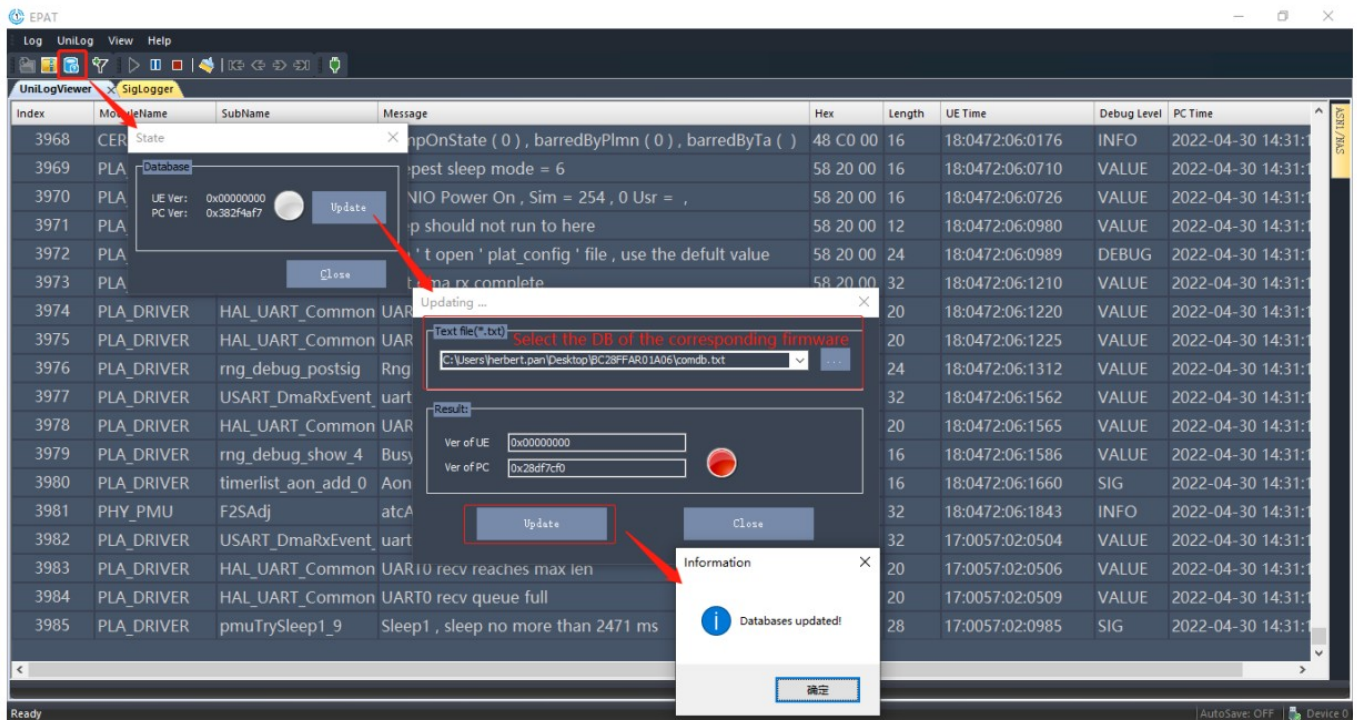
Generally, the output LOG is as shown below. If a lot of HEX codes are output in Message, the DB file needs to be imported or updated.

The screenshot shows the EPAT UniLogViewer application displaying a log of system events. The log entries are as follows:

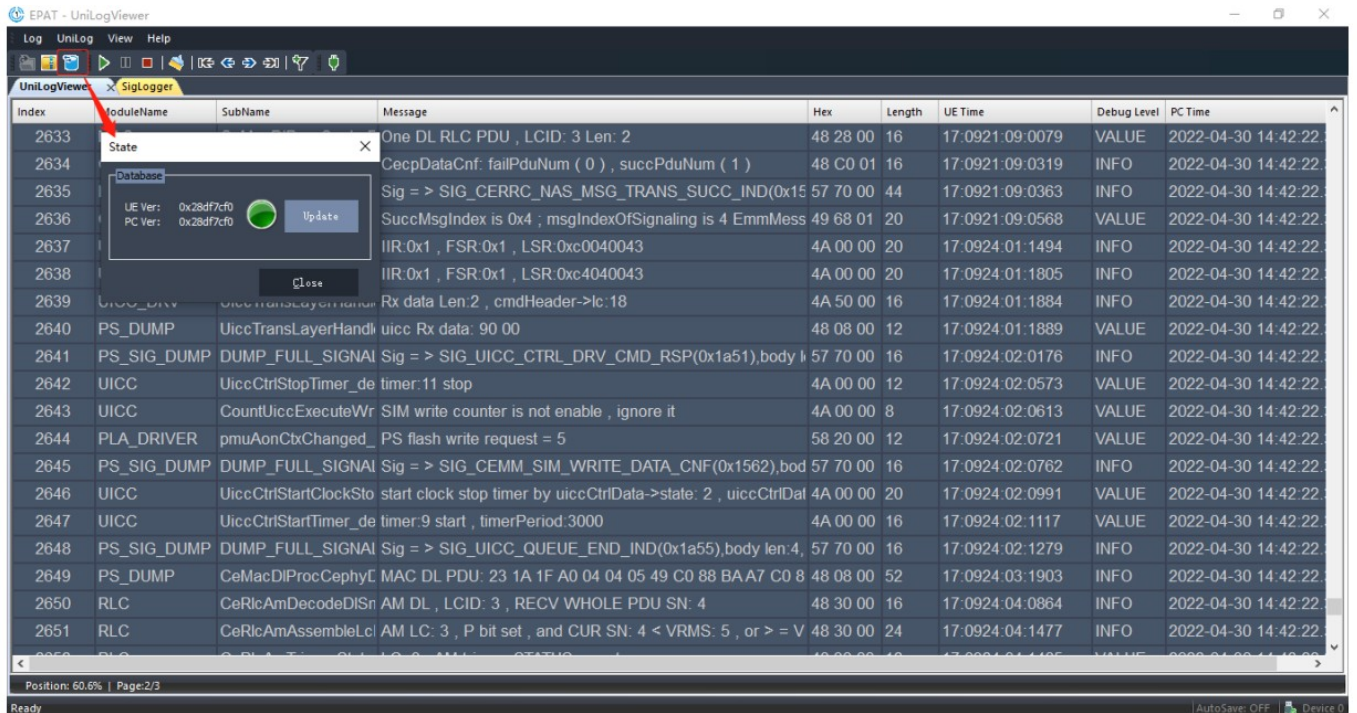
Index	ModuleName	SubName	Message	Hex	Length	UE Time	Debug Level	PC Time
3021	PLA_DRIVER	pmuAonCtxChanged	PS flash write request = 2	58 20 00	12	30:0704:01:1339	VALUE	2022-04-30 14:22:4
3022	NAS	CeNasTaskEntry_2	CENAS vote to enter HIB state	49 60 00	8	30:0704:01:1345	VALUE	2022-04-30 14:22:4
3023	NAS	CeNasTaskEntry_3	CENAS vote to enter SLEEP2 state	49 60 00	8	30:0704:01:1364	VALUE	2022-04-30 14:22:4
3024	PS_SIG_DUMP	DUMP_FULL_SIGNAL	Sig = > SIG_CAC_CMI_IND(0x11f3),body len:8, body data	57 70 00	20	30:0704:01:1600	INFO	2022-04-30 14:22:4
3025	PLA_DRIVER	pmuAonCtxChanged	PS flash write request = 1	58 20 00	12	30:0704:01:1695	VALUE	2022-04-30 14:22:4
3026	ATCMD	atPrcoCacCmiIndSig	ATCMD CMI IND , sgld: 3 ( DEV-2 / MM-3 / PS-4 / SIM-	58 30 00	16	30:0704:01:1795	INFO	2022-04-30 14:22:4
3027	PLA_DRIVER	SlowCounter32KFreq	32 KT Calibration Result = 0xffefc6a	58 20 00	12	30:0718:08:1511	VALUE	2022-04-30 14:22:4
3028	PHY_PMU	Calibr0	PhyPmuCalibr stop result = 3a944960	40 60 00	12	30:0718:08:1520	SIG	2022-04-30 14:22:4
3029	PLA_DRIVER	pmuVoteToSleep1St	Vote Sleep:L1PsSleepVoteFlag = 0x0-0\n	58 20 00	16	30:0718:09:0218	VALUE	2022-04-30 14:22:4
3030	PLA_DRIVER	pmuTrySleep1_1	worth sleep	58 20 00	8	30:0718:09:0921	VALUE	2022-04-30 14:22:4
3031	UICC_DRV	UiccDrvSleepPrepare	The uicc prepares to enter sleep1 mode...	4A 50 00	8	30:0718:09:1125	INFO	2022-04-30 14:22:4
3032	UICC_DRV	UiccDrvSleepPrepare	Pmu Aon Is not Latched , latch it	4A 50 00	8	30:0719:00:0405	INFO	2022-04-30 14:22:4
3033	PLA_DRIVER	pmuCheckCodeRunT	!!!!!!! PMU Run Time Checker 5 Too Long , 1 < <5 !!!!!!!!	58 20 00	20	30:0719:00:0691	VALUE	2022-04-30 14:22:4
3034	PLA_DRIVER	SwCntUpdate_01	SwCnt Update Input: Aon2048H = 0x0 , Aon2048L = 0x9;	58 20 00	32	30:0719:00:1035	VALUE	2022-04-30 14:22:4
3035	PLA_DRIVER	SwCntUpdate_03	SwCnt Update: sc_diff_32h = 0x0 , sc_diff_32l = 0x1345 ,	58 20 00	20	30:0719:00:1046	VALUE	2022-04-30 14:22:4
3036	PLA_DRIVER	SwCntUpdate_02	SwCnt Update Output: Aon2048H = 0x0 , Aon2048L = 0x	58 20 00	20	30:0719:00:1064	VALUE	2022-04-30 14:22:4
3037	PHY_PMU	phySlpTimeUpdt	SleepTimeUpdate , origTimeDiff = 2953 , newTimeDiff =	40 60 00	16	30:0719:00:1111	INFO	2022-04-30 14:22:4
3038	PLA_DRIVER	Enter_Sleep1	Enter Sleep1: 2611 ms PreSlp = 1 ms Wakeup SC = 1297	58 20 00	28	30:0719:00:1684	VALUE	2022-04-30 14:22:4

Click the icon in the toolbar as shown below, select the comdb.txt file of the corresponding firmware of the module, and import/update the DB file.





By reset, the DB status changes as shown below.



If the imported DB file does not match the current module firmware, the DB status will change as shown below.

EPAT - UniLogViewer

Log UniLog View Help

UniLogViewer SigLogger

Index	ModuleName	SubName	Message	Hex	Length	UE Time	Debug Level	PC Time
2633	State		One DL RLC PDU , LCID: 3 Len: 2	48 28 00	16	17:0921:09:0079	VALUE	2022-04-30 14:42:22
2634			CecpDataCnf: failPduNum ( 0 ) , succPduNum ( 1 )	48 C0 01	16	17:0921:09:0319	INFO	2022-04-30 14:42:22
2635			Sig = > SIG_CERRC_NAS_MSG_TRANS_SUCC_IND(0x15	57 70 00	44	17:0921:09:0363	INFO	2022-04-30 14:42:22
2636			SuccMsgIndex is 0x4 ; msgIndexOfSignaling is 4 EmmMess	49 68 01	20	17:0921:09:0568	VALUE	2022-04-30 14:42:22
2637			IIR:0x1 , FSR:0x1 , LSR:0xc0040043	4A 00 00	20	17:0924:01:1494	INFO	2022-04-30 14:42:22
2638			IIR:0x1 , FSR:0x1 , LSR:0xc4040043	4A 00 00	20	17:0924:01:1805	INFO	2022-04-30 14:42:22
2639	UICC_DRV	UiccTransLayerHandl	Rx data Len:2 , cmdHeader->lc:18	4A 50 00	16	17:0924:01:1884	INFO	2022-04-30 14:42:22
2640	PS_DUMP	UiccTransLayerHandl	uicc Rx data: 90 00	48 08 00	12	17:0924:01:1889	VALUE	2022-04-30 14:42:22
2641	PS_SIG_DUMP	DUMP_FULL_SIGNAL	Sig = > SIG_UICC_CTRL_DRV_CMD_RSP(0x1a51),body l	57 70 00	16	17:0924:02:0176	INFO	2022-04-30 14:42:22
2642	UICC	UiccCtrlStopTimer_de	timer:11 stop	4A 00 00	12	17:0924:02:0573	VALUE	2022-04-30 14:42:22
2643	UICC	CountUiccExecuteWr	SIM write counter is not enable , ignore it	4A 00 00	8	17:0924:02:0613	VALUE	2022-04-30 14:42:22
2644	PLA_DRIVER	pmuAonCtxChanged_	PS flash write request = 5	58 20 00	12	17:0924:02:0721	VALUE	2022-04-30 14:42:22
2645	PS_SIG_DUMP	DUMP_FULL_SIGNAL	Sig = > SIG_CEMM_SIM_WRITE_DATA_CNF(0x1562),bod	57 70 00	16	17:0924:02:0762	INFO	2022-04-30 14:42:22
2646	UICC	UiccCtrlStartClockSto	start clock stop timer by uiccCtrlData->state: 2 , uiccCtrlDat	4A 00 00	20	17:0924:02:0991	VALUE	2022-04-30 14:42:22
2647	UICC	UiccCtrlStartTimer_de	timer:9 start , timerPeriod:3000	4A 00 00	16	17:0924:02:1117	VALUE	2022-04-30 14:42:22
2648	PS_SIG_DUMP	DUMP_FULL_SIGNAL	Sig = > SIG_UICC_QUEUE_END_IND(0x1a55),body len:4,	57 70 00	16	17:0924:02:1279	INFO	2022-04-30 14:42:22
2649	PS_DUMP	CeMacDiProcCephyE	MAC DL PDU: 23 1A 1F A0 04 04 05 49 C0 88 BAA7 C0 8	48 08 00	52	17:0924:03:1903	INFO	2022-04-30 14:42:22
2650	RLC	CeRlcAmDecodeDISr	AM DL , LCID: 3 , RECV WHOLE PDU SN: 4	48 30 00	16	17:0924:04:0864	INFO	2022-04-30 14:42:22
2651	RLC	CeRlcAmAssembleLcl	AM LC: 3 , P bit set , and CUR SN: 4 < VRMS: 5 , or > = V	48 30 00	24	17:0924:04:1477	INFO	2022-04-30 14:42:22

Position: 60.6% | Page:2/3

Ready | AutoSave: OFF | Device 0

If the “UniLogViewer” in EPAT only outputs Log messages as shown below, select “Log→Log Control” in the menu bar, then select “On” and click “Start” in the popup window, then to display the command, after executing for several times, re-power the module or Reset; AT+ECPCFG=”logCtrl”,2; Enable debug output;

EPAT

Log UnLog View Help

UnLogViewer SigLogger

Index	ModuleName	SubName	Message	Hex	Length	UE Time	Debug Level	PC Time
1			at^logversion	6C 5E 74	15		DEBUG	2022-06-11 13:47:02
2			at^logversion	6C 5E 74	15		DEBUG	2022-06-11 13:47:05
3			at^logversion	6C 5E 74	15		DEBUG	2022-06-11 13:47:08
4			at^logversion	6C 5E 74	15		DEBUG	2022-06-11 13:47:11
5			at^logversion	6C 5E 74	15		DEBUG	2022-06-11 13:47:14

Ready

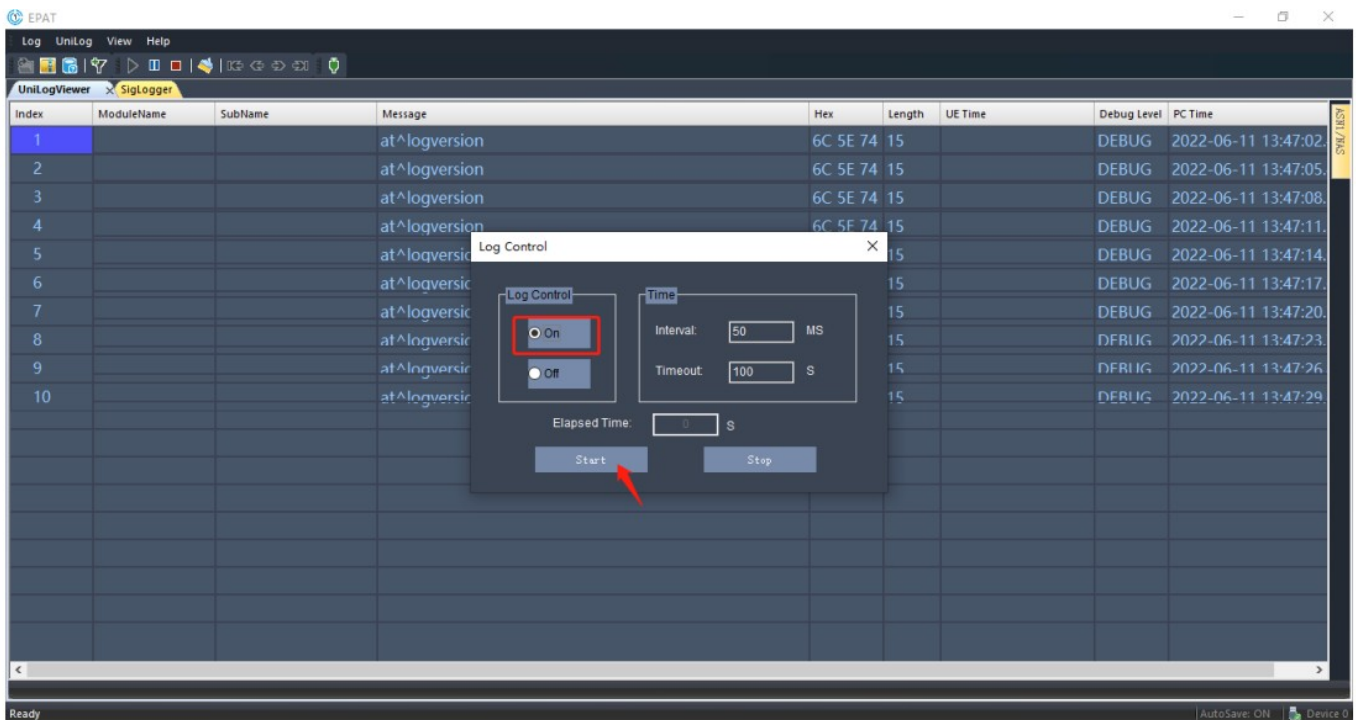
EPAT

Log UnLog View Help

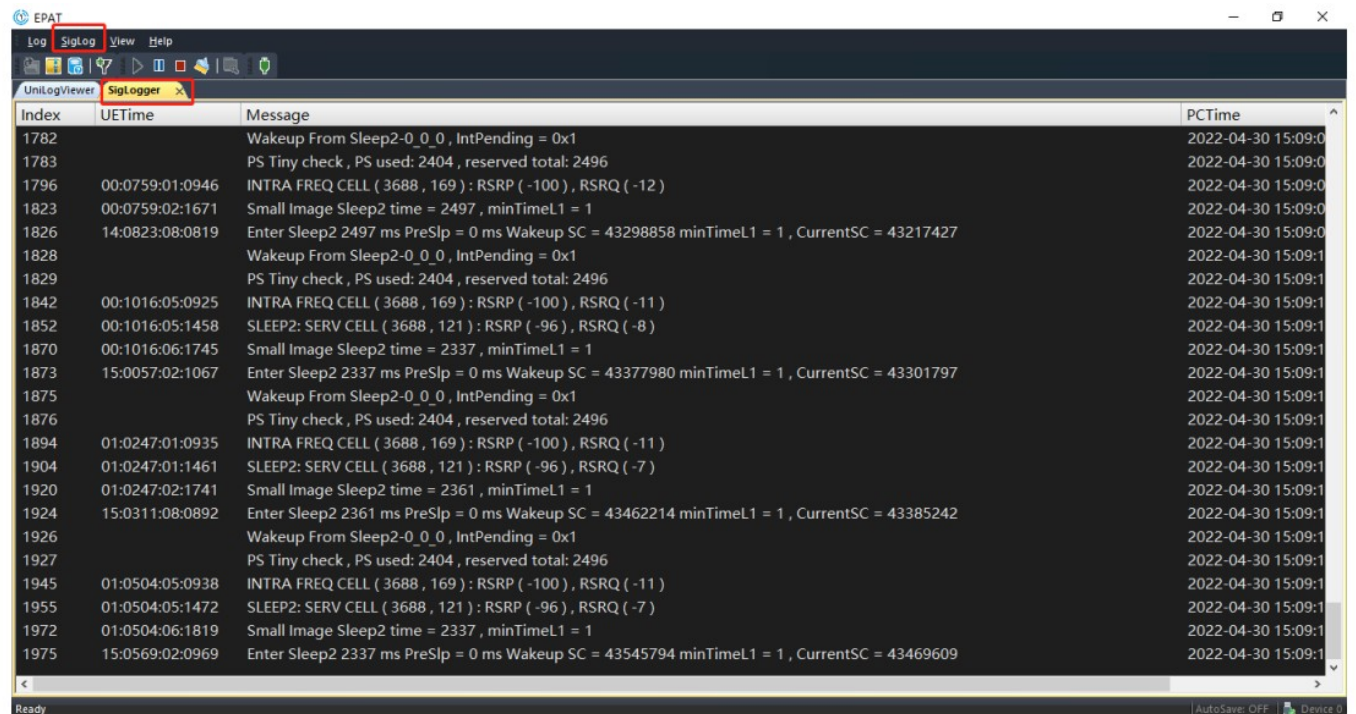
Open Log File Ctrl+O  
Save Zip Log File  
RawDump  
Preference  
Log Control

Index	SubName	Message	Hex	Length	UE Time	Debug Level	PC Time
1		at^logversion	6C 5E 74	15		DEBUG	2022-06-11 13:47:02
2		at^logversion	6C 5E 74	15		DEBUG	2022-06-11 13:47:05
3		at^logversion	6C 5E 74	15		DEBUG	2022-06-11 13:47:08
4		at^logversion	6C 5E 74	15		DEBUG	2022-06-11 13:47:11
5		at^logversion	6C 5E 74	15		DEBUG	2022-06-11 13:47:14
6		at^logversion	6C 5E 74	15		DEBUG	2022-06-11 13:47:17
7		at^logversion	6C 5E 74	15		DEBUG	2022-06-11 13:47:20
8		at^logversion	6C 5F 74	15		DEBUG	2022-06-11 13:47:23
9		at^logversion	6C 5F 74	15		DEBUG	2022-06-11 13:47:26
10		at^logversion	6C 5E 74	15		DEBUG	2022-06-11 13:47:29





As shown below, select "SigLog→Only Show Protocol Signalling" in the SigLogger window, to view NAS/AS Signalling/message.



Index	UETime	Message	PCTime
2480		PS Tiny check, PS used: 2404, reserved total: 2496	2022-04-30 15:09:4
2493		INTRA FREQ CELL ( 3688, 169 ): RSRP ( -101 ), RSRQ ( -13 )	2022-04-30 15:09:4
2503		SLEEP2: SERV CELL ( 3688, 121 ): RSRP ( -96 ), RSRQ ( -8 )	2022-04-30 15:09:4
2520		Small Image Sleep2 time = 2507, minTimeL1 = 1	2022-04-30 15:09:4
2524		Enter Sleep2 2507 ms PreSlp = 0 ms Wakeup SC = 44473554 minTimeL1 = 1, CurrentSC = 44391799	2022-04-30 15:09:4
2526		Wakeup From Sleep2-0_0_0, IntPending = 0x1	2022-04-30 15:09:4
2527		PS Tiny check, PS used: 2404, reserved total: 2496	2022-04-30 15:09:4
2540	04:0503:01:0931	INTRA FREQ CELL ( 3688, 169 ): RSRP ( -101 ), RSRQ ( -13 )	2022-04-30 15:09:4
2568	18:0567:08:0761	Enter Sleep2 2486 ms PreSlp = 0 ms Wakeup SC = 44557101 minTimeL1 = 1, CurrentSC = 44476029	2022-04-30 15:09:4
2570		Wakeup From Sleep2-0_0_0, IntPending = 0x1	2022-04-30 15:09:4
2571		PS Tiny check, PS used: 2404, reserved total: 2496	2022-04-30 15:09:4
2584	04:0759:01:0922	INTRA FREQ CELL ( 3688, 169 ): RSRP ( -102 ), RSRQ ( -14 )	2022-04-30 15:09:4
2613	18:0823:08:0768	Enter Sleep2 2497 ms PreSlp = 0 ms Wakeup SC = 44641368 minTimeL1 = 1, CurrentSC = 44559936	2022-04-30 15:09:4
2615		Wakeup From Sleep2-0_0_0, IntPending = 0x1	2022-04-30 15:09:5
2616		PS Tiny check, PS used: 2404, reserved total: 2496	2022-04-30 15:09:5
2629	04:1015:01:0938	INTRA FREQ CELL ( 3688, 169 ): RSRP ( -102 ), RSRQ ( -14 )	2022-04-30 15:09:5
2639	04:1015:01:1458	SLEEP2: SERV CELL ( 3688, 121 ): RSRP ( -96 ), RSRQ ( -8 )	2022-04-30 15:09:5
2660	19:0055:08:0835	Enter Sleep2 2486 ms PreSlp = 0 ms Wakeup SC = 44724914 minTimeL1 = 1, CurrentSC = 44643844	2022-04-30 15:09:5
2662		Wakeup From Sleep2-0_0_0, IntPending = 0x1	2022-04-30 15:09:5
2663		PS Tiny check, PS used: 2404, reserved total: 2496	2022-04-30 15:09:5
2676	05:0247:01:0941	INTRA FREQ CELL ( 3688, 169 ): RSRP ( -101 ), RSRQ ( -13 )	2022-04-30 15:09:5
2686	05:0247:01:1464	SLEEP2: SERV CELL ( 3688, 121 ): RSRP ( -96 ), RSRQ ( -8 )	2022-04-30 15:09:5
2705	19:0311:08:0784	Enter Sleep2 2497 ms PreSlp = 0 ms Wakeup SC = 44809182 minTimeL1 = 1, CurrentSC = 44727750	2022-04-30 15:09:5

Index	UETime	Message	PCTime
11550		Wakeup From Sleep	2022-04-30 15:16:2
11551		PS Tiny check, PS	2022-04-30 15:16:2
11564	25:0819:04:0969	SLEEP2: SERV CELL	2022-04-30 15:16:2
11582	25:0819:05:1130	Small Image Sleep	2022-04-30 15:16:2
11586	25:0819:06:0122	Enter Sleep2 2531	2022-04-30 15:16:2
11588		Wakeup From Sleep	2022-04-30 15:16:3
11589		PS Tiny check, PS	2022-04-30 15:16:3
11602	26:0051:04:0794	SLEEP2: SERV CELL	2022-04-30 15:16:3
11620	26:0051:05:1757	Enter Sleep2 2521	2022-04-30 15:16:3
11622		Wakeup From Sleep	2022-04-30 15:16:3
11623		PS Tiny check, PS	2022-04-30 15:16:3
11636	26:0307:04:0805	SLEEP2: SERV CELL	2022-04-30 15:16:3
11657	26:0307:05:1706	Enter Sleep2 2531	2022-04-30 15:16:3
11659		Wakeup From Sleep	2022-04-30 15:16:3
11660		PS Tiny check, PS	2022-04-30 15:16:3
11673	26:0563:04:0961	SLEEP2: SERV CELL	2022-04-30 15:16:3
11687	26:0563:05:1034	Small Image Sleep	2022-04-30 15:16:3
11690	26:0563:05:1890	Enter Sleep2 2521	2022-04-30 15:16:3
11692		Wakeup From Sleep	2022-04-30 15:16:3
11693		PS Tiny check, PS	2022-04-30 15:16:3
11706	26:0819:04:0990	SLEEP2: SERV CELL	2022-04-30 15:16:3
11723	26:0819:05:1159	Small Image Sleep2 time = 2538, minTimeL1 = 1	2022-04-30 15:16:3
11727	26:0819:06:0094	Enter Sleep2 2538 ms PreSlp = 0 ms Wakeup SC = 11153694 minTimeL1 = 1, CurrentSC = 11070907	2022-04-30 15:16:3

the pause or stop icon in the toolbar as shown below,to pause or stop grabbing Log.

EPAT

Log UnLog View Help

UnitLogViewer

Index	ModuleName	SubName	Message	Hex	Length	UE Time	Debug Level	PC Time
10674	CERRC	CerrcSkipReselFreq_i	Skip intra-freq due to: numOfCell ( 0 ) , top cell rankValid	48 C0 00	20	19:0819:04:1458	INFO	2022-04-30 15:15:2
10675	PLA_DRIVER	pmuVoteToSleep1St	Vote Sleep:L1PsSleepVoteFlag = 0x6-0\n	58 20 00	16	19:0819:05:0173	VALUE	2022-04-30 15:15:2
10676	PLA_DRIVER	pmuVoteToSleep2St	Vote Sleep2:L1PsSleep2VoteFlag = 0xfe-0\n	58 20 00	16	19:0819:05:0190	VALUE	2022-04-30 15:15:2
10677	PLA_DRIVER	ec_main_04	Exit Phy WFI Process , CurrSC = 0x851450	58 20 00	12	19:0819:05:0436	VALUE	2022-04-30 15:15:2
10678	PLA_DRIVER	ec_main_0000	exit small image lpuart status , iir: 0x0 , fcsr: 0x80012 , tcr	58 20 00	24	19:0819:05:0446	DEBUG	2022-04-30 15:15:2
10679	PLA_DRIVER	SwCntUpdate_01	SwCnt Update Input: Aon2048H = 0x0 , Aon2048L = 0x8	58 20 00	32	19:0819:05:0671	VALUE	2022-04-30 15:15:2
10680	PLA_DRIVER	SwCntUpdate_03	SwCnt Update: sc_diff_32h = 0x0 , sc_diff_32l = 0x16 , T3	58 20 00	20	19:0819:05:0678	VALUE	2022-04-30 15:15:2
10681	PLA_DRIVER	SwCntUpdate_02	SwCnt Update Output: Aon2048H = 0x0 , Aon2048L = 0x	58 20 00	20	19:0819:05:0687	VALUE	2022-04-30 15:15:2
10682	PLA_DRIVER	SwCntUpdate_01	SwCnt Update Input: Aon2048H = 0x0 , Aon2048L = 0x8	58 20 00	32	19:0819:05:1020	VALUE	2022-04-30 15:15:2
10683	PLA_DRIVER	SwCntUpdate_03	SwCnt Update: sc_diff_32h = 0x0 , sc_diff_32l = 0x6 , T32	58 20 00	20	19:0819:05:1022	VALUE	2022-04-30 15:15:2
10684	PLA_DRIVER	timerlist_hib_getnear	remain_sc_cycles = 6141155 , ID = 7	58 20 00	16	19:0819:05:1044	VALUE	2022-04-30 15:15:2
10685	PLA_DRIVER	pmuEnterHibernate	Small Image Sleep2 time = 2538 , minTimeL1 = 1	58 20 00	16	19:0819:05:1116	SIG	2022-04-30 15:15:2
10686	PHY_PMU	F2SAdj	atcAdjVal = 1 , Old_F2SRatio = 3A9448C9 , new_F2SRatio = 3A9448C9	58 20 00	16	19:0819:05:1116	SIG	2022-04-30 15:15:2
10687	PLA_DRIVER	SwCntUpdate_01	SwCnt Update Input: Aon2048H = 0x0 , Aon2048L = 0x8	58 20 00	32	19:0819:05:1545	VALUE	2022-04-30 15:15:2
10688	PLA_DRIVER	SwCntUpdate_03	SwCnt Update: sc_diff_32h = 0x0 , sc_diff_32l = 0x8 , T32	58 20 00	20	19:0819:05:1547	VALUE	2022-04-30 15:15:2
10689	PLA_DRIVER	Enter_Sleep2	Enter Sleep2 2538 ms PreSlp = 0 ms Wakeup SC = 88043	58 20 00	28	19:0819:06:0109	SIG	2022-04-30 15:15:2
10690	PLA_DRIVER	Enter_Active_1	EC616 Active Smallimg	58 20 00	8		VALUE	2022-04-30 15:15:3
10691	PLA_DRIVER	ec_main_02	Wakeup From Sleep2-0_0_0 , IntPending = 0x1	58 20 00	24		SIG	2022-04-30 15:15:3

Ready | AutoSave: OFF | Device 0

## Save Log

### 4.1 (Automatically Save)

As shown below, by the option bar “Log→Preference”, select the automatic save option and save path in the popup window, and configure the maximum file size to save. (Automatic saving is not recommended When Log meets the maximum autosave size, it can only be output to the autosave path)



The screenshot displays the EPAT software interface. The top window shows a log viewer with columns: Index, ModuleName, SubName, Message, Hex, Length, UE Time, Debug Level, and PC Time. The log contains various system events, including timer updates, process reselection, and sleep mode transitions.

The bottom window shows the 'UnitLogViewer' application with a 'Preference' dialog box open. The dialog has the following settings:

- ☒ AutoSave Log File
- Set max file size: 100 MB
- Note: When Log reaches the max size the log will save as zip file
- Save Folder: E:\TEST
- ☒ Automatically delete old log files

The dialog also includes 'OK' and 'Cancel' buttons at the bottom.

## 4.2 (Manual save)

Click the toolbar icon in the below, or “Ctrl+S”, or choose “Log→Save zip log-file” to Save the Log;



EPAT

Log Unlog View Help

Save Log

UnitLog Viewer SigLogger

Index	ModuleName	SubName	Message	Hex	Length	UE Time	Debug Level	PC Time
5113	PLA_DRIVER	SwCntUpdate_01	SwCnt Update Input: Aon2048H = 0x0 , Aon2048L = 0x204c14 ,	58 20 00	32	04:0928:05:1661	VALUE	2022
5114	PLA_DRIVER	SwCntUpdate_03	SwCnt Update: sc_diff_32h = 0x0 , sc_diff_32l = 0x5 , T32K = 0xff	58 20 00	20	04:0928:05:1671	VALUE	2022
5115	CERRC	CerrcGetHighestRankedCell	Highest ranked CELL ( 3688 , 169 ) , rank ( -1648 )	48 C0 00	20	04:0928:05:1792	INFO	2022
5116	PLA_DRIVER	SwCntUpdate_01	SwCnt Update Input: Aon2048H = 0x0 , Aon2048L = 0x204c14 ,	58 20 00	32	04:0928:06:0027	VALUE	2022
5117	PLA_DRIVER	timerlist_hib_get_8HZcount	Current_8HZ_CT = 8268 Pro_Current_CT = 8266	58 20 00	16	04:0928:06:0043	VALUE	2022
5118	CERRC	CerrcProcessReselEvaluation	Reselection evaluation result: bReselNeeded ( 0 ) , forceReselect	48 C0 00	16	04:0928:06:0158	INFO	2022
5119	CERRC	CerrcLeaveSleep2DueToSer	CERRC SLEEP2: s-SearchDeltaP ( 0 ) , Srxlev ( 22 ) , SrxlevRef ( 0 )	48 C0 01	24	04:0928:06:0195	VALUE	2022
5120	PLA_DRIVER	pmuVoteToSleep1State_1	Vote Sleep:L1PsSleepVoteFlag = 0x6-0\n	58 20 00	16	04:0928:06:0690	VALUE	2022
5121	PLA_DRIVER	pmuVoteToSleep2State_1	Vote Sleep2:L1PsSleep2VoteFlag = 0xfe-0\n	58 20 00	16	04:0928:06:0705	VALUE	2022
5122	PLA_DRIVER	ec_main_04	Exit Phy WFI Process , CurrSC = 0x204e226	58 20 00	12	04:0928:06:0967	VALUE	2022
5123	PLA_DRIVER	ec_main_0000	exit small image lpuart status , iir: 0x0 , fcsr: 0x80012 , tcr: 0xfde8	58 20 00	24	04:0928:06:0979	DEBUG	2022
5124	PLA_DRIVER	SwCntUpdate_01	SwCnt Update Input: Aon2048H = 0x0 , Aon2048L = 0x204c15 ,	58 20 00	32	04:0928:06:1200	VALUE	2022
5125	PLA_DRIVER	SwCntUpdate_03	SwCnt Update: sc_diff_32h = 0x0 , sc_diff_32l = 0x14 , T32K = 0x	58 20 00	20	04:0928:06:1210	VALUE	2022
5126	PLA_DRIVER	timerlist_hib_check_wakeup	Check Wakeup Timer Id = 7 , 6690044 , remain 32 k cycles = 0 a	58 20 00	24	04:0928:06:1302	VALUE	2022
5127	PLA_DRIVER	SwCntUpdate_01	SwCnt Update Input: Aon2048H = 0x0 , Aon2048L = 0x204c16 ,	58 20 00	32	04:0928:06:1551	VALUE	2022
5128	PLA_DRIVER	SwCntUpdate_03	SwCnt Update: sc_diff_32h = 0x0 , sc_diff_32l = 0x6 , T32K = 0xff	58 20 00	20	04:0928:06:1554	VALUE	2022
5129	PLA_DRIVER	timerlist_hib_getnearest_2	remain_sc_cycles = 4573414 , ID = 7	58 20 00	16	04:0928:06:1576	VALUE	2022
5130	PLA_DRIVER	pmuEnterHibernate_3	Small Image Sleep2 time = 2472 , minTimeL1 = 1	58 20 00	16	04:0928:06:1646	SIG	2022
5131	PLA_DRIVER	SwCntUpdate_01	SwCnt Update Input: Aon2048H = 0x0 , Aon2048L = 0x204c16 ,	58 20 00	32	06:0569:02:0317	VALUE	2022

Position: 79.3% | Page: 5/6

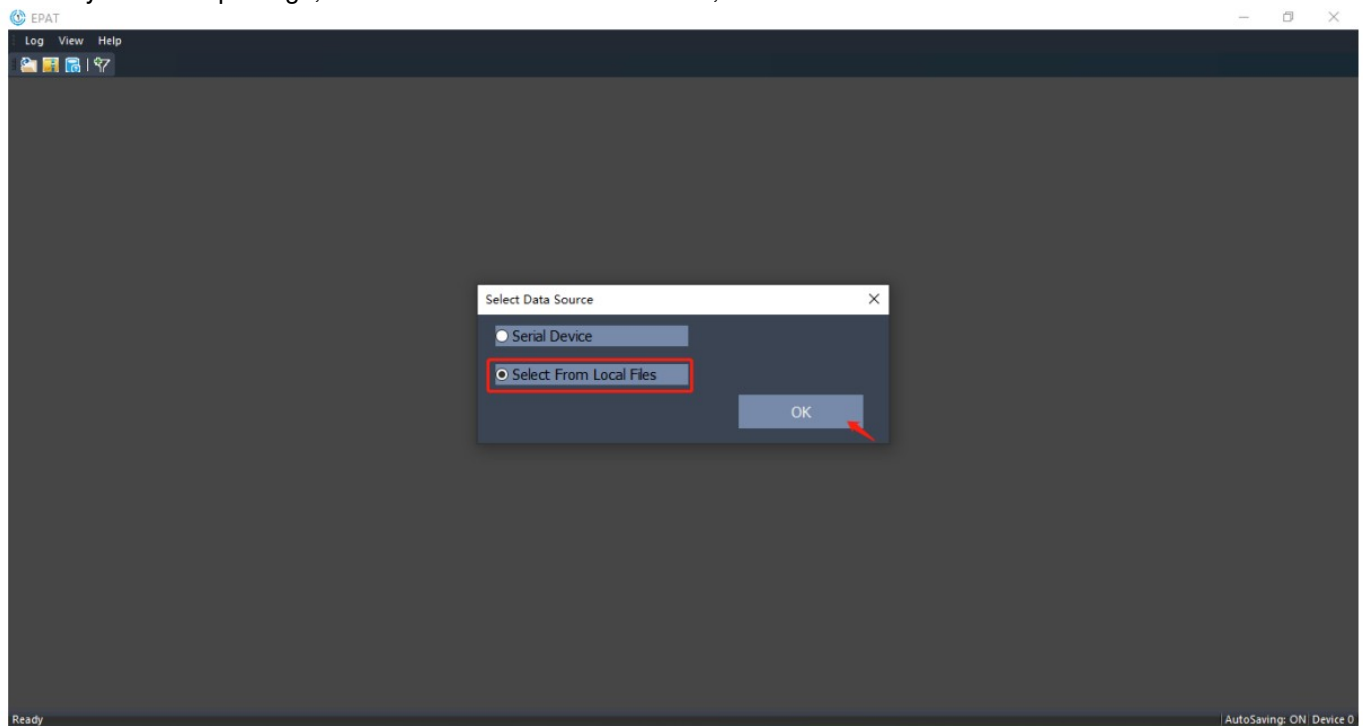
Ready

AutoSave: ON Device 0

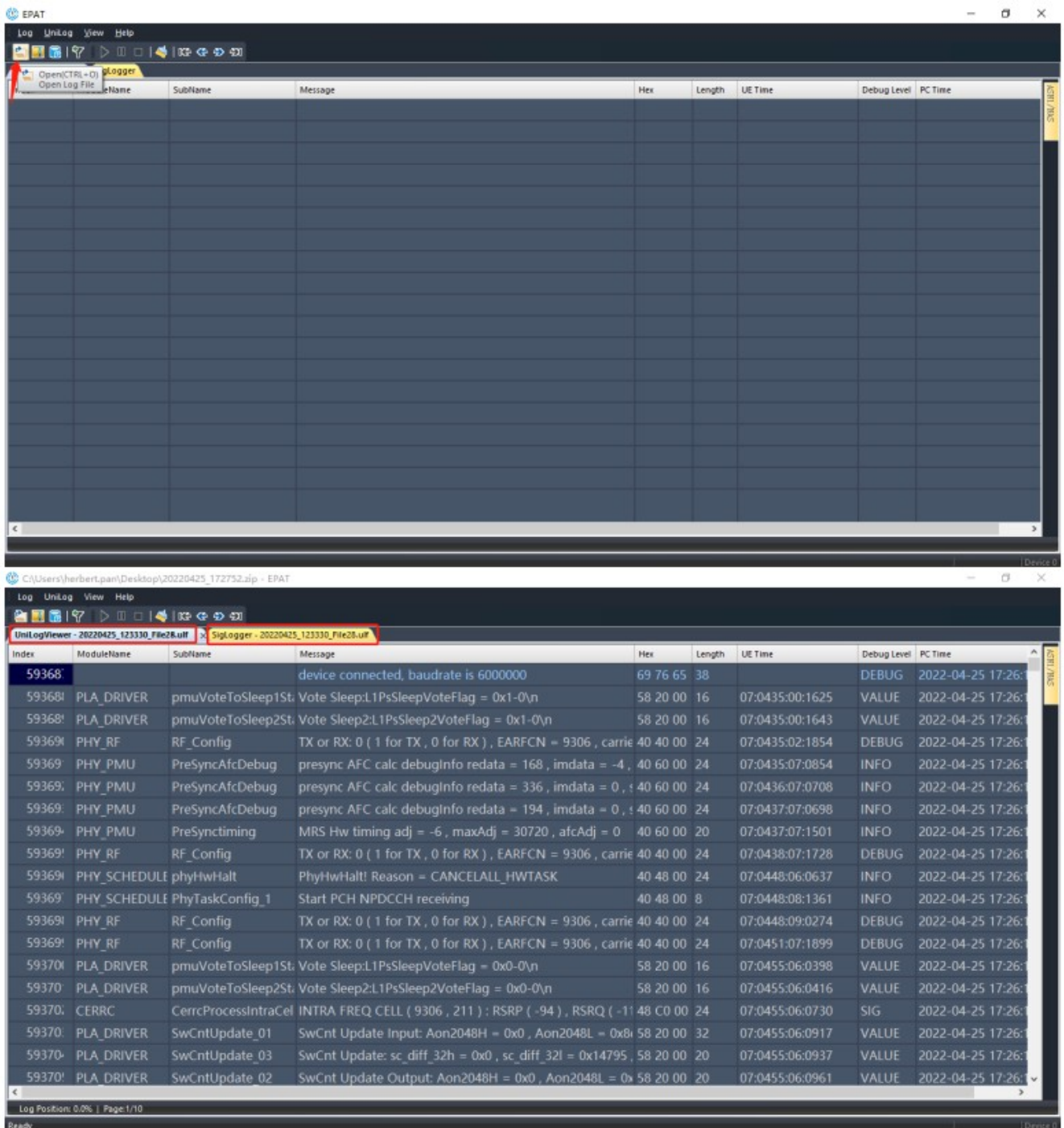
## Common Analytical Application

### 5.1 LOG(Load Log)

To analyze the output logs, Select “Select From Local Files”, as shown below.



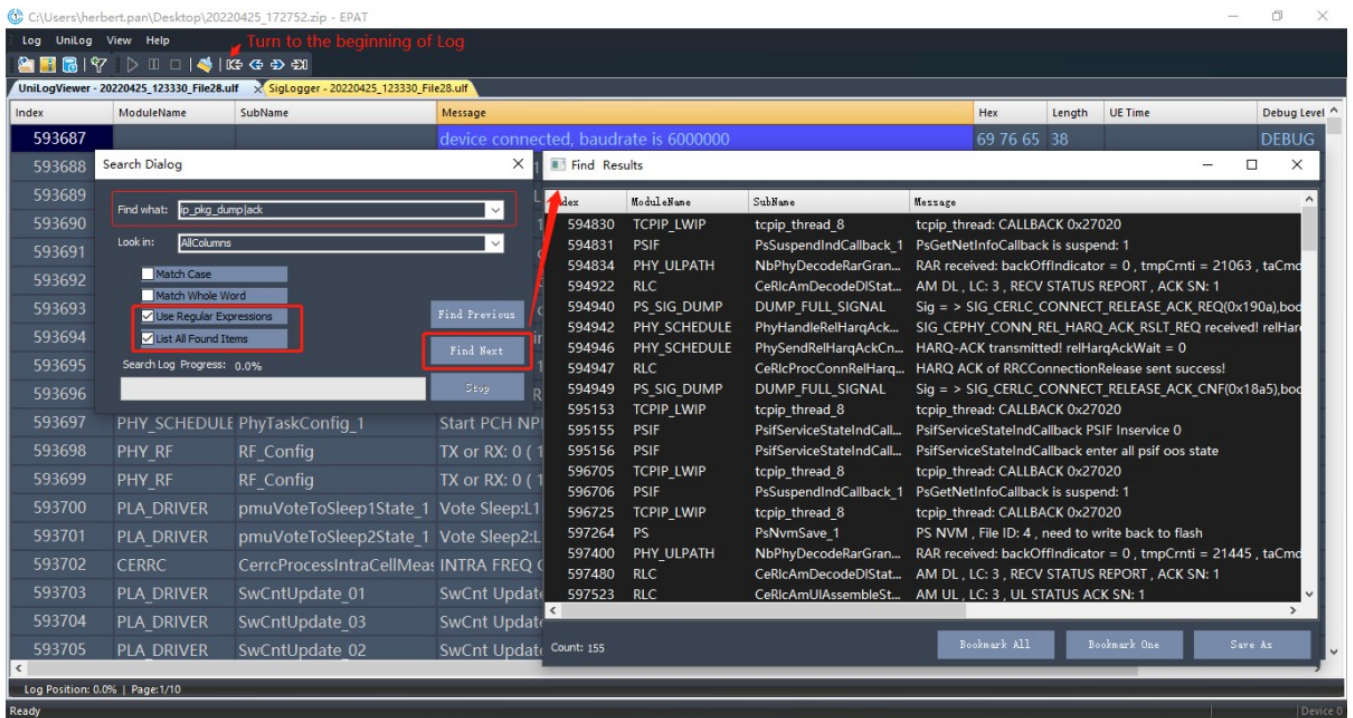
To import the analysis Log File through the toolbar icon or option bar “Log→Open Log File” as shown below.



## 5.2 (Search/Filter Log)

Open the search/filter window by shortcut "Ctrl+F", as shown below. to select the below options, through single vertical bar "|" regular filter multiple keywords;

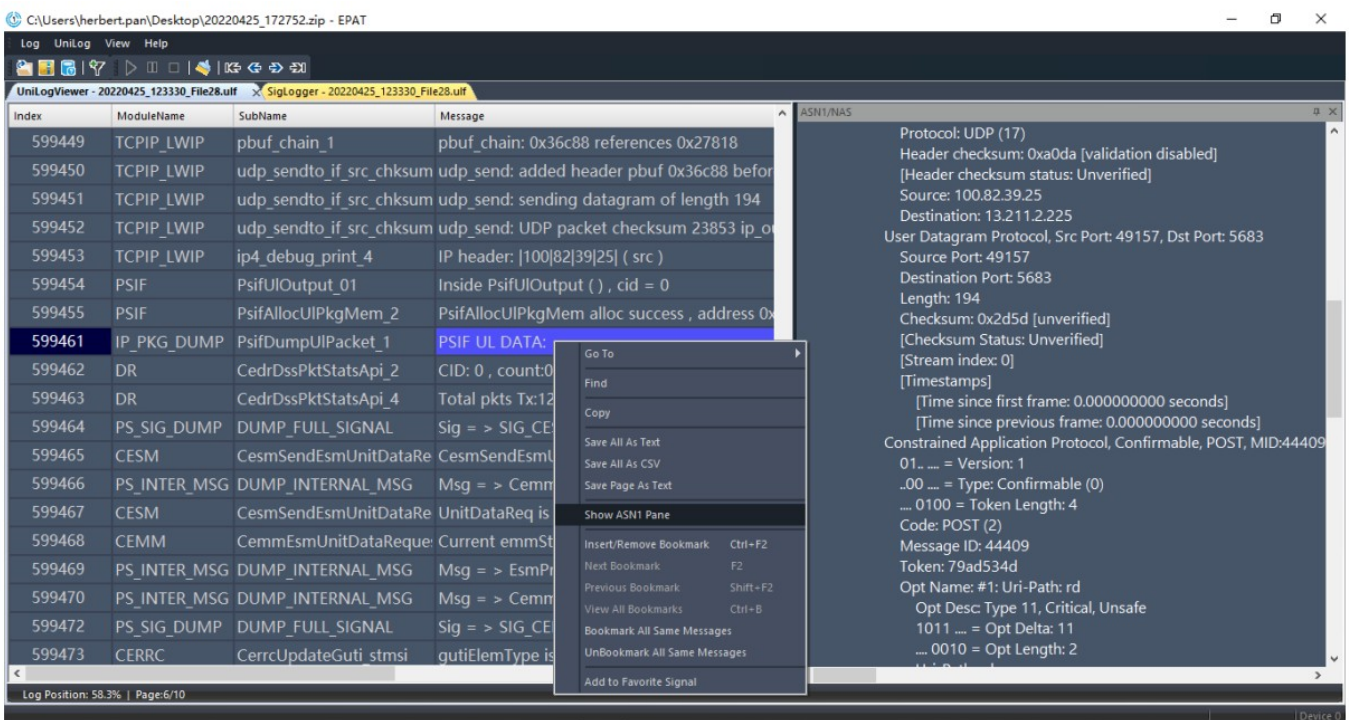




### 5.3 (Data Analysis)

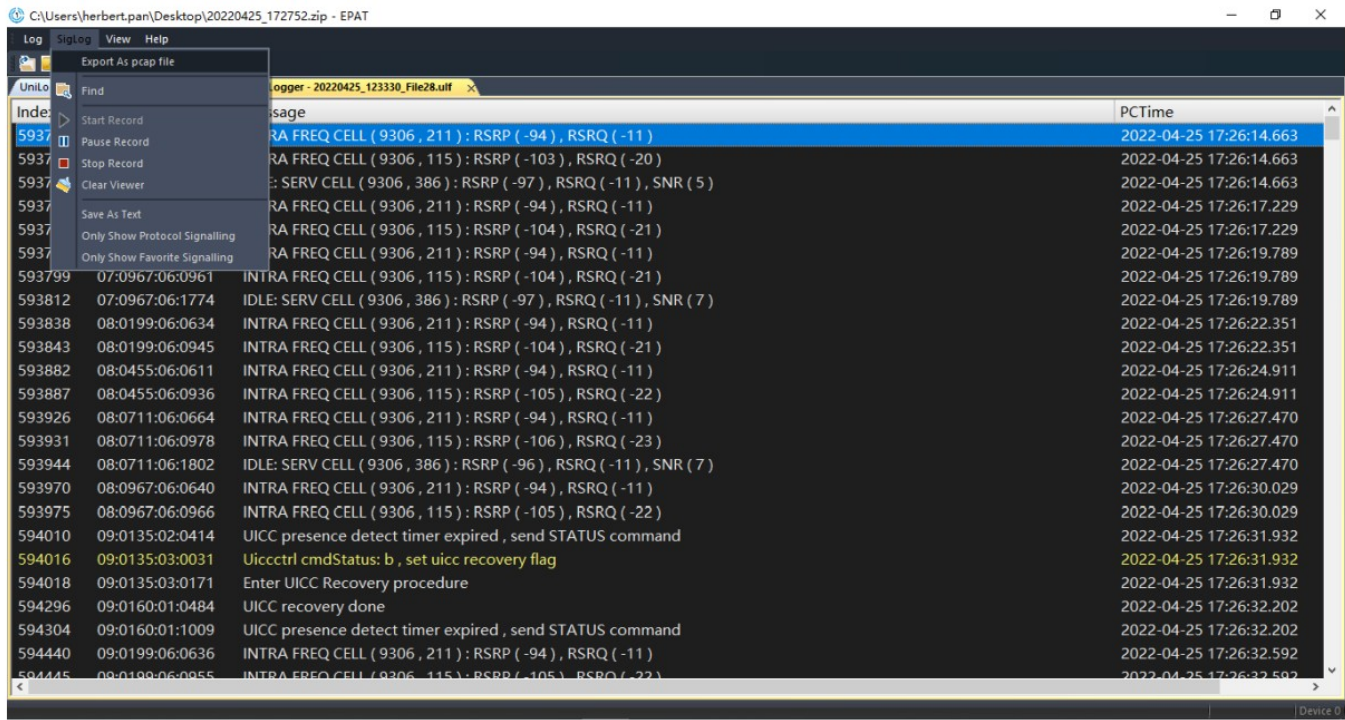
In the UniLogViewer window, select the ASN1/NAS type message, right click and choose "Show ASN1 Pane", as shown below;

ASN1/NAS message in messages can be parsed to further analyze data.



### 5.4 Pcap(Output Pcap)

To select SigLogger window and click the option bar "SigLog→Export As PCAP File", As shown below. You can export Pcap files for Wireshark analysis.



Documents / Resources



[QUECTEL BC260Y Wireless Communication Module](#) [pdf] User Manual  
BC260Y, BC660K, BC28F, BC95GF, BC300Y, BC260Y Wireless Communication Module, BC260Y, Wireless Communication Module, Communication Module, Module

References

- [User Manual](#)