

NXP UM11940 TPL Decoder Tool for Saleae Logic Software User Manual

[Home](#) » [NXP](#) » NXP UM11940 TPL Decoder Tool for Saleae Logic Software User Manual 



UM11940
TPL decoder tool for Saleae logic software
Rev. 1 — 12 June 2023

Contents

1

UM11940 TPL Decoder Tool for Saleae Logic Software

2

Introduction to the TPL decoder tool for Saleae logic software

3

Legal information

4

Documents / Resources

4.1

References

UM11940 TPL Decoder Tool for Saleae Logic Software

Document Information

Information	Content
Keywords	TPL decoder, TPL, transport protocol link, BMS communication, TPL sniffer
Abstract	This user manual aims to introduce the transport protocol link (TPL) decoder tool working as extension of the Saleae logic software.

Revision history

Rev	Date	Description
1	20230612	initial version

Introduction to the TPL decoder tool for Saleae logic software

The TPL decoder is a high-level analyzer (HLA) extension for the Saleae logic software. It helps decoding the NXP TPL protocols on several physical layers.

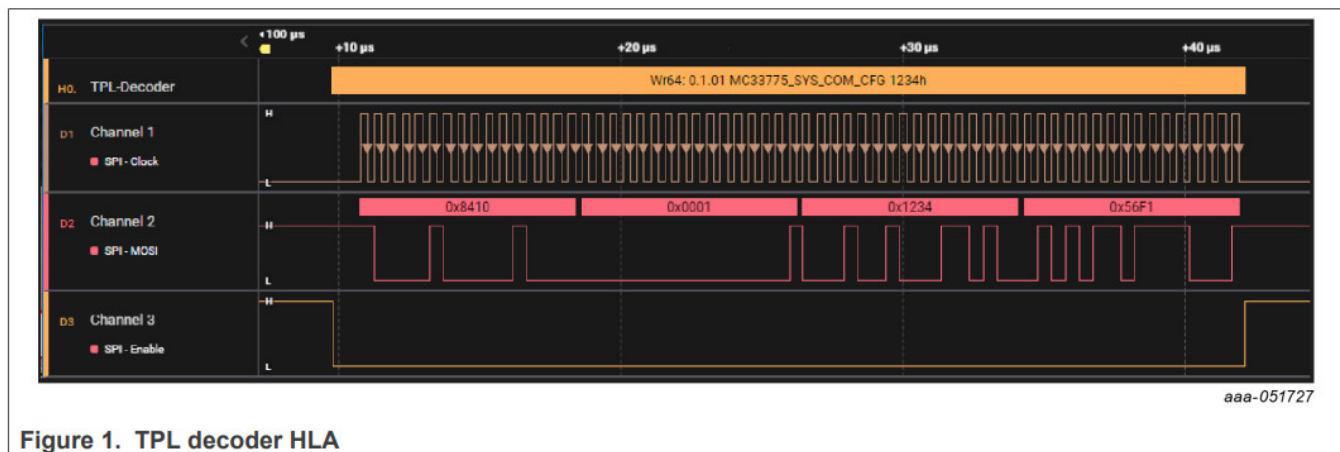


Figure 1. TPL decoder HLA

This tool is also part of the NXP battery management system (BMS) communication decoder ecosystem and can work together with the KIT-TPLSNIFEVB hardware board that facilitates the acquisition of electrical transport protocol link (ETPL) signals.

1.1 Package content

The downloadable package contains the TPL decoder installer along with a release notes and software content register (SCR) files. More information about supported devices and physical layers covered in the latest version can be found in the release notes file.

1.2 Pre-requisites

The TPL decoder is an HLA extension for the Saleae logic software. Therefore the recommended Saleae logic software version (see release notes file) must be installed prior to the TPL decoder installation. To download the Saleae logic software, visit the Saleae website.

1.3 Installation

Refer to the release notes file for installation steps.

1.4 Use

Guidance on using the TPL decoder can be found directly in the Saleae logic software by clicking the TPL decoder in the Extensions menu.

Legal information

2.1 Definitions

Draft — A draft status on a document indicates that the content is still under internal review and subject to formal approval, which may result in modifications or additions. NXP Semiconductors does not give any representations or warranties as to the accuracy or completeness of information included in a draft version of a document and shall have no liability for the consequences of use of such information.

2.2 Disclaimers

Limited warranty and liability — Information in this document is believed to be accurate and reliable. However, NXP Semiconductors does not give any representations or warranties, expressed or implied, as to the accuracy or completeness of such information and shall have no liability for the consequences of use of such information. NXP Semiconductors takes no responsibility for the content in this document if provided by an information source outside of NXP Semiconductors.

In no event shall NXP Semiconductors be liable for any indirect, incidental, punitive, special or consequential

damages (including – without limitation lost profits, lost savings, business interruption, costs related to the removal or replacement of any products or rework charges) whether or not such damages are based on tort (including negligence), warranty, breach of contract or any other legal theory.

Notwithstanding any damages that customer might incur for any reason whatsoever, NXP Semiconductors' aggregate and cumulative liability towards customer for the products described herein shall be limited in accordance with the Terms and conditions of commercial sale of NXP Semiconductors.

Right to make changes — NXP Semiconductors reserves the right to make changes to information published in this document, including without limitation specifications and product descriptions, at any time and without notice. This document supersedes and replaces all information supplied prior to the publication hereof.

Applications — Applications that are described herein for any of these products are for illustrative purposes only. NXP Semiconductors makes no representation or warranty that such applications will be suitable for the specified use without further testing or modification. Customers are responsible for the design and operation of their applications and products using NXP Semiconductors products, and NXP Semiconductors accepts no liability for any assistance with applications or customer product design. It is customer's sole responsibility to determine whether the NXP Semiconductors product is suitable and fit for the customer's applications and products planned, as well as for the planned application and use of customer's third party customer(s). Customers should provide appropriate design and operating safeguards to minimize the risks associated with their applications and products.

NXP Semiconductors does not accept any liability related to any default, damage, costs or problem which is based on any weakness or default in the customer's applications or products, or the application or use by customer's third party customer(s). Customer is responsible for doing all necessary testing for the customer's applications and products using NXP Semiconductors products in order to avoid a default of the applications and the products or of the application or use by customer's third party customer(s). NXP does not accept any liability in this respect.

Terms and conditions of commercial sale — NXP Semiconductors products are sold subject to the general terms and conditions of commercial sale, as published at <http://www.nxp.com/profile/terms>, unless otherwise agreed in a valid written individual agreement. In case an individual agreement is concluded only the terms and conditions of the respective agreement shall apply. NXP Semiconductors hereby expressly objects to applying the customer's general terms and conditions with regard to the purchase of NXP Semiconductors products by customer.

Suitability for use in automotive applications — This NXP product has been qualified for use in automotive applications. If this product is used by customer in the development of, or for incorporation into, products or services (a) used in safety critical applications or (b) in which failure could lead to death, personal injury, or severe physical or environmental damage (such products and services hereinafter referred to as "Critical Applications"), then customer makes the ultimate design decisions regarding its products and is solely responsible for compliance with all legal, regulatory, safety, and security related requirements concerning its products, regardless of any information or support that may be provided by NXP. As such, customer assumes all risk related to use of any products in Critical Applications and NXP and its suppliers shall not be liable for any such use by customer.

Accordingly, customer will indemnify and hold NXP harmless from any claims, liabilities, damages and associated costs and expenses (including attorneys' fees) that NXP may incur related to customer's incorporation of any product in a Critical Application.

Export control — This document as well as the item(s) described herein may be subject to export control regulations. Export might require a prior authorization from competent authorities.

Evaluation products — This product is provided on an "as is" and "with all faults" basis for evaluation purposes only. NXP Semiconductors, its affiliates and their suppliers expressly disclaim all warranties, whether express, implied or statutory, including but not limited to the implied warranties of noninfringement, merchantability and fitness for a particular purpose. The entire risk as to the quality, or arising out of the use or performance, of this product remains with customer.

In no event shall NXP Semiconductors, its affiliates or their suppliers be liable to customer for any special, indirect, consequential, punitive or incidental damages (including without limitation damages for loss of business, business interruption, loss of use, loss of data or information, and the like) arising out the use of or inability to use the product, whether or not based on tort (including negligence), strict liability, breach of contract, breach of warranty or any other theory, even if advised of the possibility of such damages.

Notwithstanding any damages that customer might incur for any reason whatsoever (including without limitation, all damages referenced above and all direct or general damages), the entire liability of NXP Semiconductors, its affiliates and their suppliers and customer's exclusive remedy for all of the foregoing shall be limited to actual damages incurred by customer based on reasonable reliance up to the greater of the amount actually paid by

customer for the product or five dollars (US\$5.00). The foregoing limitations, exclusions and disclaimers shall apply to the maximum extent permitted by applicable law, even if any remedy fails of its essential purpose.

Translations — A non-English (translated) version of a document, including the legal information in that document, is for reference only. The English version shall prevail in case of any discrepancy between the translated and English versions.

Security — Customer understands that all NXP products may be subject to unidentified vulnerabilities or may support established security standards or specifications with known limitations. Customer is responsible for the design and operation of its applications and products throughout their lifecycles to reduce the effect of these vulnerabilities on customer's applications and products. Customer's responsibility also extends to other open and/or proprietary technologies supported by NXP products for use in customer's applications. NXP accepts no liability for any vulnerability. Customer should regularly check security updates from NXP and follow up appropriately.

Customer shall select products with security features that best meet rules, regulations, and standards of the intended application and make the ultimate design decisions regarding its products and is solely responsible for compliance with all legal, regulatory, and security related requirements concerning its products, regardless of any information or support that may be provided by NXP.

NXP has a Product Security Incident Response Team (PSIRT) (reachable at PSIRT@nxp.com) that manages the investigation, reporting, and solution release to security vulnerabilities of NXP products.

NXP B.V. – NXP B.V. is not an operating company and it does not distribute or sell products.

2.3 Trademarks

Notice: All referenced brands, product names, service names, and trademarks are the property of their respective owners.

NXP — wordmark and logo are trademarks of NXP B.V.

Please be aware that important notices concerning this document and the product(s) described herein, have been included in section 'Legal information'.

© 2023 NXP B.V.


For more information, please visit: <http://www.nxp.com>

All rights reserved.

Date of release: 12 June 2023

Document identifier: UM11940

Documents / Resources

	<p>NXP UM11940 TPL Decoder Tool for Saleae Logic Software [pdf] User Manual</p> <p>UM11940 TPL Decoder Tool for Saleae Logic Software, UM11940, TPL Decoder Tool for Saleae Logic Software</p>
---	--

References

- [Automotive, IoT & Industrial Solutions | NXP Semiconductors](#)
- [Our Terms And Conditions Of Commercial Sale | NXP Semiconductors](#)
- [KIT-TPLSNIFEVB | Battery Management System \(BMS\) Communication Monitoring and Debugging Toolkit | NXP Semiconductors](#)
- [Logic analyzer software from Saleae](#)