




NXP UM11926 BTS6305C Evaluation Board User Guide

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Introduction

This document describes the use, design, and test results of the BTS6305C EVB.

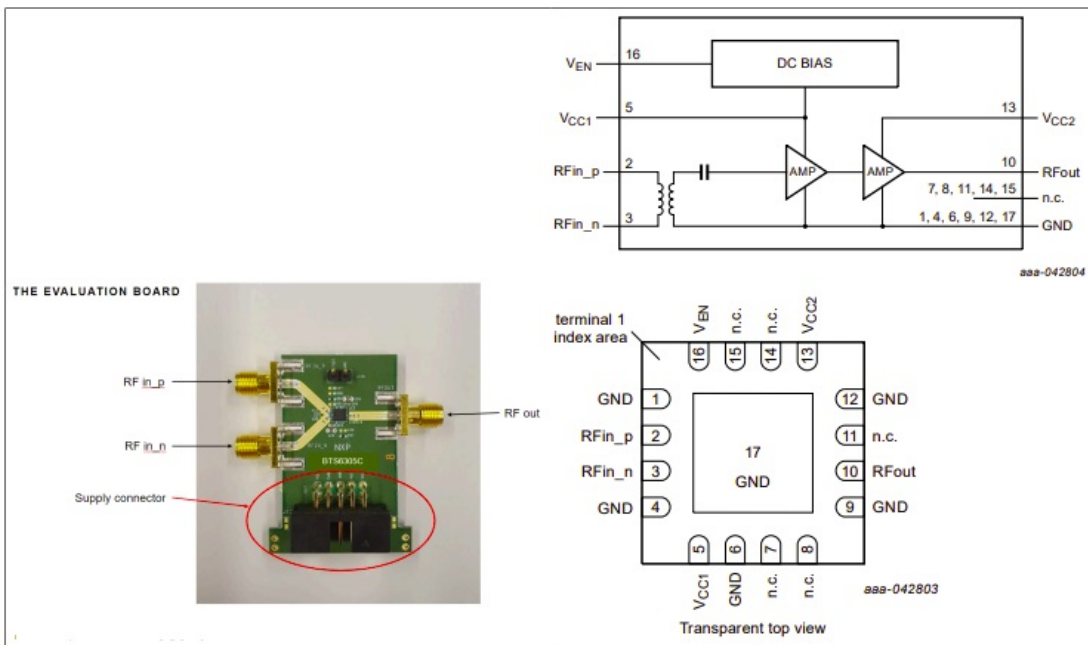
BTS6305C product description

The BTS6305C is a wideband high linearity pre-driver amplifier with differential input optimized for frequency range 4.4 GHz – 5 GHz for 5G massive MIMO infrastructure applications, with fast on-off switching to support TDD systems. The amplifier is designed to operate between 4.4 GHz and 5 GHz. The BTS6305C is housed in a 3 mm x 3 mm x 0.85 mm 16-terminal HVQFN package.

BTS6305C key features and benefits

- High saturated output power $P_{o(sat)} = 27.5$ dBm
- High power-gain $G_p = 35.5$ dB
- High linearity performance $ACLR = -42$ dBc
- Unconditionally stable
- Fast switching to support TDD systems
- 5 V single supply, quiescent current 100 mA
- Small 16-terminal leadless package 3 mm x 3 mm x 0.85 mm
- ESD protection on all terminals
- Moisture sensitivity level 1

Figure 1. EVB board

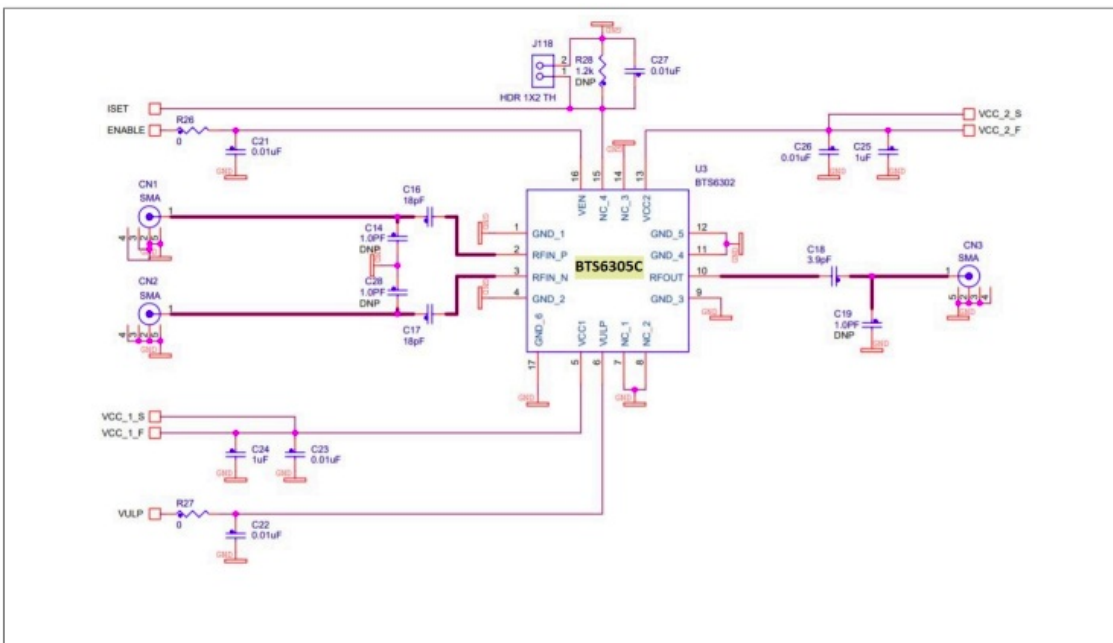


BTS6305C EVB properties

Schematic

DNP = not used components, reserved location. See Figure 1 for the location of the components.

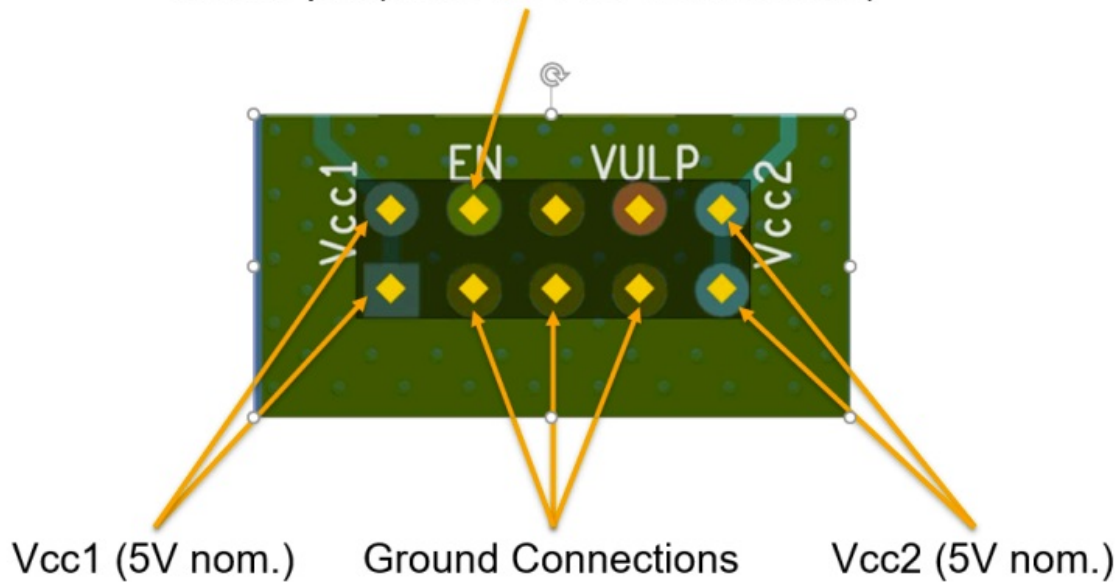
Figure 2. EVB schematic



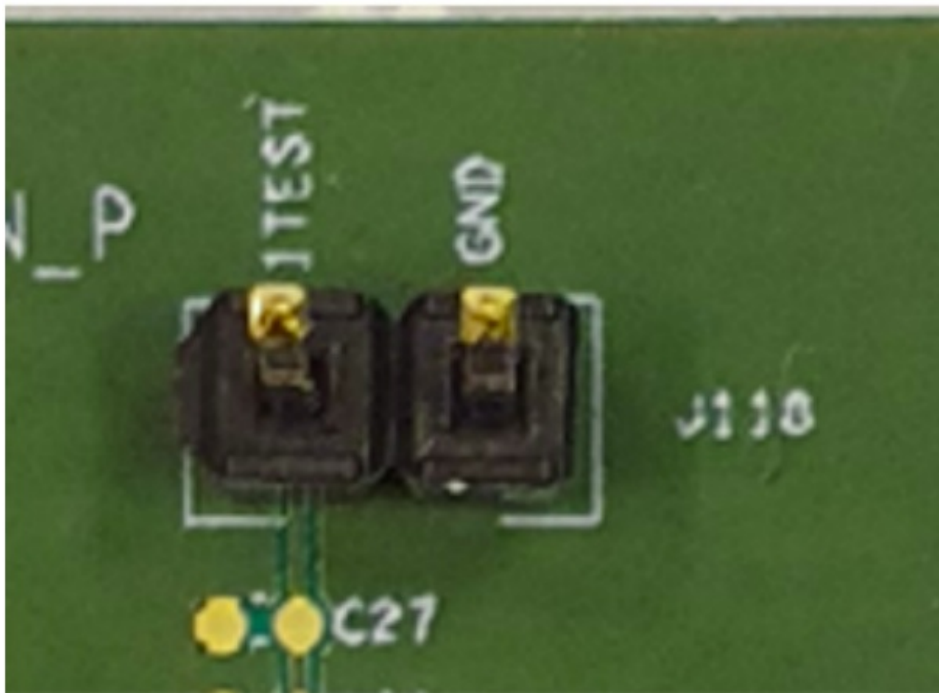
DC and control connections

Figure 3

Enable pin (nom. 1.8V for active mode)



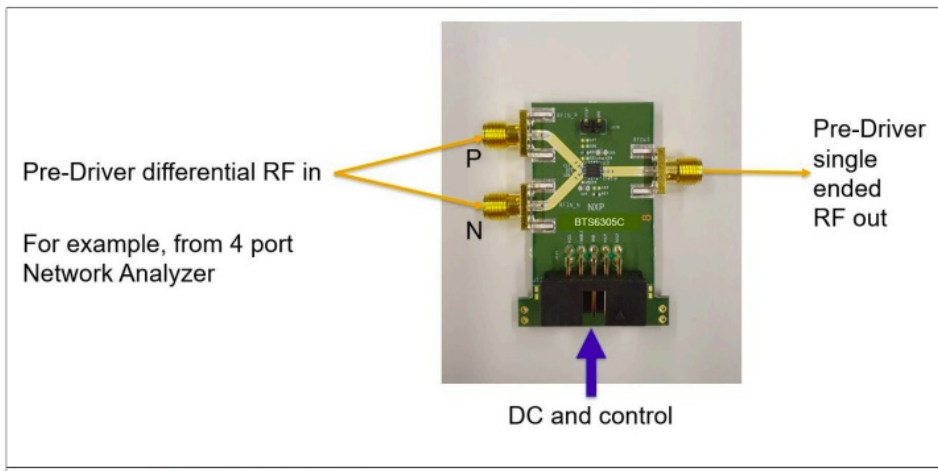
shows the DC and enable connection, at the main header (J117). The connector can be straight or 90 degrees. The text is also on the EVB. **Figure 4**



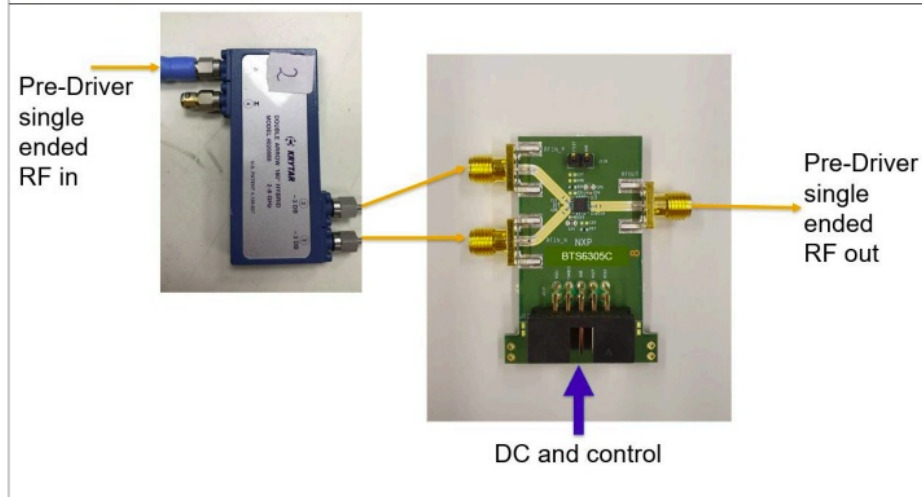
shows connectors J118 GND, and J1 ISET. J1 (ISET) on the EVB has no functionality for BTS6305C and should be left open.

Operations

Figure 5



shows the connections in a single ended to single ended operation. **Figure 6**



shows the connections on differential to single ended operation.

If only single ended to single ended operation is needed, NXP refers to UM11646 of the BTS6305C.

Measurements results

In the below graphics, the Spar measurements are shown. Measured at nominal conditions $V_{CC} = 5\text{ V}$, $T_{case} = 25\text{ }^{\circ}\text{C}$.

Figure 7. Differential gain at (typical values). $V_{CC} = 5\text{ V}$, $P_i = -25\text{ dBm}$

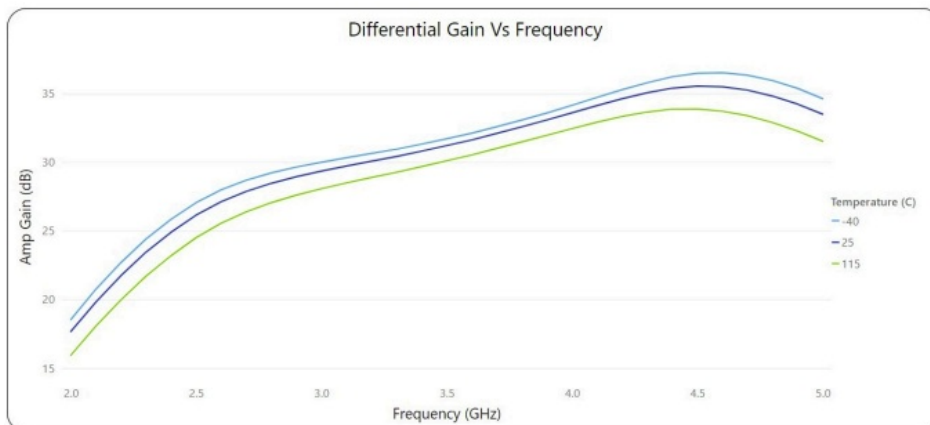


Figure 8. Differential RLi (typical values). $V_{CC} = 5\text{ V}$, $P_i = -25\text{ dBm}$

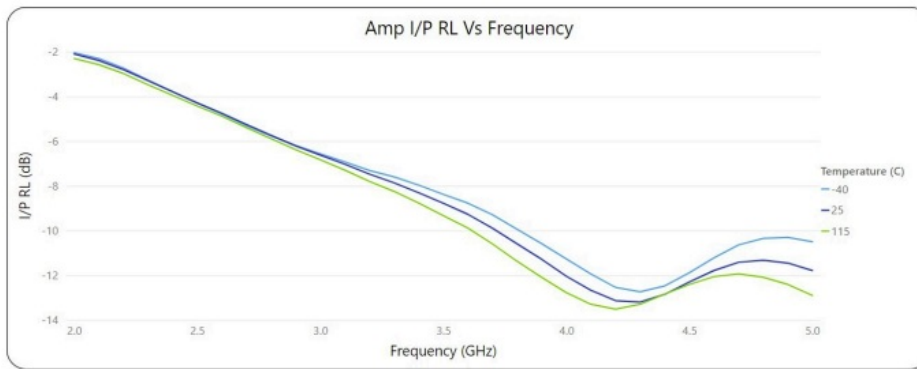


Figure 9. RLo (typical values). Vcc = 5 V, Pi = -25 dBm

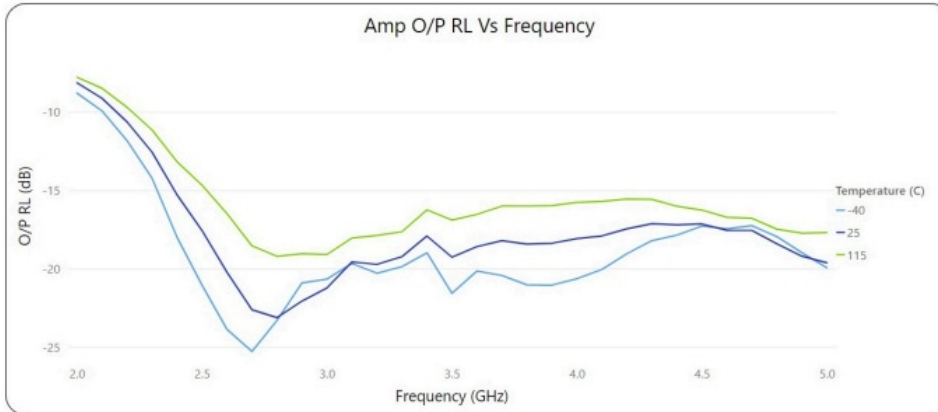
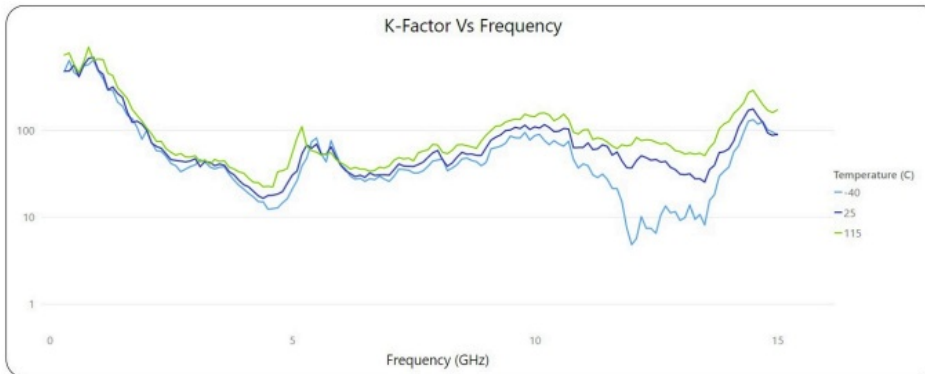


Figure 10. K-factor (typical values). Vcc = 5 V, Pi = -25 dBm



Abbreviations

Table 1. Abbreviations

Acronym	Description
ACLR	adjacent channel leakage ratio
ESD	electrostatic discharge
EVB	evaluation board
m MIMO	massive multiple-input multiple-output
RF	radio frequency
TDD	time-division duplexing

EMC information

CAUTION



This product has not undergone formal EMC assessment. It is the responsibility of the user to ensure that any finished assembly complies with applicable regulations on EMC interference. EMC testing, and other testing requirements for CE is the responsibility of the user.

Revision history

Table 2. Revision history

Document ID	Release date	Description
UM11926 Rev. 1. 1	15 April 2024	<ul style="list-style-type: none">Updated Legal information and brought to current standard
UM11926 Rev. 1	3 May 2023	<ul style="list-style-type: none">Initial release of user manual

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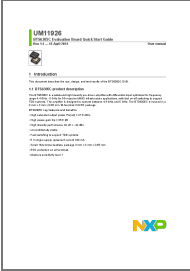
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References

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