

NXP MC56F80000 Evaluation Kit User Guide

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QUICK START GUIDE MC56F80000-EVK MC56F80000 EVALUATION KIT



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GET TO KNOW THE MC56F80000-EVK

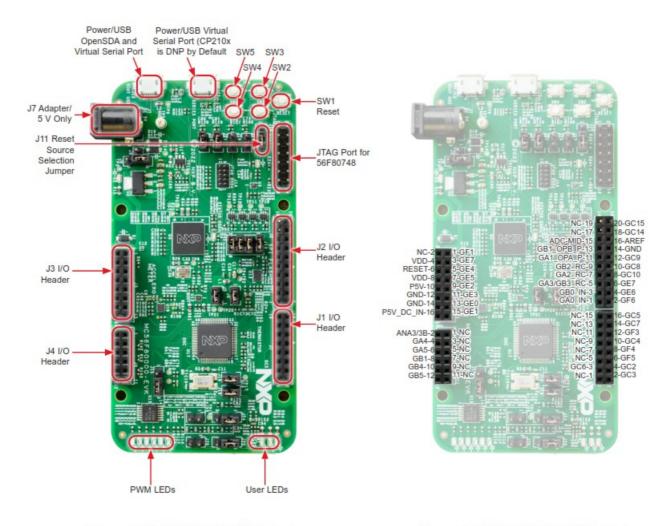


Figure 1: MC56F80000-EVK Callouts

Figure 2: MC56F80000-EVK Pin-Outs

OPERATION NOTES

- 1. MC56F80000-EVK can be powered by either of the USB connectors (J12, J26), or the adapter input (J7). Beware that only an adapter with 5V output can be used.
- 2. When the board is powered up, a green LED D2 will illuminate, indicating 3.3V is on. If the board is powered by USB connector (J12), an orange LED D4 will also illuminate, indicating K26 is powered.
- 3. J12 is the on-board OPENSDA (realized by K26) connector which can be used to debug/program MC56F80748. It provides a virtual serial port as well.
- 4. J26 is the virtual serial port connector. CP210x USB to UART bridge VCP Drivers are needed. CP210x is not populated by default on the board.

- 5. J10 is the JTAG connector for MC56F80748. Remember to remove four jumpers on J13 when this JTAG is used. This is to avoid the impact of on-board OPENSDA circuit.
- 6. Pin2 and pin3 of J11 are connected by default to enable the external reset from SW1.



Download installation software and documentation under "Jump Start Your Design" at www.nxp.com/MC56F80000-EVK.

SUPPORT

Visit <u>www.nxp.com/support</u> for a list of phone numbers within your region.

WARRANTY

Visit www.nxp.com/warranty for complete warranty information.

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Documents / Resources



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References

- MC56F80000-EVK | NXP Semiconductors
- Support | NXP Semiconductors
- Returns and Warranty Information | NXP Semiconductors

Manuals+,