

NXP KV3x-100-120 MHz Advanced 3ph FOC Sensorless Motor Control MCUs based on Arm Instructions

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NXP KV3x-100-120 MHz Advanced 3ph FOC Sensorless Motor
Control MCUs based on Arm Instructions



Kinetis® KV3x-100-120 MHz, Advanced 3ph FOC / Sensorless Motor Control MCUs based on Arm® Cortex®-M4

KV3x

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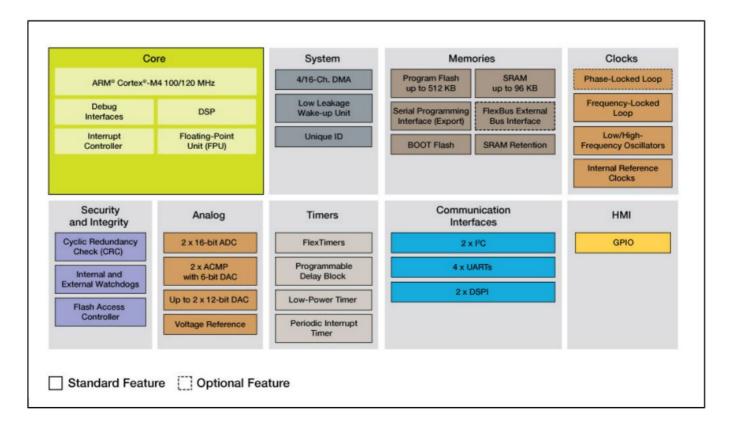
The Kinetis® KV3x family of MCUs delivers a high-performance solution for BLDC, PMSM and ACIM motor control applications.

Built upon the Arm® Cortex®-M4 core running at 100 or 120 MHz with DSP instruction set and floating point unit, the KV3 devices feature dual 16-bit analog-to-digital converters (ADCs) sampling at up to 1.2 mega samples per second (MSPS) in 12-bit mode, multiple motor control timers, 64 to 512 KB of flash memory and a comprehensive enablement suite from proprietary and third-party resources, including reference designs for ACIM, BLDC and PMSM motor control built on NXP®'s. Embedded Software libraries and motor configuration tools.

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Kinetis V Series KV3x MCUs Block Diagram Block Diagram



View additional information for <u>Kinetis® KV3x-100120 MHz</u>, <u>Advanced 3ph FOC / Sensorless Motor Control MCUs based on Arm® Cortex®-M4</u>.

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Documents / Resources



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References

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