



**PCIe-5775  
FlexRIO Digitizer  
Device Kintex**



# National Instruments PCIe-5775 FlexRIO Digitizer Device Kintex User Guide

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**National Instruments PCIe-5775 FlexRIO Digitizer Device Kintex**



### Specifications:

- **Product:** PCIe-5775
- **Operating environment:** Indoor use only

### Product Usage Instructions

#### Verifying System Requirements:

To use the PCIe-5775, ensure your system meets the minimum requirements. Refer to the readme file at [ni.com/manuals](https://ni.com/manuals) for detailed information on system requirements, recommended systems, and supported application development environments.

#### Unpacking the Kit:

**Notice:** Prevent electrostatic discharge by grounding yourself before handling the device.

1. Touch the antistatic package to a metal part of the computer chassis.
2. Inspect the device for any loose components or damage. Do not touch the exposed pins of connectors. Do not install a damaged device.
3. Unpack all items and documentation from the kit. Store the device in the antistatic package when not in use.

#### Preparing the Environment:

Ensure the operating environment meets the specified ambient temperature, relative humidity, and pollution degree requirements. For complete specifications, refer to the documentation available at [ni.com/manuals](https://ni.com/manuals).

#### Installing Software and Driver Using NI Package Manager: “

Follow the instructions provided by the NI Package Manager to install the required software and drivers for the PCIe-5775.

## Getting Started with the PCIe-5775

This document explains how to install, configure, test, and use the PCIe-5775. You can program the PCIe-5775 with the following software options.

- FlexRIO driver software
- NI LabVIEW Instrument Design Libraries for FlexRIO (instrument design libraries)

**Note** Adapter modules are not installable or interchangeable on the PCIe-5775.C

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## FlexRIO Documentation and Resources

**Table 1. FlexRIO Documentation and Resources**

Document/Resource	Location	Description
<b>PCIe-5775 Getting Started Guide</b> (this document)	Available at <a href="https://ni.com/manuals">ni.com/manuals</a> .	Contains installation instructions and basic programming instructions for your PCIe-5775.
<b>PCIe-5775 Specifications</b>	Available at <a href="https://ni.com/manuals">ni.com/manuals</a> .	Contains specifications for your PCIe-5775.
<b>PCIe-5775 Safety, Environmental, and Regulatory Information</b>	Available at <a href="https://ni.com/manuals">ni.com/manuals</a> .	Contains important safety, environmental, and regulatory information for your PCIe-5775.
<b>LabVIEW FPGA Module Help</b>	Embedded in <b>LabVIEW Help</b> and at <a href="https://ni.com/manuals">ni.com/manuals</a> .	Contains information about the basic functionality of the LabVIEW FPGA Module.
<b>FlexRIO Help</b>	Available at <a href="https://ni.com/manuals">ni.com/manuals</a> .	Contains information about the FPGA module front panel connectors and I/O,

Document/Resource	Location	Description
		programming instructions, and I/O component-level IP (CLIP).
LabVIEW Examples	Available in NI Example Finder.  InxLaambVpIIeEws, cHlicakrdHwel apre» Flninpdut  »  and Output » FlexRIO.	Contains examples of how to run F PGA VIs and Host VIs on your Flex RIO devices.
FlexRIO product page	Located at <a href="https://ni.com/flexrio">ni.com/flexrio</a> .	Contains product information and data sheets for FlexRIO hardware.

## Verifying the System Requirements

To use the PCIe-5775, your system must meet certain requirements. For more information about minimum system requirements, recommended system, and supported application development environments (ADEs), refer to the readme, which is installed or available at [ni.com/manuals](https://ni.com/manuals).

## Unpacking the Kit

**Notice** To prevent electrostatic discharge (ESD) from damaging the device, ground yourself using a grounding strap or by holding a grounded object, such as your computer chassis.

1. Touch the antistatic package to a metal part of the computer chassis.
2. Remove the device from the package and inspect the device for loose components or any other sign of damage.

**Notice** Never touch the exposed pins of connectors.

**Note** Do not install a device if it appears damaged in any way.

3. Unpack any other items and documentation from the kit.

Store the device in the antistatic package when the device is not in use.

## PCIe-5775 Kit Contents

The following items are included in the device kit:

- PCIe-5775
- **Documentation:**
  - PCIe-5775 Getting Started Guide (this document)
  - PCIe-5775 Safety, Environmental, and Regulatory Information

## Preparing the Environment

Ensure the environment in which you are using the PCIe-5775 meets the following specifications.

Operating environment	
Ambient temperature range	0 °C to 45 °C (Tested in accordance with IEC-60068-2-1 and IEC-60068-2-2. Meets MIL-PRF-28800F Class 3 low temperature limit and MIL-PRF-28800F Class 4 high temperature limit.)
Relative humidity range	10% to 90%, noncondensing (Tested in accordance with IEC 60068-2-56.)
Pollution Degree	2

**Note** For complete specifications, refer to the specifications document for your device at [ni.com/manuals](http://ni.com/manuals).

## Installing the Software and Driver Using NI Package Manager

Before installing your hardware, you must install the application software and instrument driver. Install the software in the following order:

### 1. Install LabVIEW.

1. Refer to the LabVIEW Installation Guide for installation instructions for LabVIEW and system requirements for the LabVIEW software. Refer to the LabVIEW Upgrade Notes for additional information about upgrading to the most recent version of LabVIEW for Windows. Documentation for LabVIEW is available at [ni.com/manuals](http://ni.com/manuals).

### 2. Install the LabVIEW FPGA Module.

Refer to the LabVIEW FPGA Module Release and Upgrade Notes for installation instructions and information about getting started with the LabVIEW FPGA Module. Documentation for the LabVIEW FPGA Module is available at [ni.com/manuals](http://ni.com/manuals).

### 3. (Optional) Install the LabVIEW Real-Time Module.

Refer to the LabVIEW Real-Time Module Release and Upgrade Notes for system requirements, installation instructions, and additional information about using the LabVIEW Real-Time Module.

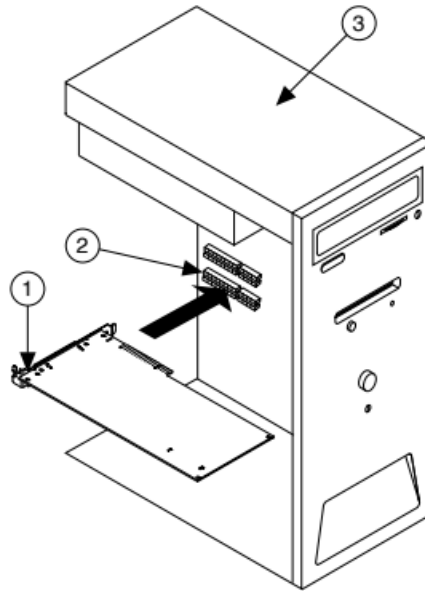
### 4. Install FlexRIO.

Refer to the FlexRIO Readme for system requirements and installation instructions for FlexRIO. Documentation for FlexRIO is available at [ni.com/manuals](http://ni.com/manuals).

## Installing the PCIe-5775 Module into PCIe Chassis

1. Power off and unplug the computer.
2. Access the computer system expansion slots. This step might require you to remove one or more access panels on the computer case.
3. Locate a compatible slot and remove the corresponding slot cover on the computer back panel.
4. Touch any metal part of the computer to discharge any static electricity.
5. Insert the module into the slot you selected. Gently rock the module into place without forcing it.

### Module Installation



1. Module
2. System Expansion Slot
3. PC

6. Secure the PCI Express mounting bracket of the module to the computer chassis with two bracket screws.

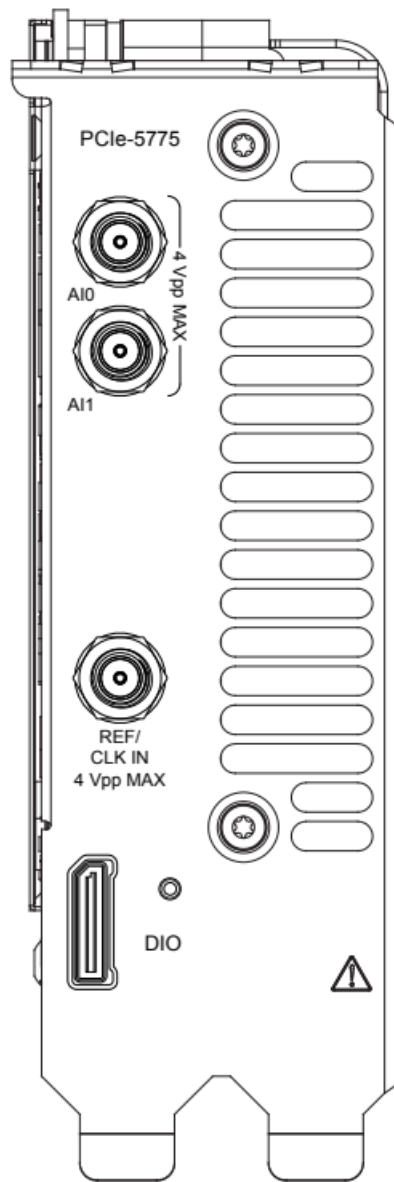
**Note** Because of the high mass of the PCI Express FlexRIO devices, installing the bracket screws is recommended. Installing the bracket screws increases mechanical stability and electrically connects the front panel to the chassis, which can improve signal quality and electromagnetic performance.

7. Connect the 6-pin PCI Express power connector from the power supply to the PCI Express FlexRIO device.
8. Reinstall any access panels on the computer case.
9. Power on your computer.

## PCIe-5775 Front Panel and Pinout

### Front Panel

The following figure shows the PCIe-5775 front panel.



The following table describes the signal connections for the PCIe-5775.

Connector	Description	Function
DIO	Molex Nano-Pitch DIO connector	Multi-signal DIO connector that provides access to FPGA multi-gigabit transceivers (MGTs) and general-purpose LVCMOS signals.
AI 0	Standard SMA female connector	Analog input connection.
AI 1		
REF/CLK IN	Standard SMA female connector	Input for an external Reference Clock or Sample Clock.

### Digital I/O Pinout

The following figure shows the Digital I/O (DIO) connector pinout.

Reserved	A1	B1	5 V
GND	A2	B2	GND
MGT Rx+ 0	A3	B3	MGT Tx+ 0
MGT Rx- 0	A4	B4	MGT Tx- 0
GND	A5	B5	GND
MGT Rx+ 1	A6	B6	MGT Tx+ 1
MGT Rx- 1	A7	B7	MGT Tx- 1
GND	A8	B8	GND
DIO 4	A9	B9	DIO 6
DIO 5	A10	B10	DIO 7
GND	A11	B11	GND
DIO 0	A12	B12	DIO 2
DIO 1	A13	B13	DIO 3
GND	A14	B14	GND
MGT Rx+ 2	A15	B15	MGT Tx+ 2
MGT Rx- 2	A16	B16	MGT Tx- 2
GND	A17	B17	GND
MGT Rx+ 3	A18	B18	MGT Tx+ 3
MGT Rx- 3	A19	B19	MGT Tx- 3
GND	A20	B20	GND
5.0 V	A21	B21	Reserved

The following table lists the available pins on the DIO connector.

Signal	Type	Direction
MGT Tx± <0..3> <u>[1]</u>	Xilinx UltraScale GTH	Output
MGT Rx± <0..3> <u>[1]</u>	Xilinx UltraScale GTH	Input
DIO <0..7>	Single-ended	Bidirectional
5.0 V	DC	Output
GND	Ground	—

- Notice The maximum input signal levels are valid only when the module is powered on. To avoid permanent damage to the PCIe-5775, do not apply a signal to the device when the module is powered down.
- Notice Connections that exceed any of the maximum ratings of any connector on the PCIe-5775 can damage the device and the system. NI is not liable for any damage resulting from such connections.

## Configuring the PCIe-5775 in MAX

Use Measurement & Automation Explorer (MAX) to configure your NI hardware. MAX informs other programs about which NI hardware products are in the system and how they are configured. MAX is automatically installed with FlexRIO.

1. Launch MAX.



2. In the configuration tree, expand Devices and Interfaces to see the list of installed NI hardware.  
**Note** If you do not see your module listed, press <F5> to refresh the list of installed modules. If the module is still not listed, power off the system, ensure the module is correctly installed, and restart.
3. Record the identifier MAX assigns to the hardware. Use this identifier when programming the PCIe-5775.
4. Self-test the hardware by selecting the item in the configuration tree and clicking Self-Test in the MAX toolbar.

The MAX self-test performs a basic verification of hardware resources.

### **FlexRIO Examples**

FlexRIO includes several example applications for LabVIEW. These examples serve as interactive tools, programming models, and as building blocks in your own applications.

### **Accessing FlexRIO Examples**

FlexRIO examples are available in LabVIEW's NI Example Finder. Complete the following steps to access the examples by task.

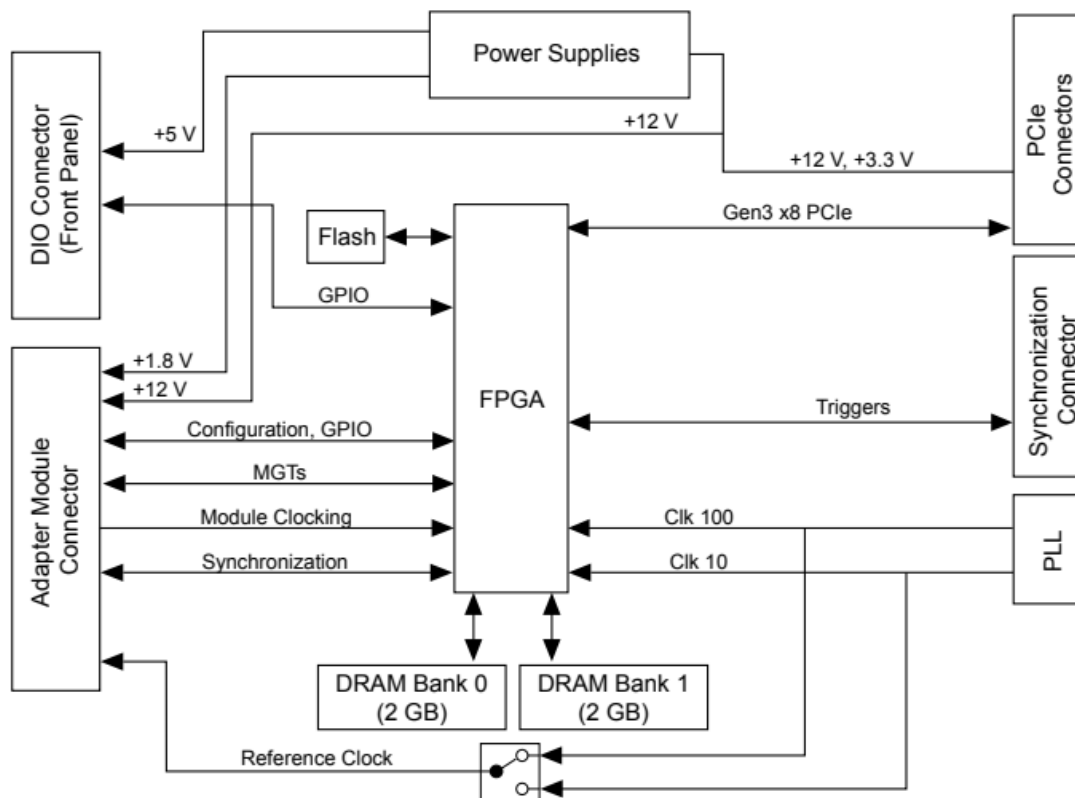
1. In LabVIEW, click Help » Find Examples.
2. In the NI Example Finder window that appears, click Hardware Input and Output » FlexRIO.

Click on an example and refer to the Information window for a description of the example. Refer the Requirements window for a list of hardware that can run the example. You can also click the Search tab to search all installed examples by keyword. For example, search for FlexRIO to locate all FlexRIO examples. Online examples are also available to demonstrate FlexRIO basics, such as using DRAM, acquiring data from adapter modules, and performing high-throughput streaming. To access these examples, search FlexRIO examples in the Search the community field at [ni.com/examples](https://ni.com/examples).

### **Block Diagram**

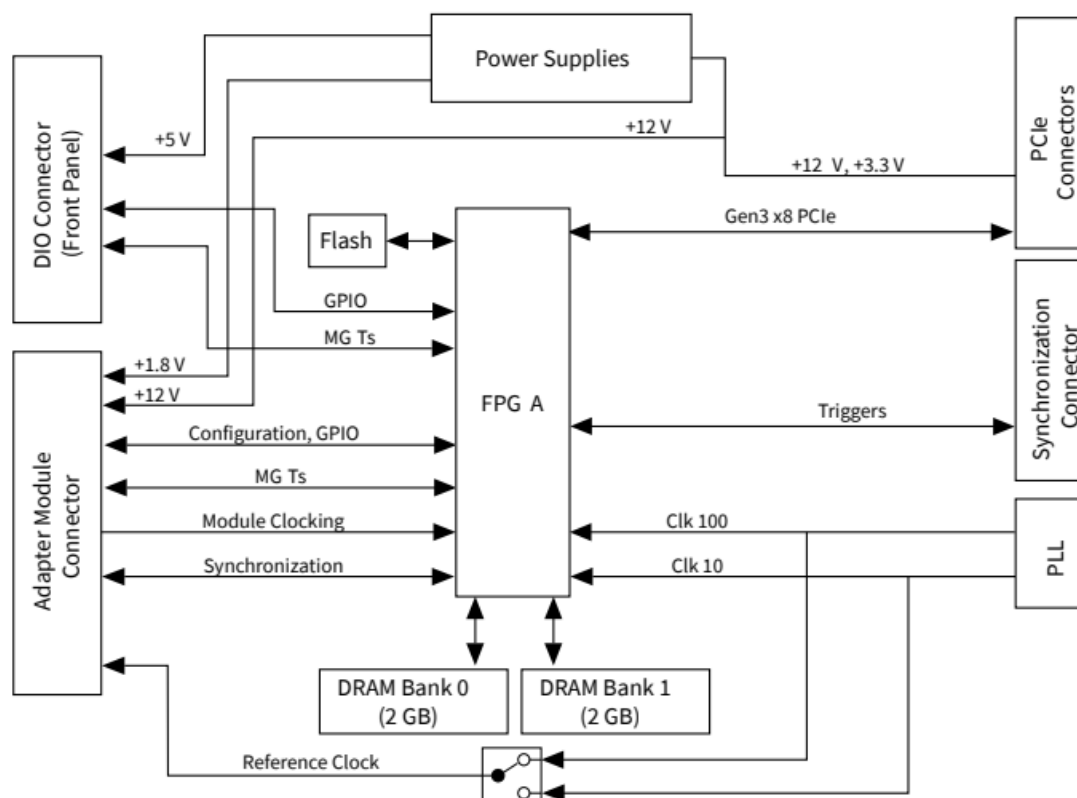
The following figure shows a block diagram of the carrier portion of the PCIe-5775 (KU035 FPGA version).

### **Carrier Block Diagram (KU035)**



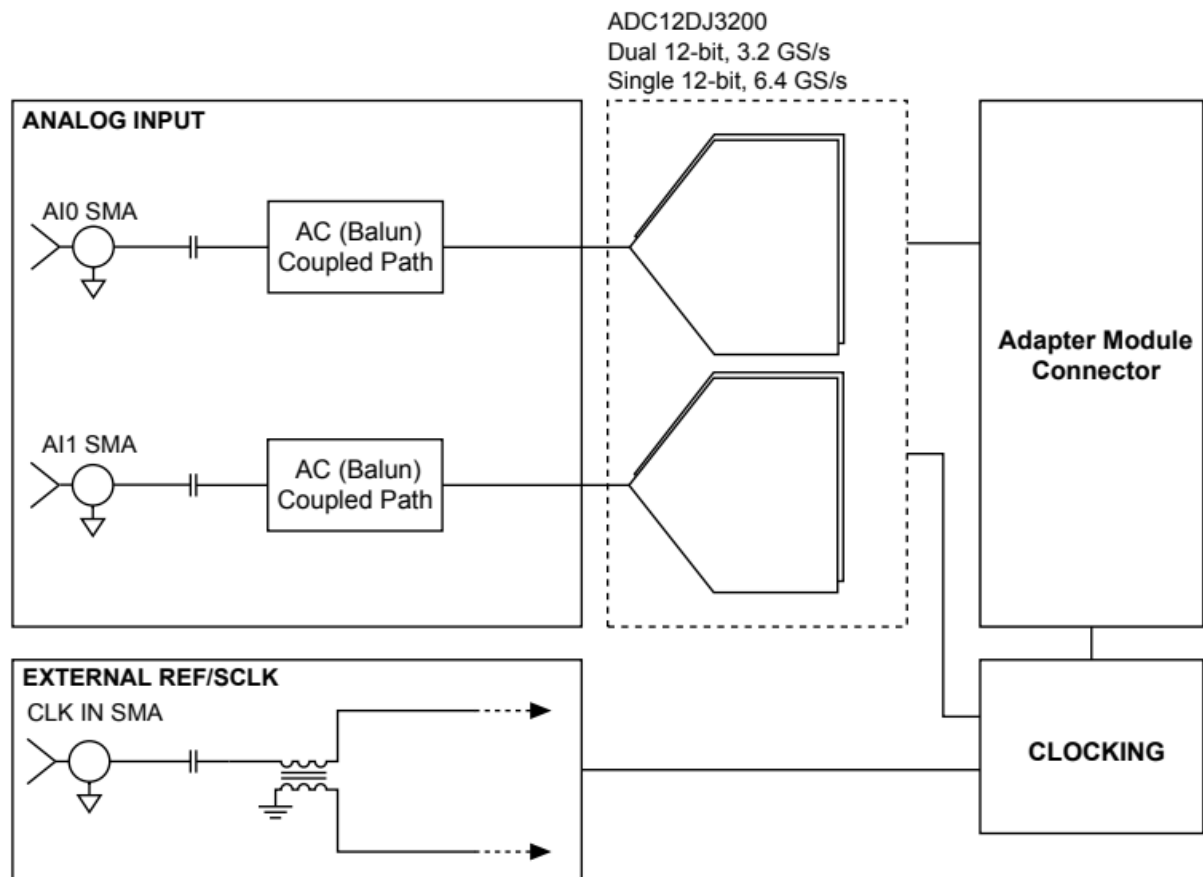
The following figure shows a block diagram of the carrier portion of the PCIe-5775 (KU040 and KU060 FPGA versions).

#### Carrier Block Diagram (KU040 and KU060)



The following figure shows a block diagram of the I/O portion of the PCIe-5775.

#### PCIe-5775 Block Diagram



## Component-Level Intellectual Property (CLIP)

The LabVIEW FPGA Module includes component-level intellectual property (CLIP) for HDL IP integration. FlexRIO devices support two types of CLIP: user-defined and socketed.

- User-defined CLIP allows you to insert HDL IP into an FPGA target, enabling VHDL code to communicate directly with an FPGA VI.
- Socketed CLIP provides the same IP integration of the user-defined CLIP, but it also allows the CLIP to communicate directly with circuitry external to the FPGA. Adapter module socketed CLIP allows your IP to communicate directly with both the FPGA VI and the external adapter module connector interface.

The PCIe-5775 ships with socketed CLIP items that add module I/O to the LabVIEW project.

## Making a Measurement with LabVIEW

1. Launch LabVIEW.
2. Select Help » Find Example.
3. Open the example VI that you want to use by selecting Hardware Input and Output » FlexRIO.
4. Follow any setup, configuration, and execution instructions in the VI.

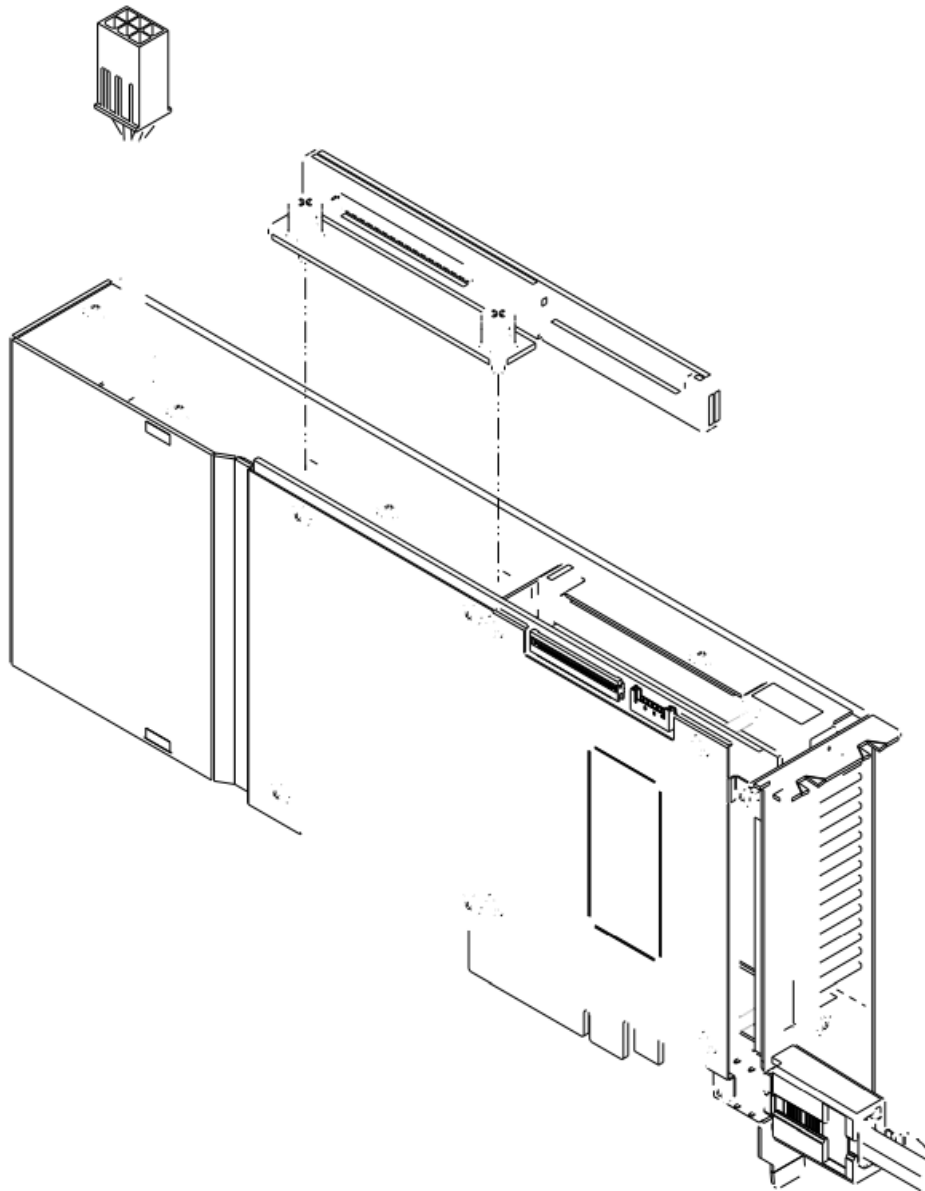
## Synchronizing Reference Clocks and Triggers across PCI Express devices

To synchronize the PCIe-5775 with another PCI Express FlexRIO device, you need the following accessories:

- Compact Synchronization Interface Cable, NI part number 787213-01 To synchronize the PCIe-5775 with any PCI Express device that supports RTSI synchronization, you need the following accessories:

- Compact Synchronization Interface to RTSI Adapter, NI part number 787214-01
- RTSI cable, NI part number 776249-02

## Synchronization Connectors on the PCIe-5775



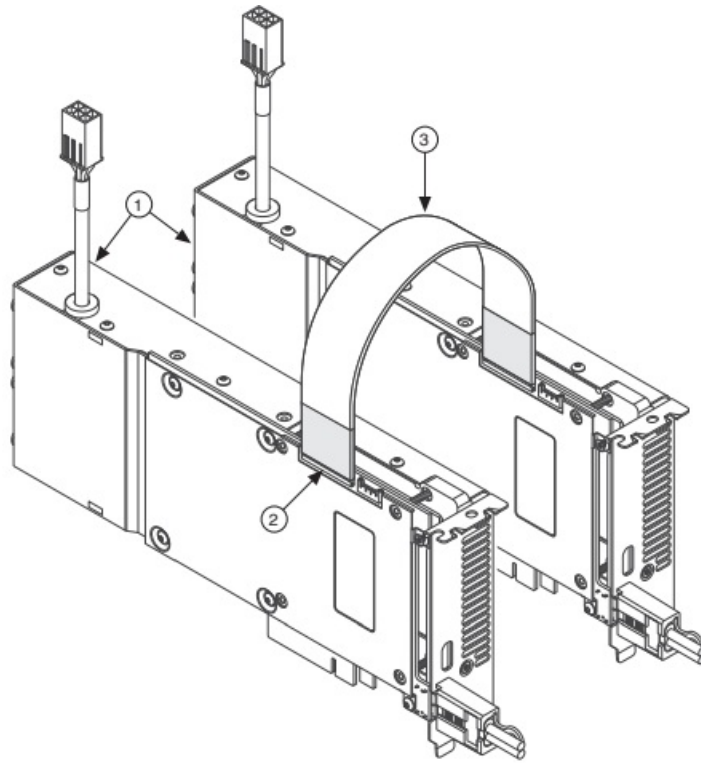
1. Compact synchronization cable connector
2. Compact synchronization cable to RTSI adapter

## Sharing Reference Clocks and Triggers with Another FlexRIO PCI Express Device

**Note** To synchronize the PCIe-5775 with another FlexRIO PCI Express device, you need the Compact Synchronization Interface Cable, NI part number 787213-01.

1. Install the PCIe-5775 and another PCI Express FlexRIO device into one PCI Express backplane.
2. Insert one end of the Compact Synchronization Interface Cable into the synchronization connector on each PCI Express FlexRIO device. Ensure the contacts on the cable are facing the FlexRIO device.

## Connecting Two FlexRIO PCI Express Devices



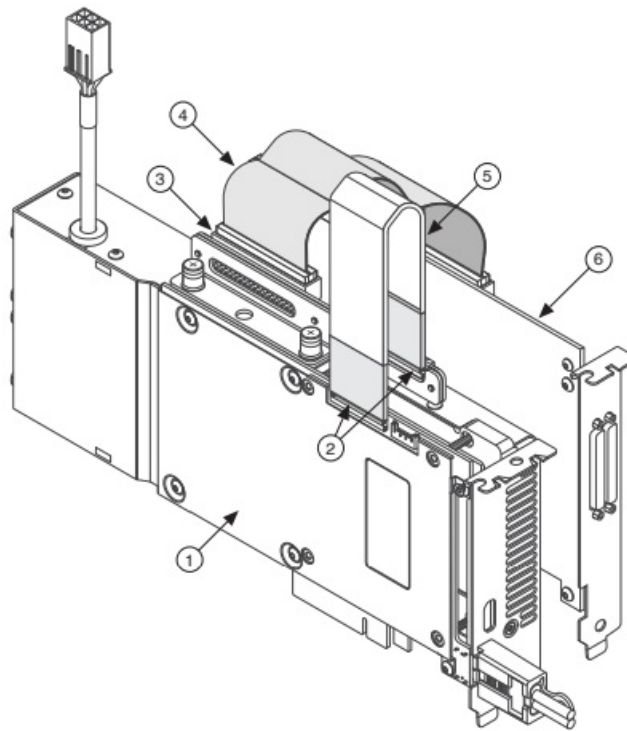
1. FlexRIO PCI Express devices
2. Compact synchronization cable connector
3. Compact Synchronization Interface Cable

## Sharing Reference Clocks and Triggers with PCI Express Devices Using RTSI

**Note** To synchronize the PCIe-5775 with a RTSI-supported PCI Express device, you need the Compact Synchronization Interface to RTSI Adapter, NI part number 787214-01, and a RTSI cable, NI part number 776249-02.

1. Affix the RTSI adapter to the top of the PCIe-5775 using the attached mounting screws.
2. Install the PCIe-5775 and up to five additional PCI Express devices in one PCI Express backplane.
3. Insert one end of the compact synchronization interface cable into the synchronization connector on the PCIe-5775. Ensure the contacts on the cable are facing the PCIe-5775.
4. Insert the other end of the compact synchronization interface cable into the closest synchronization connector on the RTSI adapter.
5. Connect one RTSI female connector on the RTSI cable to the RTSI male connector on the compact synchronization interface to RTSI adapter on the PCIe-5775. Connect the remaining RTSI female connectors on the RTSI cable to the male RTSI connectors on the other PCI Express devices.

## Connecting FlexRIO PCI Express Devices to RTSI-Supported PCI Express Devices



1. FlexRIO PCI Express device
2. Compact synchronization cable connector
3. Compact synchronization interface to RTSI adapter
4. RTSI cable
5. Compact synchronization interface cable
6. RTSI-supported PCI Express device

## Troubleshooting

If an issue persists after you complete a troubleshooting procedure, search our KnowledgeBase for additional information our technical support engineers create as they answer common user questions and resolve unexpected issues.

### What Should I Do if the PCIe-5775 Does Not Appear in MAX?

1. In the MAX configuration tree, expand Devices and Interfaces.
2. Press <F5> to refresh the list.
3. If the module is still not listed, power off the system, ensure that all hardware is correctly installed, and restart the system.
4. Navigate to the Device Manager by right-clicking the Start button, and selecting Device Manager.
5. Verify the PCIe-5775 appears in the Device Manager.
  1. Under an NI entry, confirm that a PCIe-5775 entry appears.
 

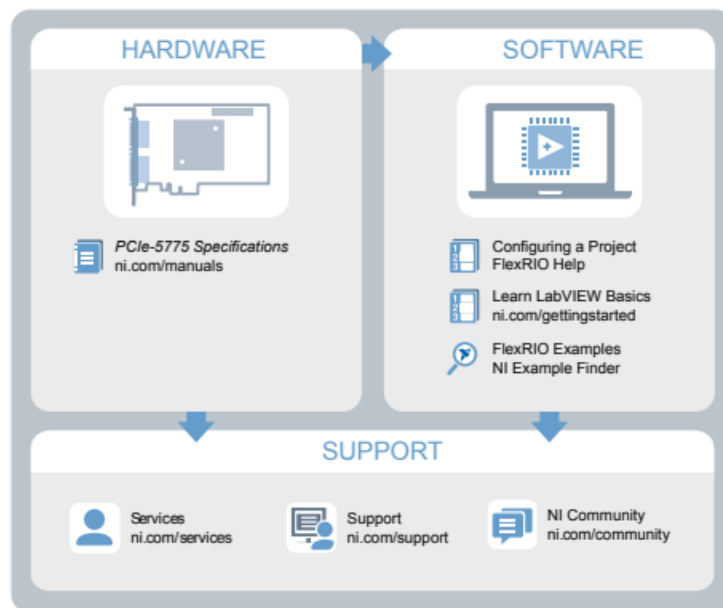
**Note** If you are using a PC with a device for PXI remote control system, under System Devices, also confirm that no error conditions appear for the PCI-to-PCI Bridge.
  2. If error conditions appear, reinstall the FlexRIO driver.

### What Should I Do if the PCIe-5775 Fails the Self-Test?

1. Restart the system.
2. Launch MAX, and perform the self-test again.
3. Power off the chassis.
4. Reinstall the failed module in a different slot.
5. Power on the chassis.
6. Perform the self-test again.

### Where to Go Next

Refer to the following figure for information about other product tasks and associated resources for those tasks.



### NI Services

- Visit [ni.com/support](https://ni.com/support) to find support resources including documentation, downloads, and troubleshooting and application development self-help such as tutorials and examples.
- Visit [ni.com/services](https://ni.com/services) to learn about NI service offerings such as calibration options, repair, and replacement.
- Visit [ni.com/register](https://ni.com/register) to register your NI product. Product registration facilitates technical support and ensures that you receive important information updates from NI.

NI corporate headquarters is located at 11500 N Mopac Expwy, Austin, TX, 78759-3504, USA.

[ni.com](https://ni.com)

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### FAQ

#### **Q: Where can I find more information about programming options for the PCIe-5775?**

A: You can refer to the FlexRIO driver software and NI LabVIEW Instrument Design Libraries for FlexRIO (instrument design libraries) for programming options. Additional information is available in the documentation provided at [ni.com/manuals](https://ni.com/manuals).

### Documents / Resources

	<p><a href="#">National Instruments PCIe-5775 FlexRIO Digitizer Device Kintex</a> [pdf] User Guide PCIe-5775 FlexRIO Digitizer Device Kintex, PCIe-5775, FlexRIO Digitizer Device Kintex, Digitizer Device Kintex, Device Kintex, Kintex</p>
	<p><a href="#">National Instruments PCIe-5775 FlexRIO Digitizer Device Kintex</a> [pdf] User Guide KU040, KU060, PCIe-5775 FlexRIO Digitizer Device Kintex, PCIe-5775, FlexRIO Digitizer Device Kintex, Digitizer Device Kintex, Device Kintex, Kintex</p>

References

- [NI Example Programs - NI Community](#)
- [NI Product Documentation - NI](#)
- [User Manual](#)

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