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MICROCHIP MIC2775-44YM5-TR Micro-Power Voltage Supervisor Instruction Manual

Contents

MICROCHIP MIC2775-44YM5-TR Micro-Power Voltage Supervisor

Product Information

The MIC2775 is a power supply supervisor that provides under voltage monitoring, manual reset capability, and power-on reset generation in a compact 5-pin SOT package. It features an under voltage detector, a delay generator, a manual reset input, and both active-high and active-low reset outputs. The under voltage detector compares VDD against a fixed threshold. The MIC2775 is available in ten factory-programmed thresholds. It is compatible with the Itty-bitty trademark of Michel, Inc. Datasheets and support documentation for the MIC2775 can be
found on Marcel's website at: **www.micrel.com**.

Product Usage Instructions

Pin Number	Pin Name	Pin Function
1	/RST	Digital (Output): Asserted low whenever VDD falls below the reference voltage. It will remain asserted for no less than 140ms after VDD returns above the threshold limit.
2	GND	Ground.
3	RST	Digital (Output): Asserted high whenever VDD falls below the reference voltage. It will remain asserted for no less than 140ms after VDD returns above the threshold limit.
4	/MR	Digital (Input): Driving this pin low initiates an immediate and unconditional reset. Assuming VDD is above the threshold when /MR is released (returns high), the reset output will be de-asserted no less than 140ms later. /MR has an internal pull-up to VDD and may be left open if unused.
5	VDD	Analog (Input): Power supply input and the voltage being monitored.

General Description

The MIC2775 is a power supply supervisor that provides undervoltage monitoring, manual reset capability, and power-on reset generation in a compact 5-pin SOT package. Features include an undervoltage detector, a delay generator, a manual reset input, and both active-high and active-low reset outputs. The undervoltage detector compares VDD against a fixed threshold. Ten factory-programmed thresholds are available.

The reset outputs are asserted for no less than 140ms at power-on and any time the input voltage drops below the reference voltage. It remains asserted for the timeout period after the input voltage subsequently rises back above the threshold boundary. A reset can be generated at any time by asserting the manual reset input, /MR. This reset output will remain active at least 140ms after the release of /MR. The /MR input can also be used to daisy-chain the MIC2775 onto existing power monitoring circuitry or other supervisors. Hysteresis is included to prevent chattering due to noise. Typical soppy current is an ultra-low 3.5µA.

Datasheets and support documentation are available on Marcel's website at: **www.micrel.com**.

Features

- Monitors power supply for undervoltage conditions
- Choice of factory-programmed thresholds
- Generates 140ms (minimum) power-on reset pulse
- Manual reset capability
- Both active-high and active-low reset outputs
- RST output valid down to 1.2V
- Ultra-low supply current, 3.5µA typical
- Rejects brief input transients
- No external components
- For extended temperature range see Michel's MAX825
- Itty Bitty® 5-pin SOT-23 package

Applications

- Monitoring processor core and input/output voltages
- Computer systems
- PDAs, handheld PCs
- Embedded controllers
- Telecommunications systems
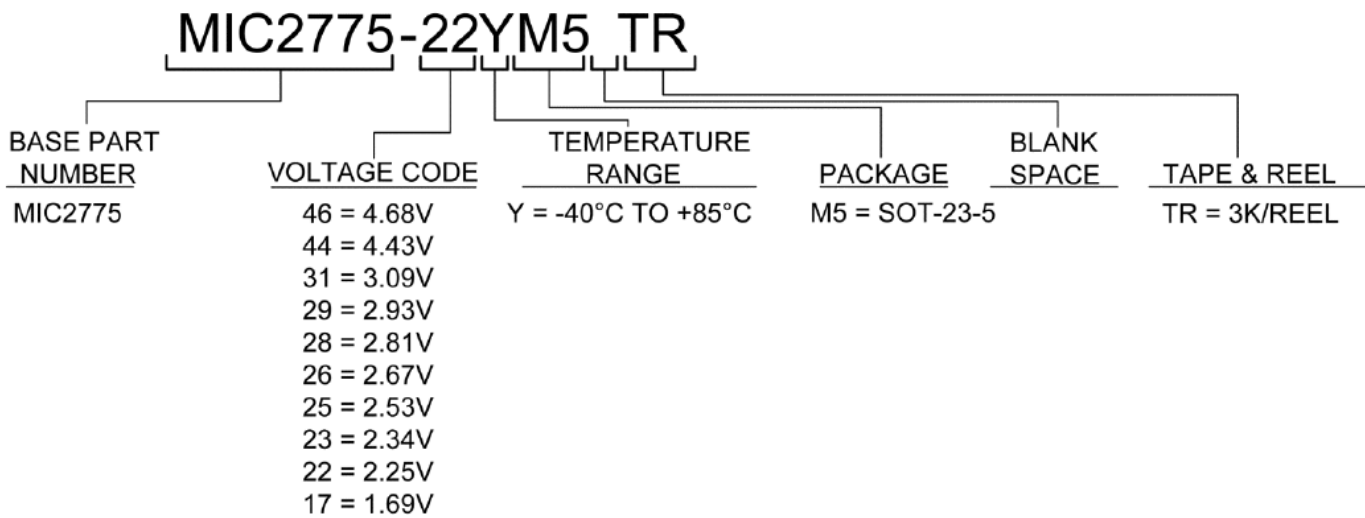- Power supplies

Typical Application
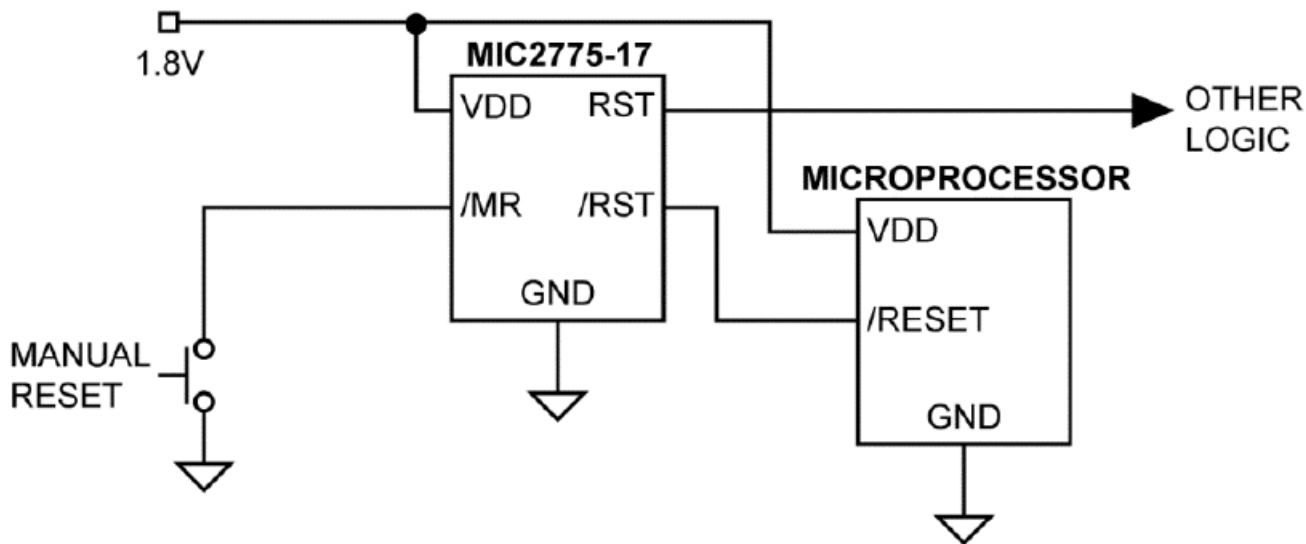
Ordering Information

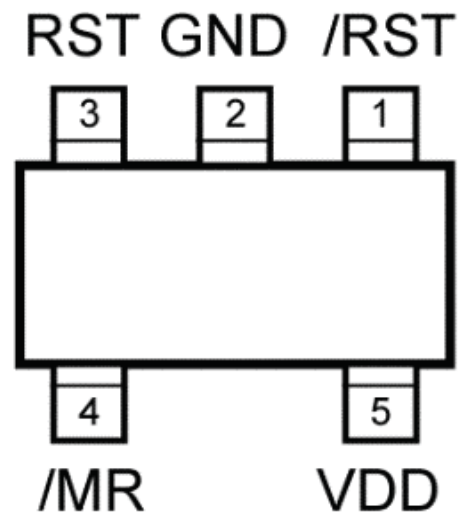| Part Number[1,2]	Marking[1,3]	Junction Temperature Range	Package[4]
MIC2775-XXYM5	UJXX	−40°C to +85°C	SOT-23-5

Note

1. XX = Voltage Code, see Part Numbering Conventions below. There are ten standard versions available with an order increment of 3,000 pieces. Samples of standard versions are normally available from stock. Contact factory for information on non-standard versions. Available in tape-and-reel only.
2. Order entry part number add a space, then TR. Example: MIC2775-22YM5 TR.
3. Underbar (__) symbol may not be to scale.
4. Standard reel SOT-23: Reel diameter is 7 inches, hub diameter is 2 inches, width is 8mm.

Part Numbering Conventions

Pin Configuration

RST GND /RST

SOT-23-5 (M5)
(Top View)

Pin Description

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1	/RST	Digital (Output): Asserted low whenever VDD falls below the reference voltage. It will remain asserted for no less than 140ms after VDD returns above the threshold limit.
2	GND	Ground.
3	RST	Digital (Output): Asserted high whenever VDD falls below the reference voltage. It will remain asserted for no less than 140ms after VDD returns above the threshold limit.
4	/MR	Digital (Input): Driving this pin low initiates an immediate and unconditional reset. Assuming VDD is above the threshold when /MR is released (returns high), the re set output will be de-asserted no less than 140ms later. /MR has an internal pull-up to VDD and may be left open if unused.
5	VDD	Analog (Input): Power supply input and the voltage being monitored.

Absolute Maximum Ratings(5)

Supply Voltage (VDD) 0.3V to +7.0V
Output Voltage (V/MR) –0.3V to +7.0V
RST, (/RST) Current . 20mA
Storage Temperature (Ts) –65°C to +150°C
ESD Rating(7). . 1.5kV

Operating Ratings(6)

Supply Voltage (VDD) +1.5V to +5.5V
Input Voltage (V/MR) –0.3V to +6.0V
Ambient Temperature (TA) 40°C to +85°C
Package Thermal Resistance (θJA) 256°C/W

Electrical Characteristics(8)

TA = 25°C, bold values indicate −40°C≤ TA ≤ +85°C, unless noted.

| Symbol	Parameter	Condition	Min.	Typ.	Max.	Units
IDD	Supply Current	VDD = VTH + 1.6%, /MR, RST, /RST open,		3.5		µA
VDD Voltage Threshold						
	Undervoltage Threshold on VDD		**VTH–1.5%**	VTH	**VTH+1.5%**	V
VHYST	Hysteresis Voltage			1		%
RST, /RST Outputs						
tPROP	Propagation Delay	VDD = VTH + 1.5% + 100mV to VDD = VTH − 1.5% − 100mV		20		µs
tRST	Reset Pulse Width		**140**		**280**	ms
VOL	RST or /RST Output Voltage Low	ISINK = 1.6mA; VDD ≥ 1.6V			**0.3**	V
		ISINK = 100µA; VDD ≥ 1.2V; Note 9			**0.3**	V
VOH	RST or /RST Output Voltage High	ISOURCE = 500µA; VDD ≥ 1.5V	**0.8VDD**			V
		ISOURCE = 10µA; VDD ≥ 1.2V; Note 9	**0.8VDD**			V
/MR Input						
VIH	Input High Voltage		**0.7VDD**			V
VIL	Input Low Voltage				**0.3VDD**	V
tPROP	Propagation Delay	From V/MR < (VIL − 100mV)		5		µs
tMIN	Minimum Input Pulse Width	Reset occurs, V/MR < VIL		33		ns
IPU	Internal Pull-up Current			100		nA
IIN	Input Current, /MR	V/MR < VIL		100		nA

Notes

- Exceeding the absolute maximum ratings may damage the device.
- The device is not guaranteed to function outside its operating ratings.
- Devices are ESD sensitive. Handling precautions are recommended. Human body model, 1.5kΩ in series with

100pF.

- Specification for packaged product only.
- VDD operating range is 1.5V to 5.5V. Output is guaranteed to be asserted down to VDD = 1.2V.
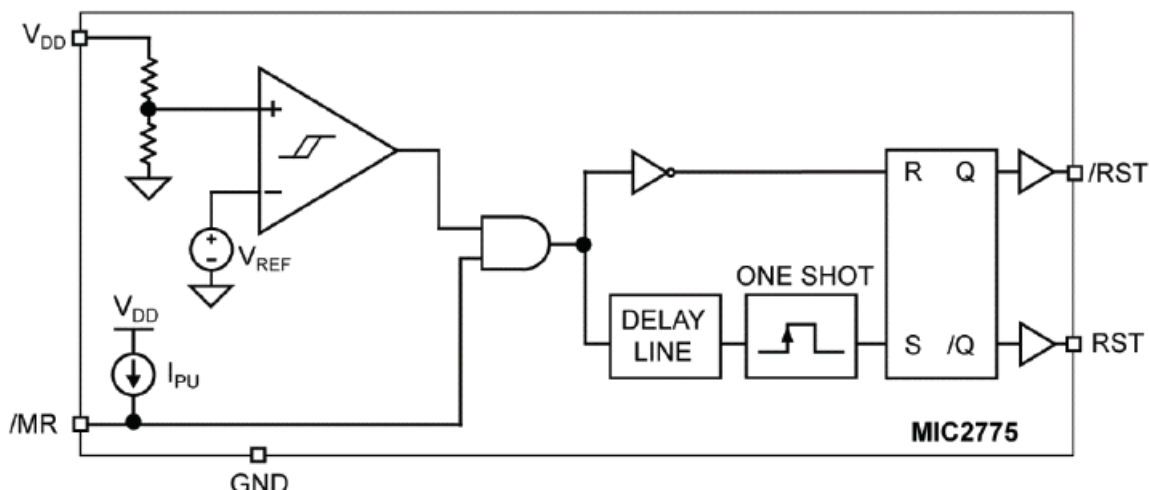
Timing Diagram
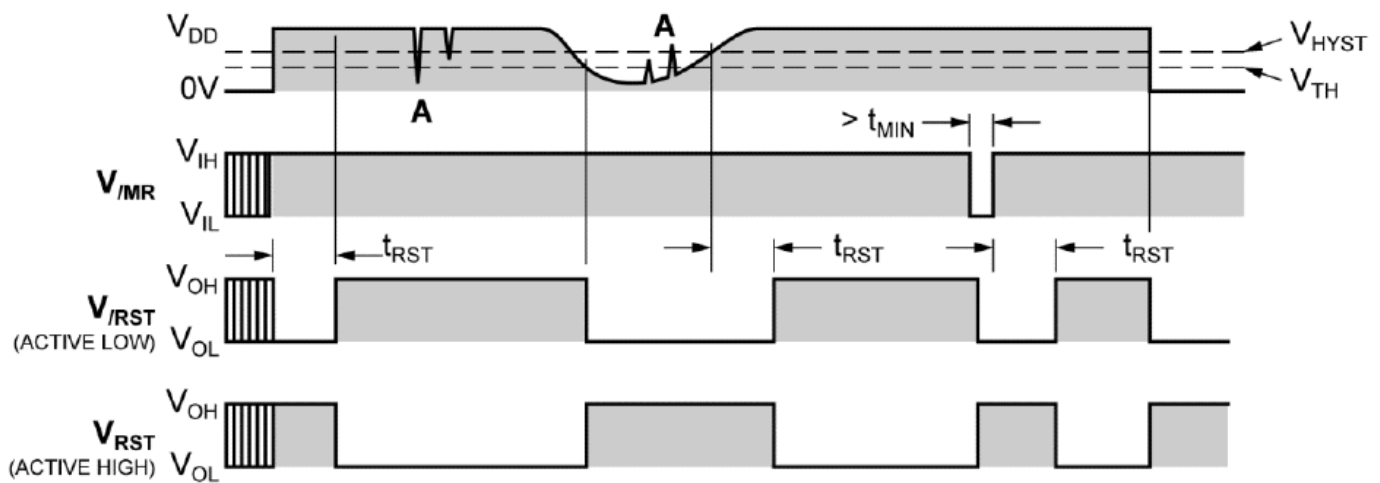
Note: Propagation delays not shown for clarity.

A = The MIC2775 ignores very brief transients. See the Application Information section for details.

Functional Diagram

Functional Description

VDD Input
The VDD pin is both the power supply terminal and a monitored input voltage. The voltage at this pin is continually compared against the internal reference. The trip-point at which a reset occurs is factory-programmed. A reset is triggered if and when VDD falls below the trip-point. Hysteresis is employed to prevent chattering due to noise. The comparator on the VDD input is relatively immune to very brief negative-going transients.

RST, /RST Reset Outputs
Typically, the MIC2775 is used to monitor the power supply of intelligent circuits such as microcontrollers and microprocessors. By connecting the appropriate reset output of an MIC2775 to the reset input of a µC or µP, the processor will be properly reset at power-on, power-down, and brown-out conditions. In addition, asserting /MR, the manual reset input, will activate the reset function.

The reset outputs are asserted any time /MR is asserted or if VDD drops below the threshold voltage. The reset outputs remain asserted for tRST(min) after VDD subsequently returns above the threshold boundary and/or /MR is released. A reset pulse is also generated at power-on. Hysteresis is included in the comparator to prevent chattering of the outputs due to noise.

/MR, Manual Reset Input

The ability to initiate a reset via external logic or a manual switch is provided in addition to the MIC2775's automatic supervisory functions. Driving the /MR input to a logic-low causes an immediate and unconditional reset to occur. Assuming VDD is within tolerance when /MR is released (returns high), the reset outputs will be de-asserted no less than tRST later. /MR may be driven by a logic signal or mechanical switch. Typically, a momentary push-button switch is connected such that /MR is shorted to ground when the switch contact close. The switch may be connected directly between /MR and GND. /MR has an internal 100nA pull-up current to VDD and may be left open if unused.

Application Information

Ensuring Proper Operation at Low Supply
At VDD levels below 1.2V, the MIC2775's /RST output cannot turn on sufficiently to produce a valid logic-low on /RST. In this situation, circuits driven by /RST could be allowed to float, causing undesired operation. In most cases, however, it is expected that the circuits driven by the MIC2775 will be similarly inoperative at VDD ≤ 1.2V.

If a given application requires that /RST be valid below VDD = 1.2V, this can be accomplished by adding a pull-down resistor to the /RST output. A value or 100kΩ is recommended as this is usually an acceptable compromise of quiescent current and pull-down current. The resistor's value is not critical, however. See Figure 1.
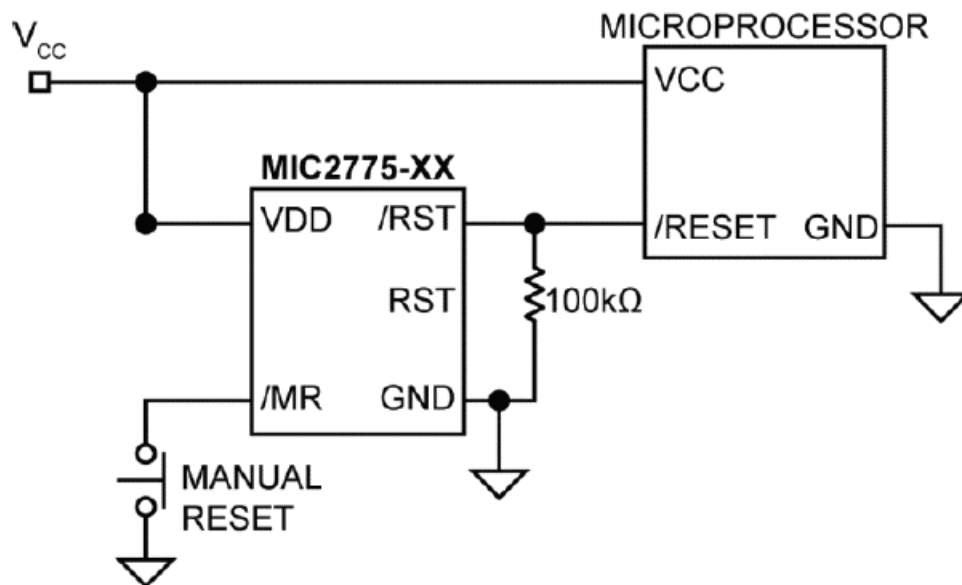

Figure 1. MIC2775 Valid /RST Below 1.2V
The statements above also apply to the MIC2775's RST output. That is, to ensure valid RST signal levels when VDD < 1.2V, a pull-up resistor (as opposed to a pull-down) should be added to the RST output. A value or 100kΩ is typical for this application, as well. See Figure 2.
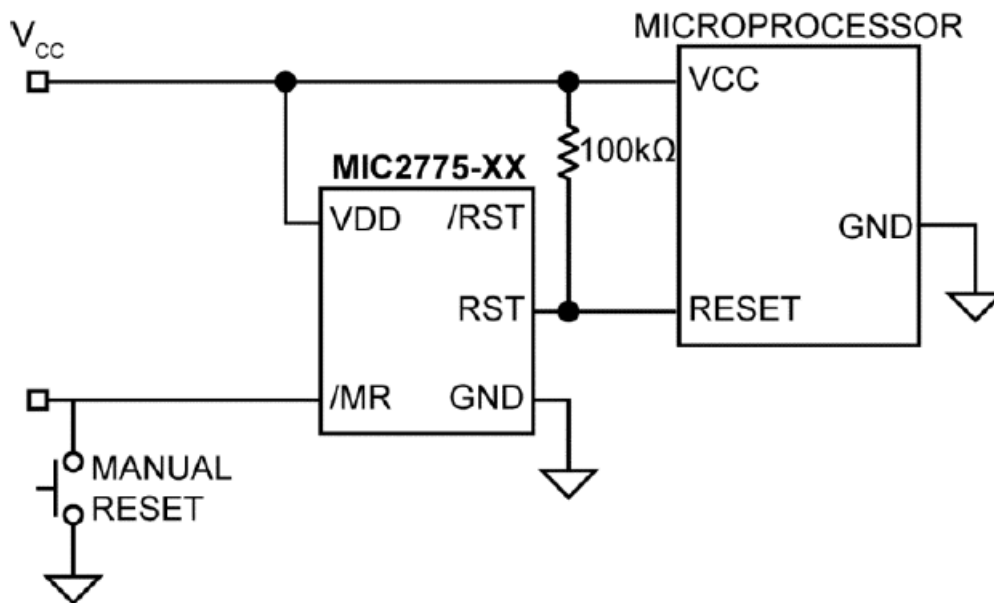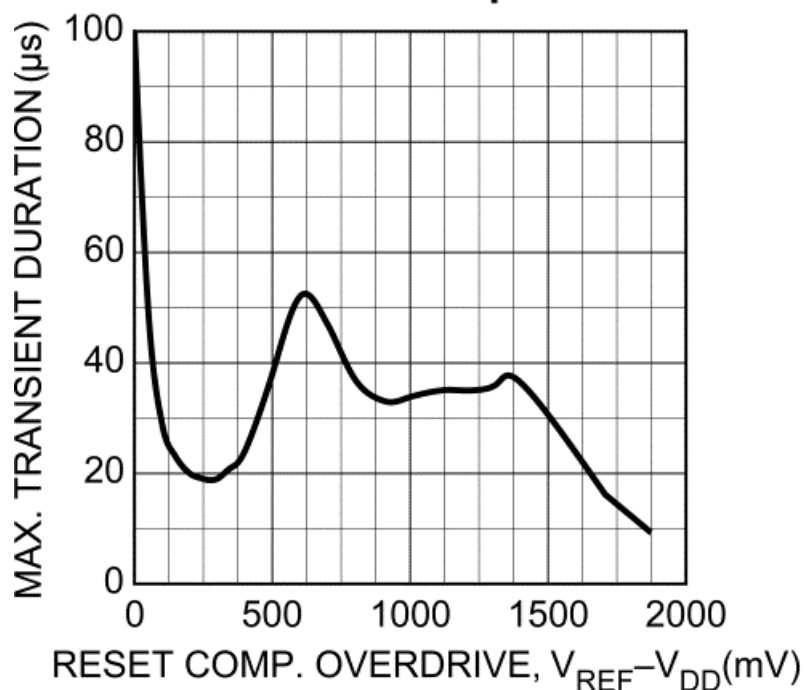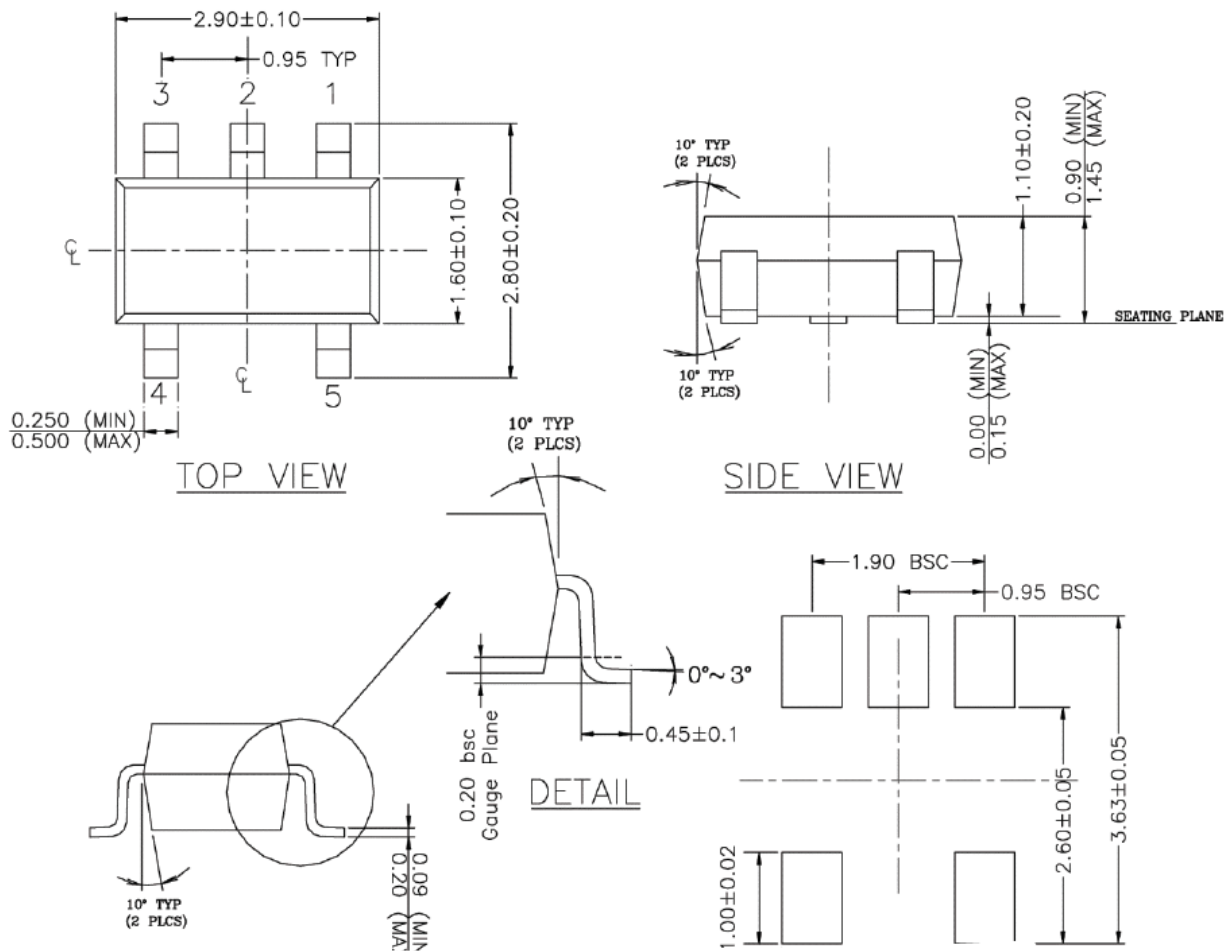
Figure 2. MIC2775 Valid RST Below 1.2V

Transient Response

The MIC2775 is inherently immune to very short negative-going glitches. Very brief transients may exceed the voltage threshold without tripping the output.

In general, as shown in Figure 3, the narrower the transient, the deeper the threshold overdrive that will be ignored by the MIC2775. The graph represents the typical allowable transient duration for a given amount of threshold overdrive that will not generate a reset.

Package Information and Recommended Landing Pattern(10)

TOP VIEW — 2.90±0.10, 0.95 TYP, 1.60±0.10, 2.80±0.20, 0.250 (MIN) / 0.500 (MAX)

SIDE VIEW — 10° TYP (2 PLCS), 1.10±0.20, 0.90 (MIN) / 1.45 (MAX), SEATING PLANE, 0.00 (MIN) / 0.15 (MAX)

DETAIL — 10° TYP (2 PLCS), 0°~3°, 0.45±0.1, 0.20 bsc Gauge Plane, 0.20 (MIN) / 0.60 (MAX)

1.90 BSC, 0.95 BSC, 1.00±0.02, 2.60±0.05, 3.63±0.05

NOTE

1. PACKAGE OUTLINE EXCLUSIVE OF MOLD FLASH & BURR.
2. PACKAGE OUTLINE INCLUSIVE OF SOLER PLATING.
3. DIMENSION AND TOLERANCE PER ANSI Y14.5M, 1982.
4. FOOT LENGTH MEASUREMENT BASED ON GAUGE PLANE METHOD.
5. DIE FACES UP FOR MOLD, AND FACES DOWN FOR TRIM/FORM. 6. ALL DIMENSIONS ARE IN MILLIMETERS.

   Note: Package information is correct as of the publication date. For updates and most current information, go to **www.micrel.com**.

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Documents / Resources

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MIC2775-44YM5-TR Micro-Power Voltage Supervisor, MIC2775-44YM5-TR, Micro-Power Voltage Supervisor, Power Voltage Supervisor, Voltage Supervisor, Supervisor

References

- **User Manual**