

Description

The DGD0597FUQ is a high-frequency, high-side and low-side gate driver capable of driving n-channel MOSFETs in a half-bridge configuration. The floating high-side driver is rated up to 40V and provides a 5V gate drive to the MOSFETs.

The DGD0597FUQ logic inputs are compatible with standard TTL and CMOS levels (down to 3.3V) to interface easily with MCUs. A UVLO will protect ICs and MOSFETs with loss of supply.

Fast and well-matched propagation delays allow a higher switching frequency, enabling a smaller, more compact power switching design, using smaller associated components. The DGD0597FUQ is offered in the V-QFN3030-8 (Standard) package and operates over an extended -40°C to +125°C temperature range.

Features

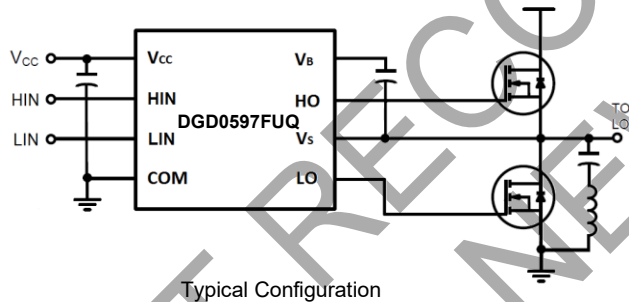
- 40V Floating High-Side Driver
- Low V_{CC} Operating Voltage: 4.5V to 5.5V
- Drives Two N-Channel Logic-Level MOSFETs in a Half-Bridge Configuration
- 1.5A Source/2.5A Sink Output Current Capability
- Internal Bootstrap Diode Included
- 3.4V UVLO with 0.4V Hysteresis
- Fast Rise and Fall Time (7ns/5ns)
- Propagation Delay Typical of 14ns
- Delay Matching Typical of 2.5ns
- Extended Temperature Range: -40°C to +125°C
- **Totally Lead-Free & Fully RoHS Compliant (Notes 1 & 2)**
- **Halogen and Antimony Free, "Green" Device (Note 3)**
- **The DGD0597FUQ is suitable for automotive applications requiring specific change control; this part is AEC-Q100 qualified, PPAP capable, and manufactured in IATF 16949 certified facilities.**
<https://www.diodes.com/quality/product-definitions/>

Applications

- Wireless power chargers
- Motor drives
- Logic-level MOSFET gate drivers

Mechanical Data

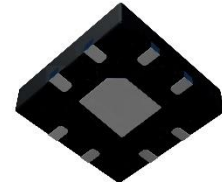
- Package: V-QFN3030-8
- Package Material: Molded Plastic. "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 3 per J-STD-020
- Terminals: Finish - NiPdAu Solderable per MIL-STD-202, Method 208 (e4)
- Weight: 0.017 grams (Approximate)



V-QFN3030-8 (Standard)



Top View



Bottom View

Ordering Information (Note 4)

Orderable Part Number	Package	Marking	Reel Size (inches)	Tape Width (mm)	Packing	
					Qty.	Carrier
DGD0597FUQ-7	V-QFN3030-8 (Standard)	DGD0597	7	8	3,000	Reel

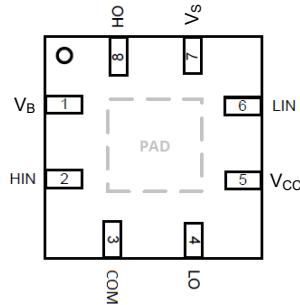
- Notes:
1. No purposely added lead. Fully EU Directive 2002/95/EC (RoHS), 2011/65/EU (RoHS 2) & 2015/863/EU (RoHS 3) compliant.
 2. See <https://www.diodes.com/quality/lead-free/> for more information about Diodes Incorporated's definitions of Halogen- and Antimony-free, "Green" and Lead-free.
 3. Halogen- and Antimony-free "Green" products are defined as those which contain <900ppm bromine, <900ppm chlorine (<1500ppm total Br + Cl) and <1000ppm antimony compounds.
 4. For packaging details, go to our website at <https://www.diodes.com/design/support/packaging/diodes-packaging/>.

Marking Information

**YYWW-
DGD0597**

DGD0597 = Product Type Marking Code
YY = Year (ex: 25 = 2025)
WW = Week (01 to 53)
The bar "-" next to "YYWW" indicates SAT assembly

Pin Diagrams

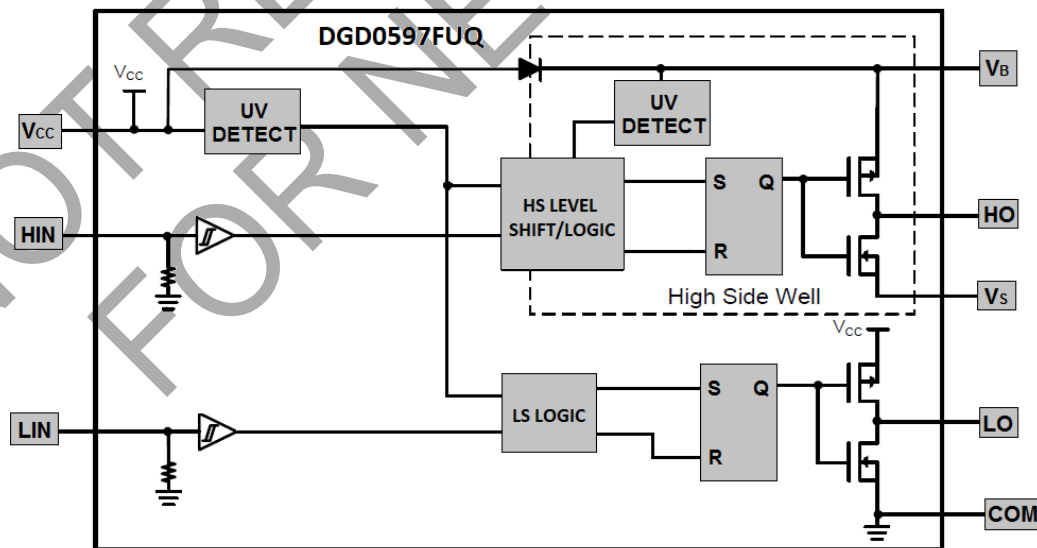


Top View: V-QFN3030-8 (Standard)

Pin Descriptions

Pin Number	Pin Name	Function
1	VB	High-Side Floating Supply
2	HIN	Logic Input for High-Side Gate Driver, in Phase with HO, Pulldown Resistor at Input
3	COM	Low-Side and Logic Return
4	LO	Low-Side Gate Driver Output
5	VCC	Low-Side and Logic Supply
6	LIN	Logic Input for Low-Side Gate Driver, in Phase with LO, Pulldown Resistor at Input
7	VS	High-Side Floating Supply Return
8	HO	High-Side Gate Driver Output
PAD	Substrate	Connect to COM on PCB

Functional Block Diagram



Absolute Maximum Ratings (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
High-Side Floating Positive Supply Voltage	V _B	-0.3 to +50	V
High-Side Floating Negative Supply Voltage	V _S	V _B -6 to V _B +0.3	V
High-Side Floating Output Voltage	V _{HO}	V _S -0.3 to V _B +0.3	V
Offset Supply Voltage Transient	dV _S / dt	50	V/ns
Logic and Low-Side Fixed Supply Voltage	V _{CC}	-0.3 to +6	V
Low-Side Output Voltage	V _{LO}	-0.3 to V _{CC} +0.3	V
Logic Input Voltage (HIN and LIN)	V _{IN}	-0.3 to +6	V

Thermal Characteristics (@T_A = +25°C, unless otherwise specified.)

Characteristic	Symbol	Value	Unit
Power Dissipation Linear Derating Factor (Note 5)	P _D	1.0	W
Thermal Resistance, Junction to Ambient (Note 5)	R _{θJA}	120	°C/W
Operating Temperature	T _J	+150	°C
Lead Temperature (Soldering, 10s)	T _L	+300	
Storage Temperature Range	T _{STG}	-55 to +150	

Note: 5. When mounted on a standard JEDEC 2-layer FR-4 board.

Recommended Operating Conditions

Parameter	Symbol	Min	Max	Unit
High-Side Floating Supply	V _B	V _S + 4.5	V _S + 5.5	V
High-Side Floating Supply Offset Voltage	V _S	0	40 (Note 6)	V
High-Side Floating Output Voltage	V _{HO}	V _S	V _B	V
Logic and Low-Side Fixed Supply Voltage	V _{CC}	4.5	5.5	V
Low-Side Output Voltage	V _{LO}	0	V _{CC}	V
Logic Input Voltage (HIN and LIN)	V _{IN}	0	5	V
Ambient Temperature	T _A	-40	+125	°C

Note: 6. Provided V_B does not exceed absolute maximum rating of 50V.

DC Electrical Characteristics ($V_{CC} = 5V$, $@T_A = +25^{\circ}C$, unless otherwise specified.)

Parameter	Symbol	Min	Typ	Max	Unit	Conditions
Logic "1" Input Voltage	V_{HIH}	2.5	2.1	—	V	—
Logic "0" Input Voltage	V_{HIL}	—	1.3	0.8	V	—
Logic Input Bias Current	I_{IN+}	—	28	60	μA	$V_{IN} = V_{CC}$
V_{CC} Quiescent Supply Current	I_{CCQ}	—	40	60	μA	—
V_{CC} Operating Supply Current	I_{CCO}	—	300	500	μA	HO and LO Open, $f_s = 250kHz$
Source Impedance	R_{SO}	—	1.5	2.6	Ω	Source = 100mA
Sink Impedance	R_{SI}	—	0.4	1.6	Ω	Sink = 100mA
Output High Short-Circuit Pulsed Current	I_{OH+}	—	1.5	—	A	$V_O = 0$, $PW \leq 10\mu s$
Output Low Short-Circuit Pulsed Current	I_{OL-}	—	2.5	—	A	$V_O = 15V$, $PW \leq 10\mu s$
V_{CC} Supply Undervoltage Positive Going Threshold	V_{CCUV+}	2.85	3.4	3.85	V	—
V_{CC} Supply Undervoltage Hysteresis	V_{CCU_HYST}	—	0.4	—	V	—
V_{BS} Supply Undervoltage Positive Going Threshold	V_{BSUV+}	2.85	3.3	3.65	V	—
V_{BS} Supply Undervoltage Hysteresis	V_{BSU_HYST}	—	0.4	—	V	—
Bootstrap Diode Forward Voltage	V_{BFD}	—	650	750	mV	$I = 100\mu A$
Bootstrap Diode Reverse Leakage	I_{BDL}	—	0.1	1.0	μA	$V_B = V_S = 45.5V$ $V_{CC} = 0$

AC Electrical Characteristics ($V_{CC} = 5V$, $@T_A = +25^{\circ}C$, unless otherwise specified.)

Parameter	Symbol	Min	Typ	Max	Unit	Conditions
Turn-On Rise Time	t_r	—	7	—	ns	$C_L = 1000pF$
Turn-Off Fall Time	t_f	—	5	—	ns	$C_L = 1000pF$
Turn-On Propagation Delay	t_{ON}	—	14	—	ns	—
Turn-Off Propagation Delay	t_{OFF}	—	14	—	ns	—
Delay Matching	t_{DM}	—	2.5	10	ns	—

Timing Waveforms

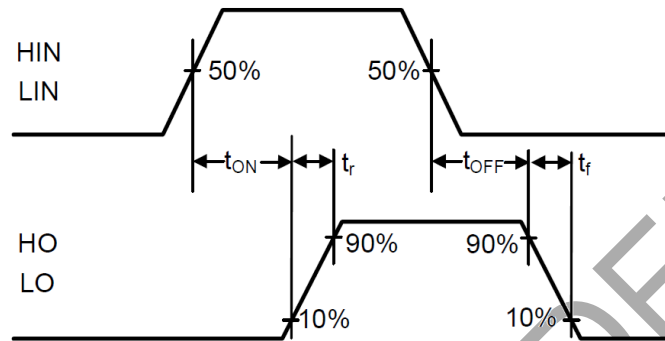


Figure 1. Switching Time Waveform Definitions

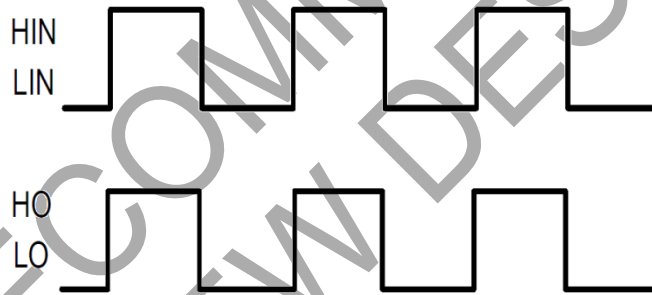


Figure 2. Input Output Timing Diagram

Typical Performance Characteristics ($V_{CC} = 5V$, $@T_A = +25^\circ C$, unless otherwise specified.)

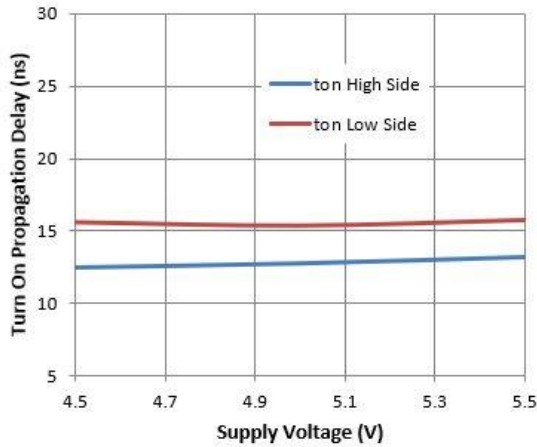


Figure 3. Turn-on Propagation Delay vs. Supply Voltage

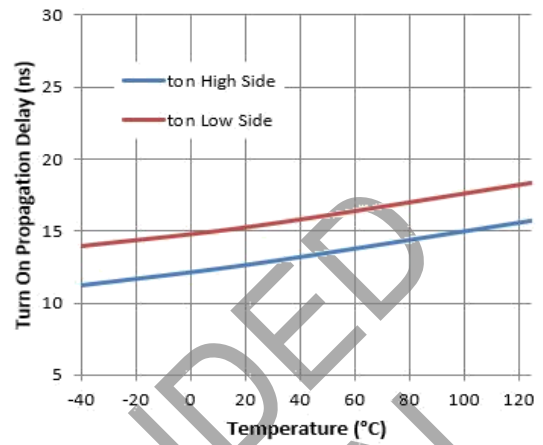


Figure 4. Turn-on Propagation Delay vs. Temperature

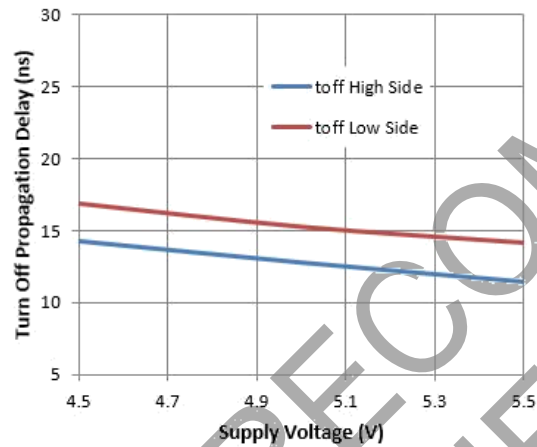


Figure 5. Turn-off Propagation Delay vs. Supply Voltage

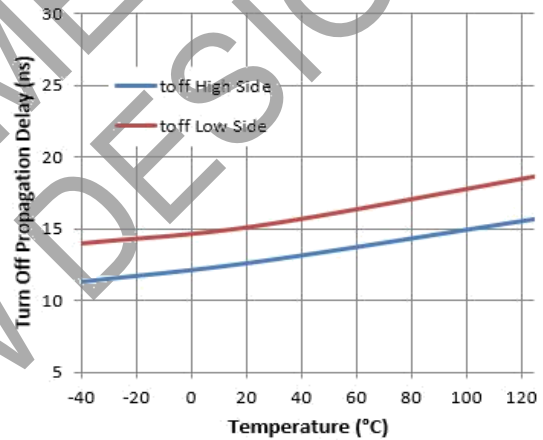


Figure 6. Turn-off Propagation Delay vs. Temperature

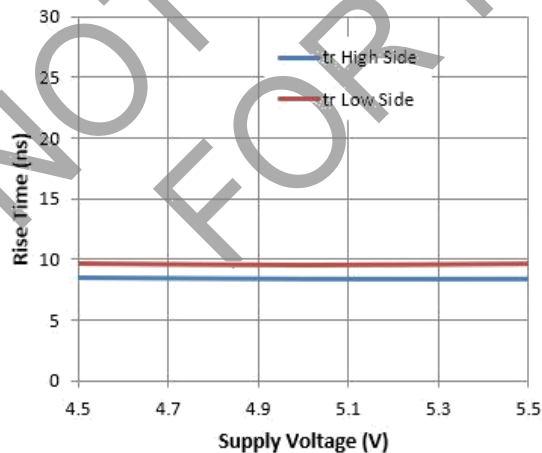


Figure 7. Rise Time vs. Supply Voltage

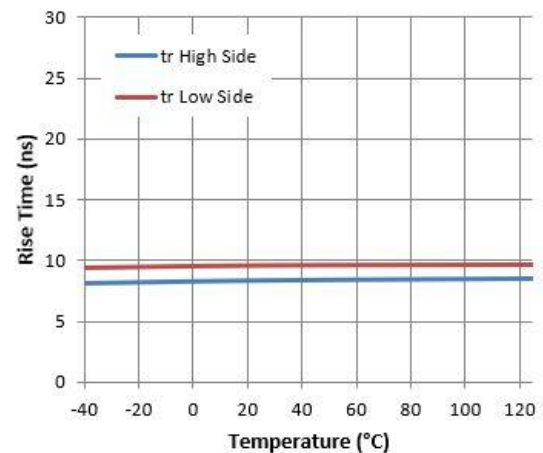


Figure 8. Rise Time vs. Temperature

Typical Performance Characteristics ($V_{CC} = 5V$, $@T_A = +25^\circ C$, unless otherwise specified.) (continued)

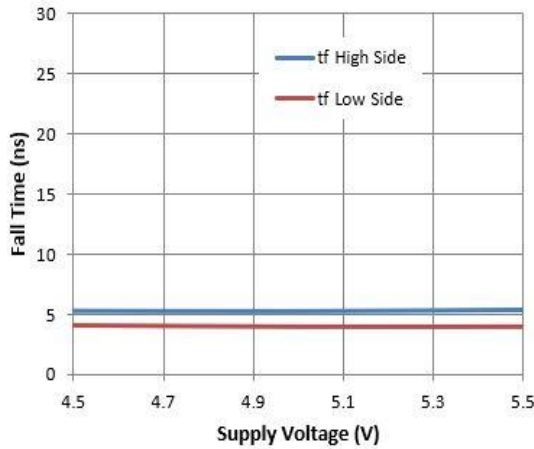


Figure 9. Fall Time vs. Supply Voltage

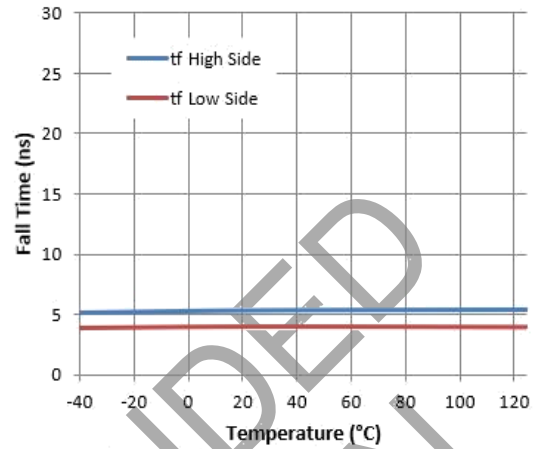


Figure 10. Fall Time vs. Temperature

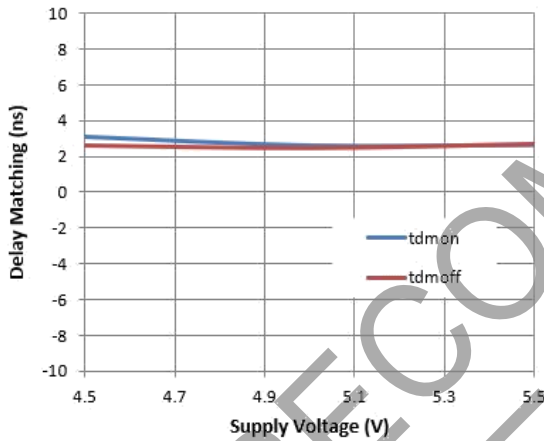


Figure 11. Delay Matching vs. Supply Voltage

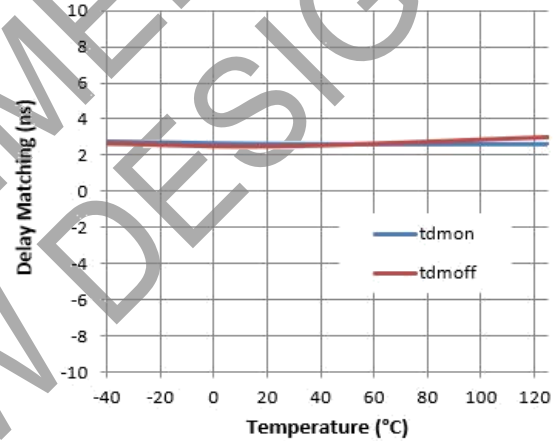


Figure 12. Delay Matching vs. Temperature

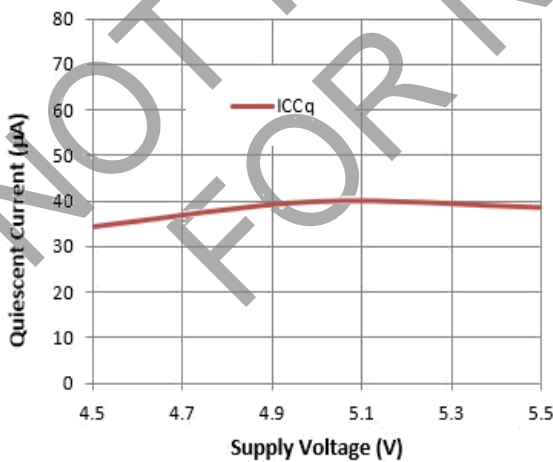


Figure 13. Quiescent Current vs. Supply Voltage

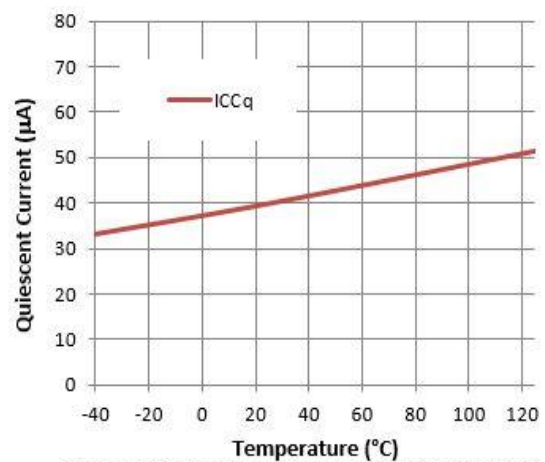


Figure 14. Quiescent Current vs. Temperature

Typical Performance Characteristics ($V_{CC} = 5V$, $@T_A = +25^\circ C$, unless otherwise specified.) (continued)

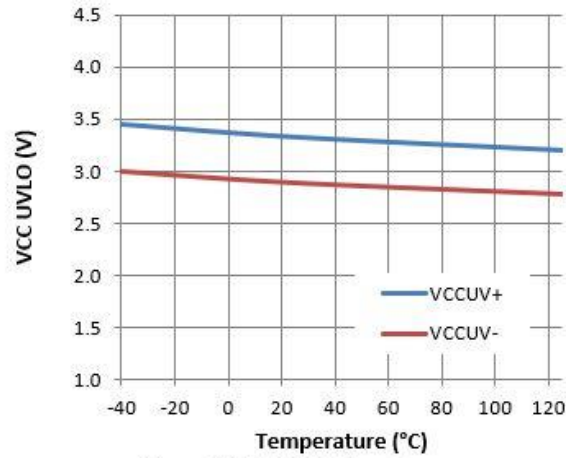


Figure 15. VCC UVLO vs. Temperature

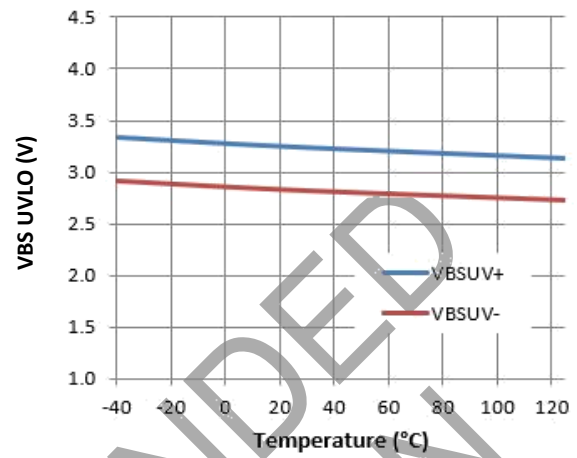


Figure 16. VBS UVLO vs. Temperature

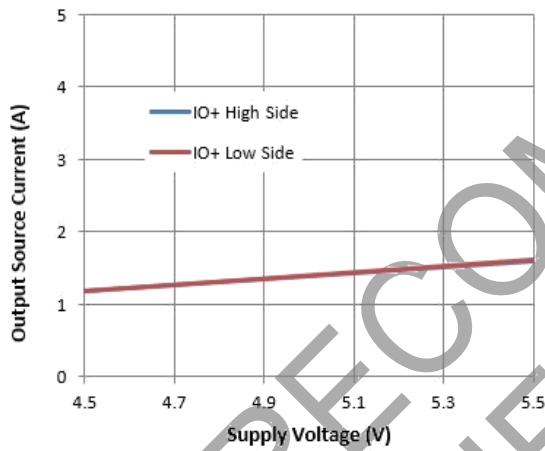


Figure 17. Output Source Current vs. Supply Voltage

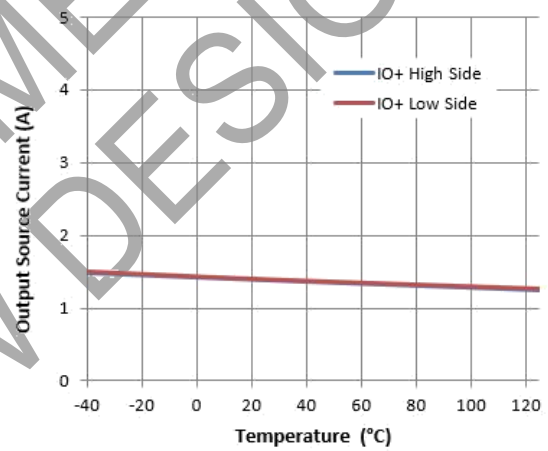


Figure 18. Output Source Current vs. Temperature

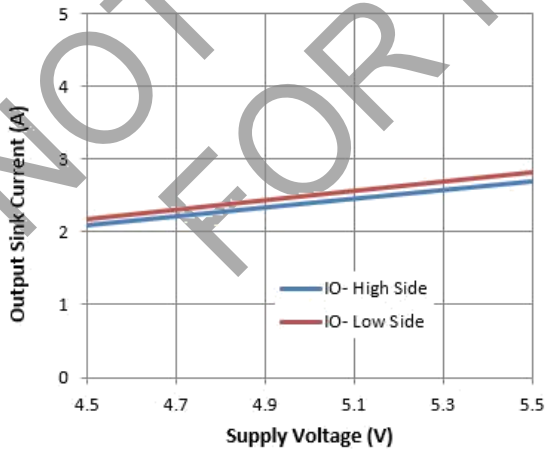


Figure 19. Output Sink Current vs. Supply Voltage

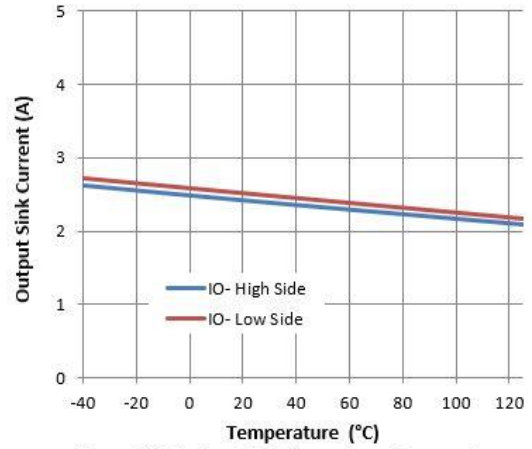


Figure 20. Output Sink Current vs. Temperature

Typical Performance Characteristics ($V_{CC} = 5V$, $@T_A = +25^\circ C$, unless otherwise specified.) (continued)

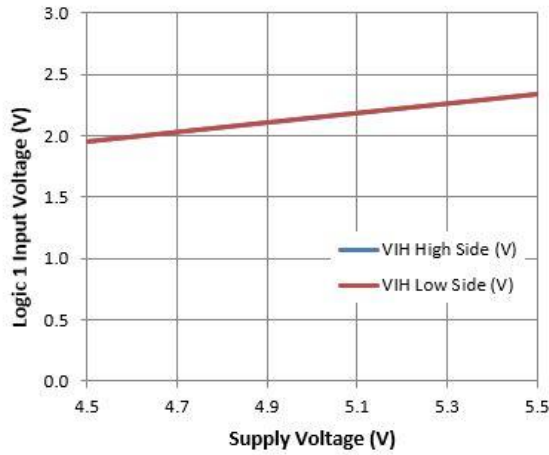


Figure 21. Logic 1 Input Voltage vs. Supply Voltage

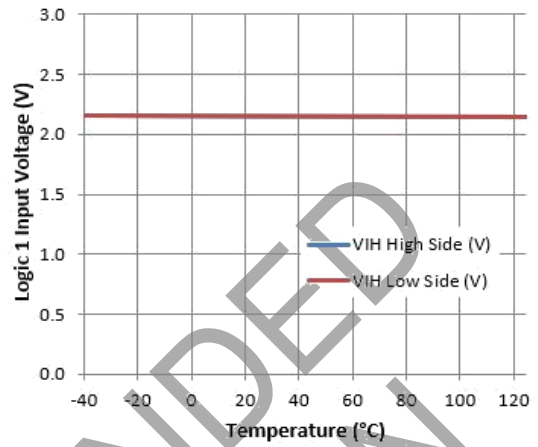


Figure 22. Logic 1 Input Voltage vs. Temperature

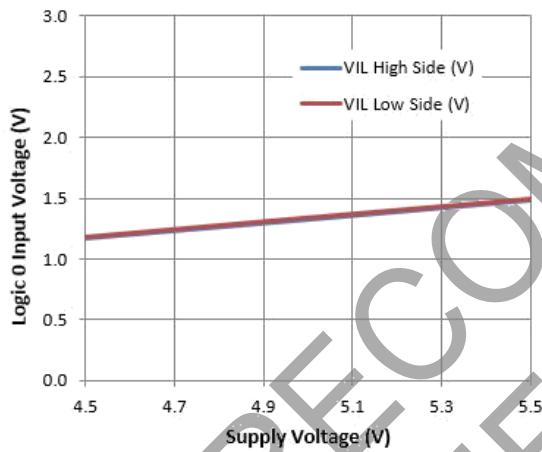


Figure 23. Logic 0 Input Voltage vs. Supply Voltage

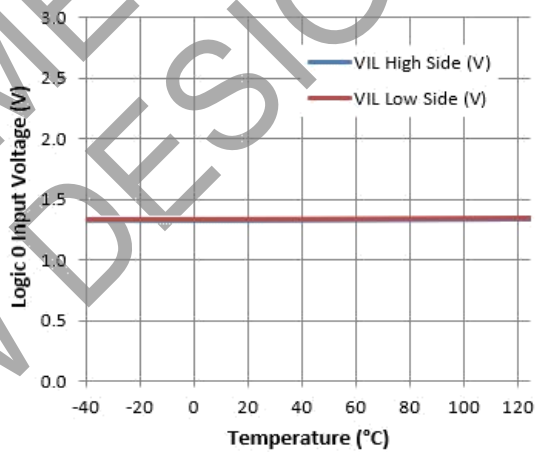


Figure 24. Logic 0 Input Voltage vs. Temperature

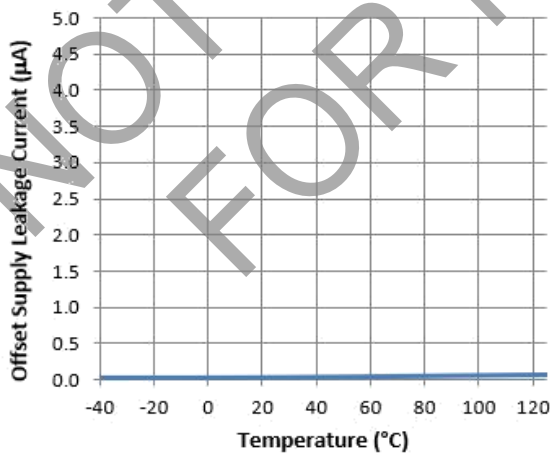


Figure 25. Offset Supply Leakage Current vs. Temperature

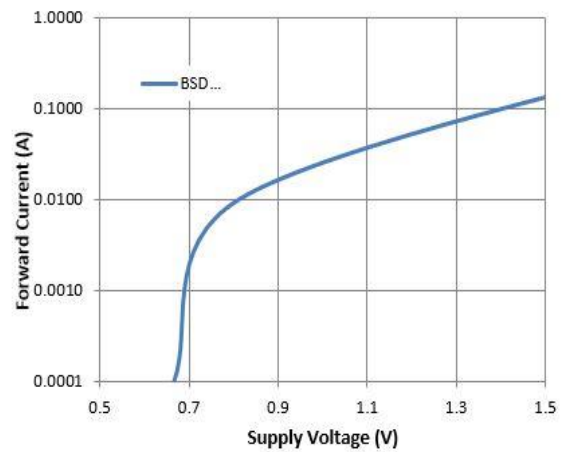
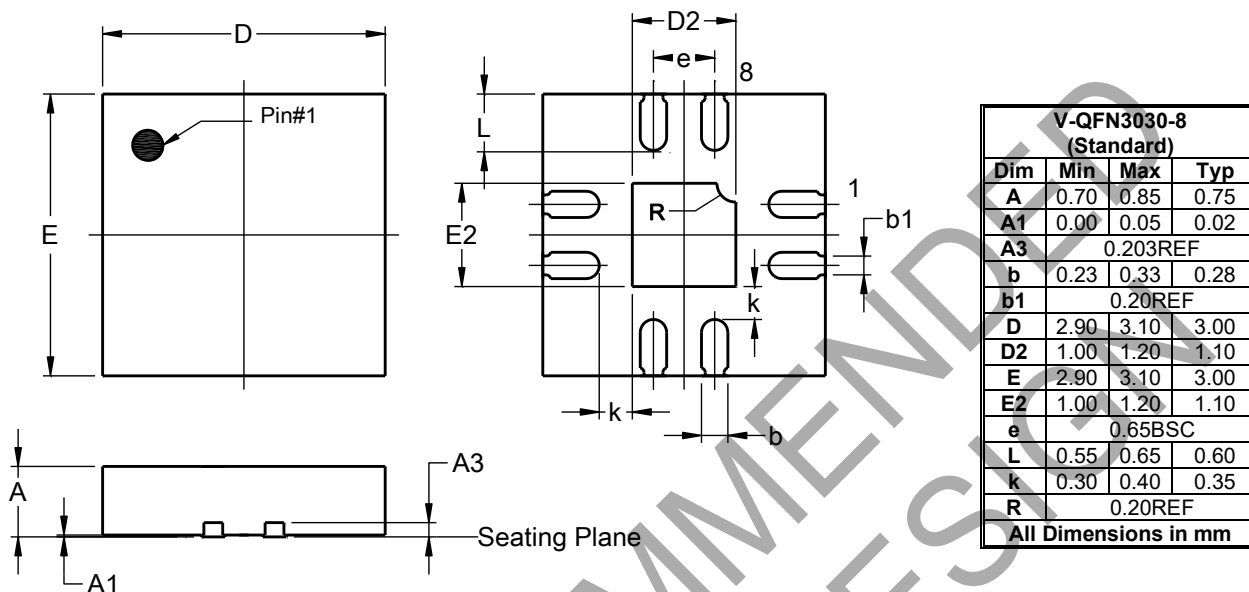


Figure 26. Bootstrap Diode I-V Characteristics

Package Outline Dimensions

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

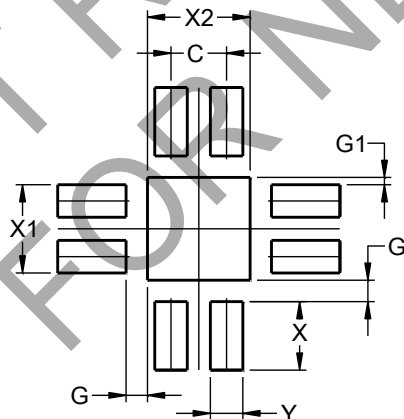
V-QFN3030-8 (Standard)



Suggested Pad Layout

Please see <http://www.diodes.com/package-outlines.html> for the latest version.

V-QFN3030-8 (Standard)



Dimensions	Value (in mm)
C	0.650
G	0.250
G1	0.085
X	0.800
X1	1.030
X2	1.200
Y	0.380

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