



STM32H7 MCUs for rich and complex applications

Marketing presentation

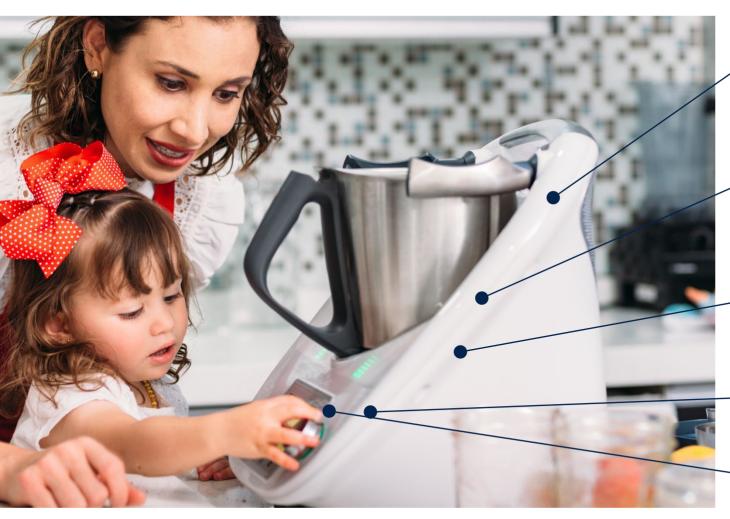




If only I could add natural language, cloud-based voice UI to my product



STM32H7 dual core - Adding natural language cloud-based voice UI to your product



All necessary memory is embedded: 2 MB of Flash and 1 MB of SRAM

Arm® Cortex®-M7 core @480 MHz high and Arm® Cortex®-M4 core @240 MHz performances for Audio and voice DSP

Embedded security to protect your application and secure firmware update

One chip, 2 applications running in parallel

Ready for security: state of the ART cyber protection and secure firmware update





If only I could address the design challenges in factory automation systems



STM32H7 single core - Building a factory automation product



High performance for optimized control or HMI

Large embedded memory and external memory support

- Up to 2 MB of Flash & up to 1.4 MB of SRAM
- Fast multiple Octo-SPI interface

Extended connectivity with Ethernet MAC, Multiple FD-CAN and USB

Fast 16-bit and 12-Bit ADC, and extended Temp range support up to 125 °C

SIL ready enabled by native hardware features and safety library

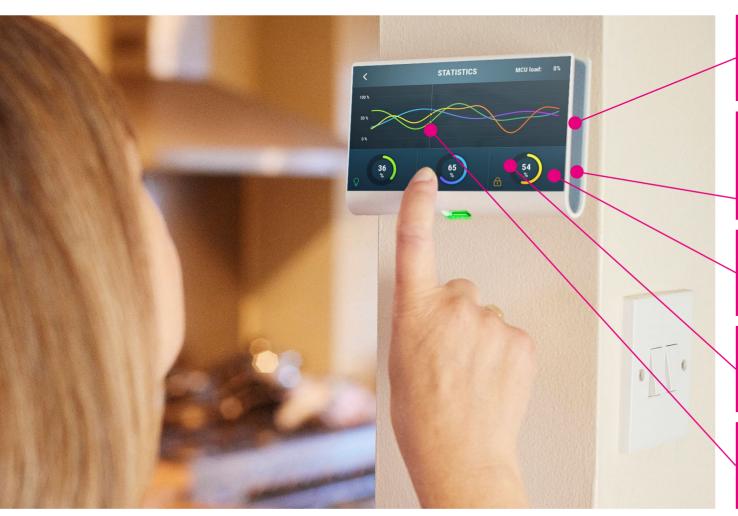




If only
I could run deeply embedded applications with advanced performance at minimum cost



STM32H7 - Creating a smartphone-like graphic UI for your embedded device



Graphic hardware accelerations for better effects, transitions and fluidity

Up to 1.4 MB SRAM (frame buffer) for integrated and cost-effective single chip solution

Multiple high-speed external memory interface

Graphic support from 64-pin LQFP packages (improved cost effectiveness)

TouchGFX free graphic tool suite for stunning HMI and simplified development











STM32H7









STM32H7 series

New product lines expanding the STM32 portfolio



New Performance Record

Up to 2424 + 800 CoreMark (Cortex[©]-M7 @480MHz + Cortex[©]-M4 @240MHz) in Dual core Up to 2778 CoreMark (Cortex[©]-M7 @550MHz) in Single core



Single and Dual-core flexible architecture for industrial, security or AI applications Accelerated graphics, fast data transfer, advanced peripherals



Advanced security features
Crypto Hash, Cortex[©]-M7 Security services



Rich eco-system to speed-up your design SW tools, HW boards, community and partners



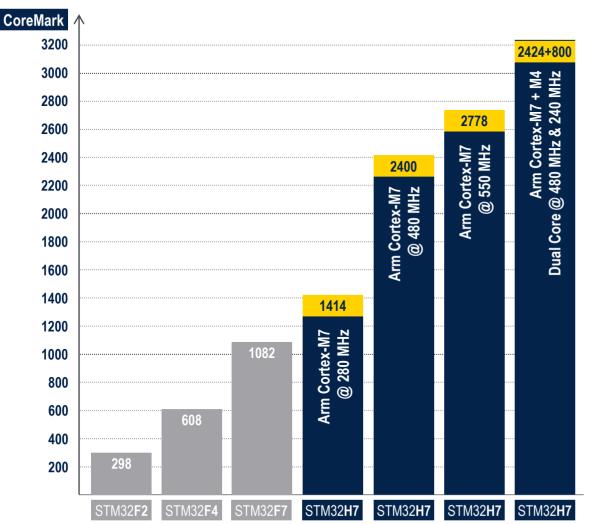
Performance record







High performance range



Arm® Cortex® -M7 up to 550 MHz

Most powerful Cortex core with double precision FPU, MPU, advanced DSP and L1 cache

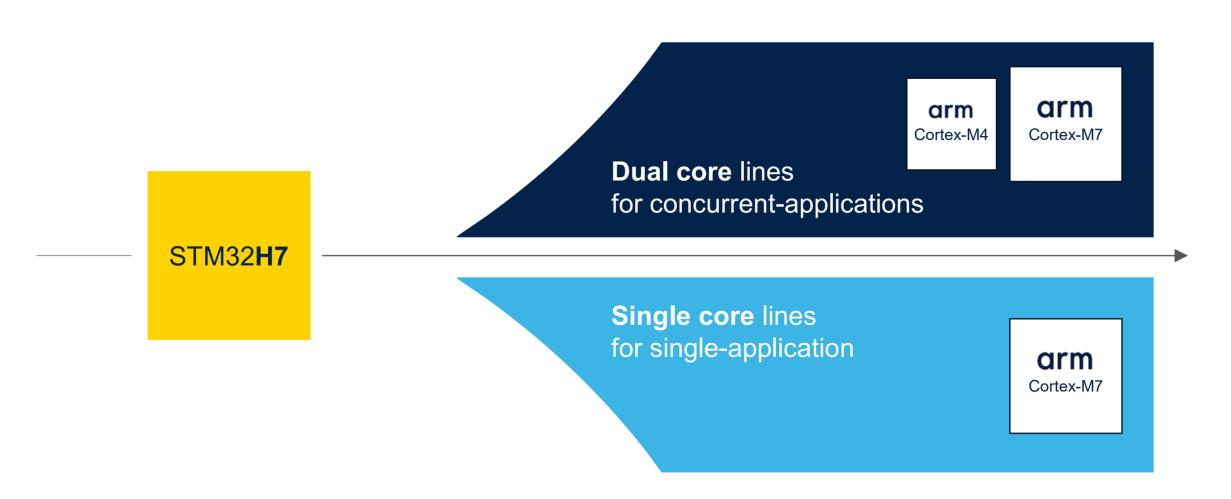
Arm® Cortex® -M4 @240 MHz

Best in class core for **real-time** with single precision FPU, DSP, MPU and ART Accelerator™





The extended STM32H7 experience





Single Core Architecture Approach for performance and advanced HMI

Factory automation



Cortex-M7 = HMI, process control, power management

Connectivity & security

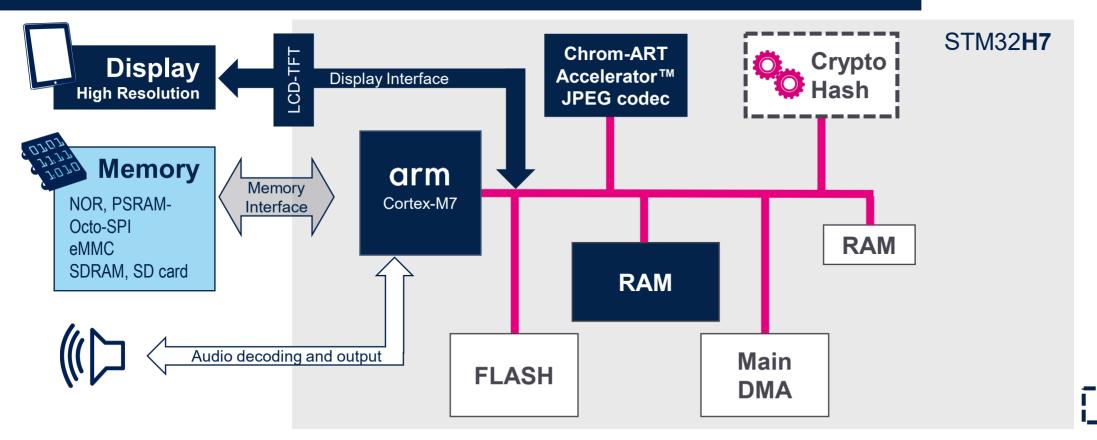


Cortex-M7 = Alarm panel, Wireless Modules



Create a rich human machine interface

Cortex-M7 - handling audio and rich HMI, Real Time control tasks





optional

Dual-core architecture approach for richer and more complex applications

Industrial tool machine



Cortex-M7 = HMI

Cortex-M4 = Com/Gateway + Motor Control

+ Sensor pre-processing (AI)

Home automation & security



Cortex-M7 = AI NN (Pattern recognition, ASR)

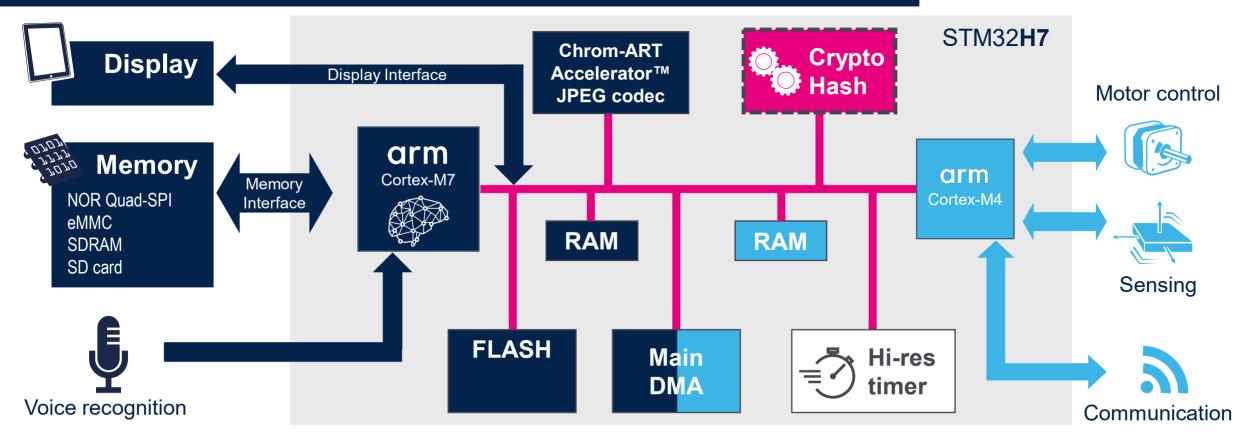
Cortex-M4 = Com/Gateway + Real-time I/F





Build complex applications mixing AI and real-time control

Connected Kitchen Aid with advanced HMI (Large display and Voice recognition)









Powerful cores supported by a powerful architecture

Display nice graphic

The Chrom-ART Accelerator™ and MJPEG codec offload the CPU by more than 90%



STM32H7



Manage security

Use dedicated **cryptography** and **Hashing** HW acceleration to **offload the CPU by more than 90%**



The Main DMA takes care of the most complex schemes between memories and peripherals with up to 16 channels to offload the CPU





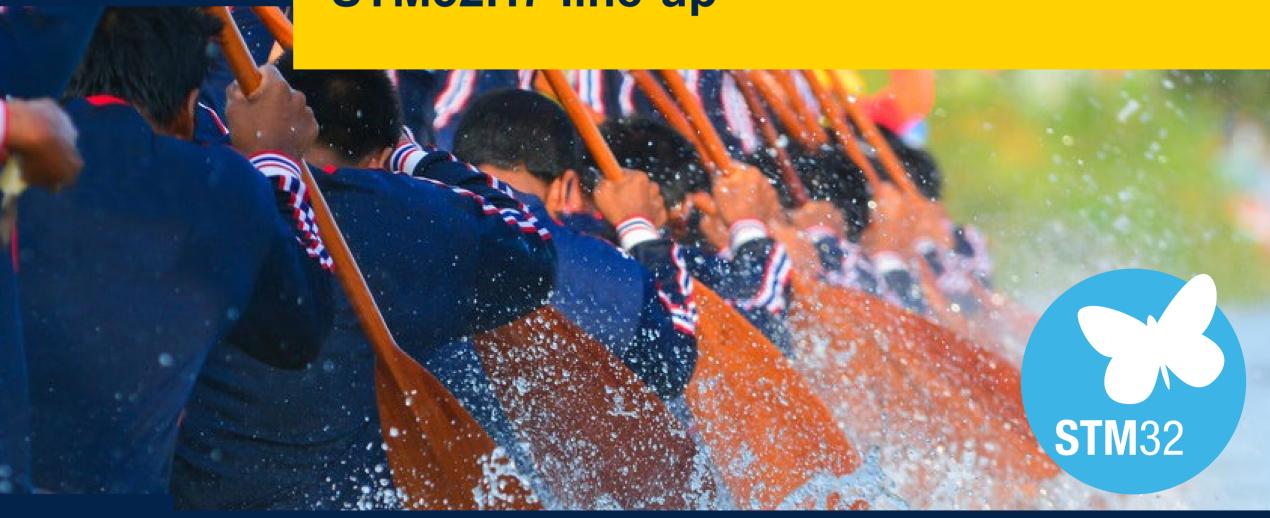
Generate complex wave forms

High-Resolution timer (2.1ns)

can generate complex wave forms synchronized on multiples events, with no CPU assist



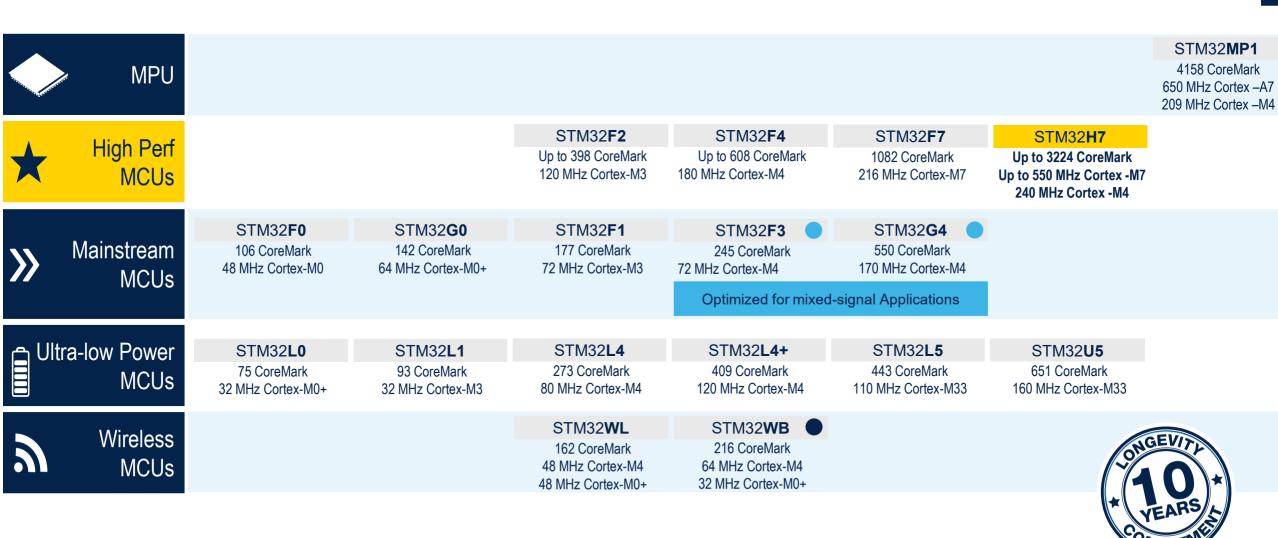
STM32H7 line-up







STM32H7 MCU series





Optimized for mixed-signal applications

Cortex-M0+ Radio co-processor





Extensive STM32H7 portfolio



Dual-core Line STM32**H745/755**

480+240 MHz SMPS 1027 + 300 DMIPS RAM 1 MB

Flash up to 2 MB

STM32**H747/757**

480+240 MHz SMPS 1027 + 300 DMIPS

RAM 1 MB

Flash up to 2 MB



Single-core Line STM32H7A3/B3

280 MHz **599 DMIPS**

RAM 14 MB Flash up to 2 MB STM32H742

LDO 480 MHz LDO **1027 DMIPS RAM 692 KB**

Flash up to 2 MB

STM32**H743/753**

480 MHz **1027 DMIPS** RAM 1 MB

Flash up to 2 MB

STM32**H723/733**

550 MHz LDO **1177 DMIPS RAM 564 KB** Flash up to 1 MB

STM32**H725/735**

LDO 550 MHz SMPS **1177 DMIPS RAM 564 KB** Flash up to 1 MB

Extended temperature range 125 ° C ambient



Value Line

STM32H7B0

LDO

280 MHz 599 DMIPS RAM 14 MB Flash 128 KB STM32H750

480 MHz 1027 DMIPS RAM 1 MB Flash 128 KB STM32H730

LDO 550 MHz 1177 DMIPS **RAM 564 KB** Flash 128 KB STM32H730Q

LDO 550 MHz SMPS 1177 DMIPS **RAM 564 KB** Flash 128 KB

Arm® Cortex® core

Cortex-M7

Cortex-M7 & -M4





STM32H7 MCU Series 32-bit Arm® Cortex®-M7 or Cortex®-M7 + Cortex®-M4



CORE, MEMORIES AND ACCELERATION	Product line	f _{ceu} (MHz)	Dual- Bank Flash memory (bytes)	RAM (bytes)	OctoSPI & OTFDEC ³	Ethernet	Graphic	Power supply	Stop mode (typical) / RAM reten - tion
 Single-core Cortex-M7 up to 550 MHz 	Dual-core lines								
Dual-core Cortex-M7 480 MHz and Cortex-M4 240 MHz Flash and RAM acceleration SP-FPU and DP-FPU 4 x DMA	STM32H747/757 ¹	480 + 240	Up to 2 Mbytes	1 Mbyte (incl.128 Kbytes DTCM + 64 Kbytes ITCM + 64 Kbytes backup 1) + 4 Kbytes backup 2		•	TFT-LCD JPEG codec MIPI-DSI	SMPS + LD0	360 μA / 1MB 250 μA / 768KB
Mathematics (only H723/733/725/735/730) CONNECTIVITY Up to 2 x USB2.0 OTG FS/HS 2 x SDMMC	STM32H745/7551	480 + 240	Up to 2 Mbytes	1 Mbyte (incl.128 Kbytes DTCM + 64 Kbytes ITCM + 64 Kbytes backup 1) + 4 Kbytes backup 2		•	TFT-LCD JPEG codec	SMPS + LD0	360 μA / 1MB 250 μA / 768KB
USART, UART, SPI, I ² C	Single-core lines								
Up to 3 x CAN (2 x FD and 1 x TT) HDMI-CEC FMC, Dual-mode Quad-SPI or	STM32H7A3/7B31	280	Up to 2 Mbytes	1,4MB (incl.128K DTCM, 64K ITCM, 1184K+SRAM, 4K backup)	•		TFT-LCD JPEG codec Chrom- GRC	SMPS + LD0	32 μA / 1.4MB 28 μA / 32KB
2 x Octo-SPI • Camera I/F AUDIO • 3 x IPS + audio PLL • 4 x SAI	STM32H743/7531	480	Up to 2 Mbytes	1 Mbyte (incl.128 Kbytes DTCM + 64 Kbytes ITCM + 64 Kbytes backup 1) + 4 Kbytes backup 2			TFT-LCD JPEG codec	LD0	1270 µA / 1MB 910 µA / 768KB
2 x 12-bit DAC SPDIF-RX GRAPHIC Chrom-ART Accelerator™	STM32H742	480	Up to 2 Mbytes	692 Kbytes (incl.128 Kbytes DTCM + 64 Kbytes ITCM + 16 Kbytes backup 1) + 4 Kbytes backup 2		•		LD0	1270 µA / 692KB 910 µA / 704KB
OTHER • Crypto/Hash option (except H742)	STM32H725/7353	550	Up to 1 Mbyte	564KB (incl.128K DTCM, 432KB Syst + 4K bckup)	•	•	TFT-LCD	SMPS ⁵ + LD0	200 μA / 564KB
Security services option (except H742) TRNG	STM32H723/7333	550	Up to 1 Mbyte	564KB (incl.128K DTCM, 432KB Syst + 4K bckup)	•	•	TFT-LCD	LD0	520 μA / 564KB
• DFSDM	Value line								
• 16- and 32-bit timers • HRTimer (except STM32H7A/H7B/H7B0/H723/H725/H730/H733/H735)	STM32H7B0	280	128 Kbytes	1,4MB (incl.128K DTCM, 64K ITCM, 1184K+SRAM, 4K backup)	•		TFT-LCD JPEG codec Chrom- GRC	SMPS + LD0	32 μA / 1.4MB 28 μA / 32KB
Up to 3 x 16-bit ADC (up to 3.6 MSPS) Analog (compt, AOP) Voltage range 1.62 to 3.6 V (except 100-pin and VFOFPN68 packages : 1.71 to 3.6 V)	STM32H750	480	128 Kbytes	1 Mbyte (incl.128 Kbytes DTCM + 64 Kbytes ITCM + 64 Kbytes backup 1) + 4 Kbytes backup 2		•	TFT-LCD JPEG codec	LD0	1270 μA / 1MB 910 μA / 768KB
 Multi-power domains -40°C up to 105°C ambiant -40°C up to 125°C ambiant² 	STM32H730	550	128 Kbytes	564KB (incl.128K DTCM, 432KB Syst + 4K bckup)	•	•	TFT-LCD	SMPS ⁴ + LD0	200 μA / 564KB 520 μA / 564KB

Tailored for your needs

- Single and Dual core versions
- High performance up to 480 MHz in Dual core and up to 550 MHz in Single core
- 128 KB to 2 MB Flash Dual Bank
- Up to 1.4 MB RAM
- More security features (Boot, Tamper ...), OTFDEC on external memories, Crypto/Hash and security services (optional)
- 35 communication peripherals
- 16-bit ADC up to 3.6 Msps, up to 5 MSPS in 12-bit, Comparators, Op Amp
- TT-FD-CAN and FD-CAN
- High-Resolution timer (2.1ns)
- Low-Power Timers
- LDO and SMPS option
- Up to 140 °C junction temperature / 125 °C ambient (optional)

Notes:

ional - dedicated CPN, STM32H733, STM32H735, STM32H753, STM32H755, STM32H757, STM32H7B3 for the Crypto Variants

^{125 °}C ambiant / 140 °C junction. Dedicated part numbers on STM32H725/H735. STM32H745/H755

Crypto and Security services on CPN: STM32H733, STM32H735 and STM32H7

[.] SMPS available only on STM32H730Q CPN



STM32H7 72x/73x series links to product pages

ST's new STM32H7 microcontrollers combine the high performance of a single core with/and rich feature integration



- System integration
- Advanced connectivity and control
- Security services

- ► STM32H723/733 here
- STM32H725/735 here
- ► STM32H730 here
- ► ST blog article <u>here</u>







STM32H7 74x/75x series links to product pages

New STMicroelectronics' STM32H7 Microcontrollers Combine Dual-Core Performance with Rich Feature Integration



- System integration
- Advanced connectivity and control
- Security services

- ► STM32H745/755 here
- ► STM32H747/757 <u>here</u>
- ► STM32H742 here
- ► STM32H743/753 <u>here</u>
- ► STM32H750 here
- ST blog article <u>here</u>







STM32H7 Ax/Bx series links to product pages

New STM32H7 Microcontrollers for best combination of performance, integration and power saving inside an MCU







- ► STM32H7A3/H7B3 here
- ➤ STM32H7B0 Value line here





Performance and smart architecture are yours to innovate

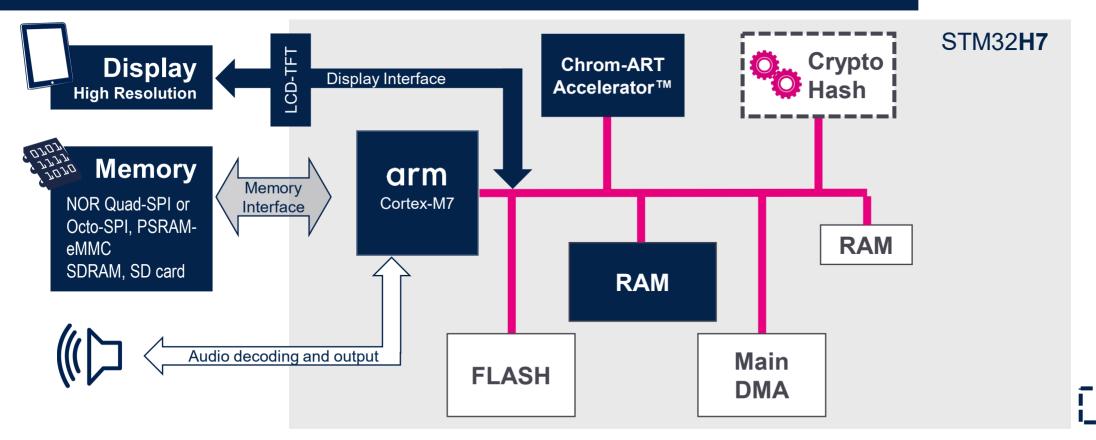






Create a rich human machine interface

Cortex-M7 - handling audio and rich HMI, Real Time control tasks



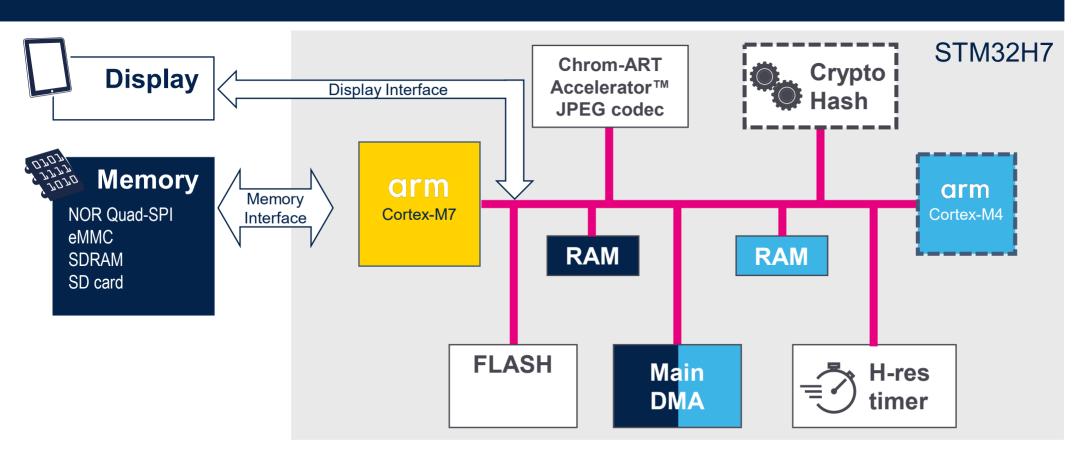


optional



Seamlessly move and format data

Main DMA - Flexible and high-speed data transfers schemes without CPU load



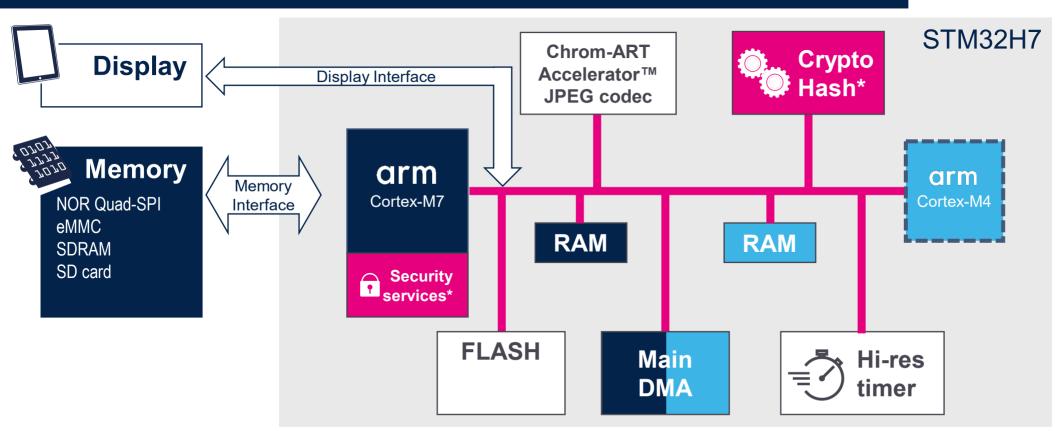






Reinforce the security in your solution

Cryptography and Hashing hardware assist Authenticate your chip and securely install your code in memory



^{*} requires part numbers with integrated security options

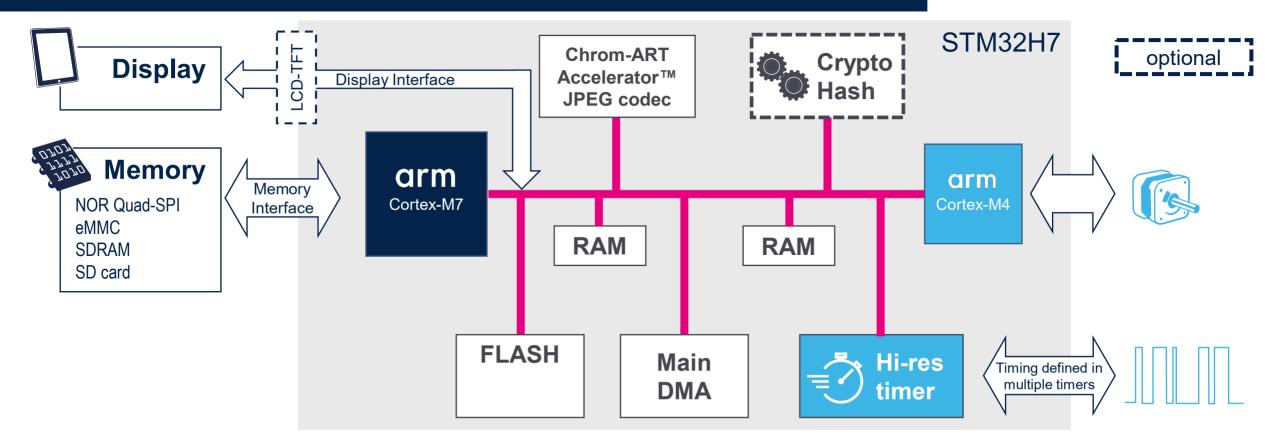


optional



Control real-time applications

High resolution timer: advanced wave forms generation







Industrial and health & wellness DNA

Industrial Health & Wellness

- Error Code Correction on all Flash and RAM and dual core for safety
- Large choice of packages
- Advanced digital and analog
 (High resolution timer, 16-bit and 12-bit ADC, Op-Amp, Ethernet, CANFD...)
- High temperature -40°C up to 140°C junction temperature (125°C ambient)





Industrial and health & wellness DNA

Industrial

- Inverters
 Advanced timers and analog peripherals
- Communication gateway
 Rich connectivity and optional dual core
- Human Machine Interface
 Chrom-ART Accelerator and display interfaces for TFT and MIPI-DSI

Health & Wellness

- Health and wellness
 Chrom-ART Accelerator™ and display interfaces
 for TFT and MIPI-DSI
- Individual assistance (hearing, respiratory)
 Advanced timers and analog
- Measurements and Data logger
 Advanced Analog





Consumer DNA

Consumer

- Small packages
- Power efficiency and high performance
- Advanced audio and graphic
- High-speed peripherals
- Large expandable memories to support ever increasing communication protocols





Consumer DNA

Consumer IoT gateway Large memory and rich communication peripherals **Access control** Chrom-ART Accelerator™ and display interfaces for TFT and MIPI-DSI **Drones** High processing architecture with dual core option, advanced timers and analog peripherals, small packages



Secure your production and your applications





STM32Trust on STM32H7 Series



www.st.com/stm32trust

Global security ecosystem and services

STM32 concept Support customer's Secure Boot / Root Of Trust

SFI
A Secure Installer of
Secure Boot / Root Of Trust

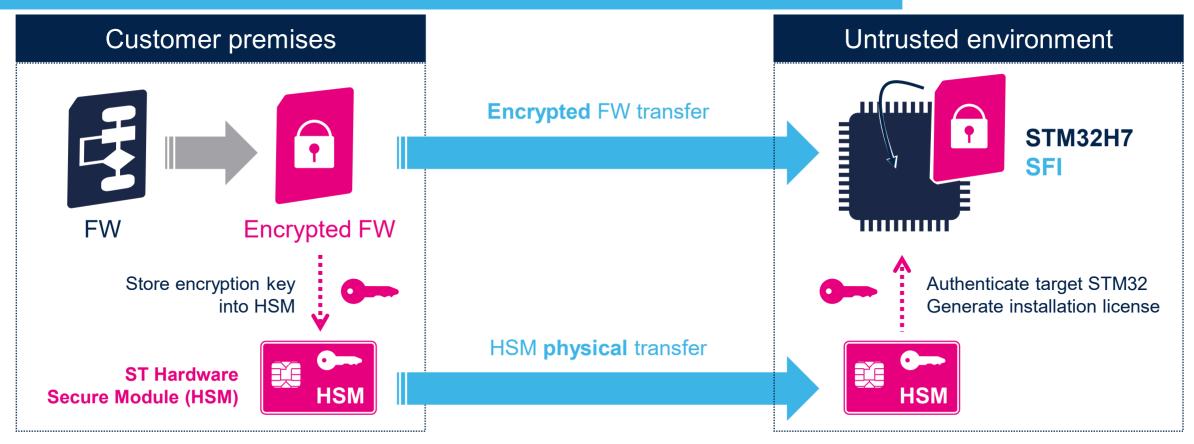
SBSFU
A reference SW package for **FW Update**and **Secure Boot** / **Root Of Trust**





Secure your production flow with secure firmware install (SFI*)

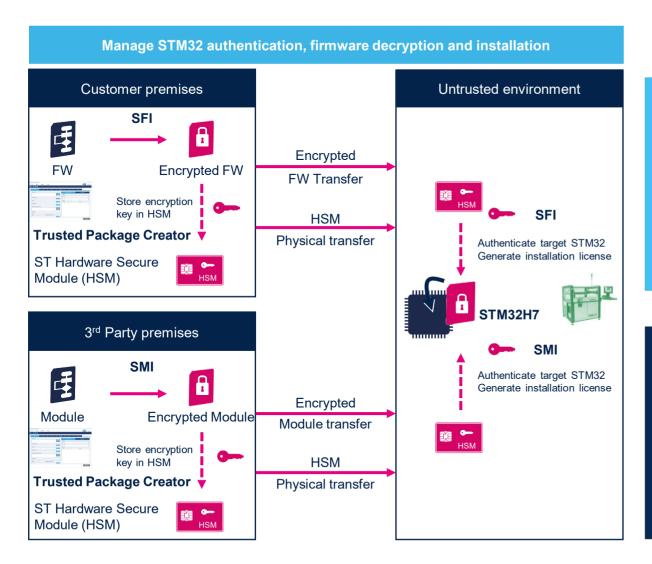
Manage STM32 authentication, firmware decryption and installation





36

Embedded Secure Firmware Install - SFI



Secure Loader
embedded services
provisioned by ST
→ Mass Market
approach

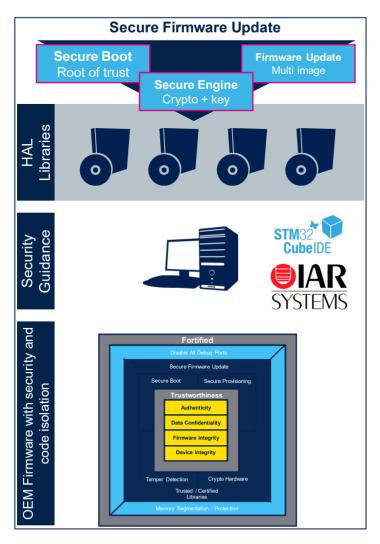
ST ecosystem
with
Encryption, HSM and
programming tools

Firmware cloning
protection on the first
installation
via
UART / SPI / USB

Protect 3rd party Software IP (SMI)



Secure Boot Secure FW Update - SBSFU



Reference library source code for IAP

Demonstrate SW modules for:

- Secure Boot
- Secure Engine for Crypto and key
- Firmware Update image management

Ensure authentication and secure programing of in the field products

Reference implementation of STM32H7 hardware memory protections





A full set of security



Encryption Decryption Authentication

- AES-128/256 Encryption
- DES/TDES crypto engine
- SHA-256 Authentication
- Certified Crypto library
- True Random Number Generator
- Unique ID
- Key provisioning for STM32 authentication



STM32**H7**



Some of the above features are optional and require specific part numbers.
See the next slide for more details.



Memory and IP protection

- Anti-tamper detection
- Memory Protection Unit (MPU)
- Secure Boot
- Read and Write Protection
- Secure User Area (Hide Protect)
- PC-ROP
- JTAG fuse
- Octo-SPI On The Fly Decrypt engine on external NOR Flash



STM32H7 detailed security functions

STM32Trust Security function	CM7 CM7/CM4 STM32H72x STM32H74x STM32H7Ax	CM7 Crypto CM7/CM4 Crypto STM32H75x	CM7 STM32H73x STM32H7Bx
Secure Boot Secure User Memory for SBSFU software package		•	•
Secure Install/Update By SBSFU software package		•	•
Secure Storage for Boot only		•	•
Access Debug Read Out Protection RDPL0/1/2	•	•	•
Resource Isolation Memory Protection Unit	•	•	•
Secure Execution By SBSFU software package		•	•
Crypto Engine Hardware crypto accelerator	TRNG Fips	AES / DES / SHA / TRNG Fips	AES / DES / SHA / TRNG Fips
Crypto Engine On-the-fly decryption from external memories			•
SWIP Protection/Collaborative Dev Secure Module Install (SMI)		•	•
Secure Manufacturing Secure Firmware Install (SFI)		•	•

Note: a crypto library is available on request for both crypto and non crypto parts.

Solutions for STM32H7 Graphics



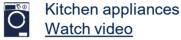


Enhance your product with great graphic



























Enhanced graphic UI for any resolution

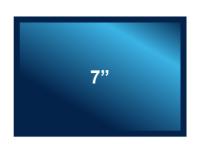
24bpp 16bpp 8bpp



Display with GRAM Single frame buffer



Display without GRAM **Double** frame buffer



Display without GRAM **Double** frame buffer



Display without GRAM **Double** frame buffer

Internal RAM STM32H7A3/7B3/7B0

STM32H723/725/730

STM32H743/745/747/750



STM32H7A3/7B3/7B0

STM32H723/725/730

STM32H743/745/747/750



STM32H7 detailed graphic features

	Features	STM32H7A3/7B0	STM32H723/725/730	STM32H745/747	STM32H743/750
Hardware acceleration	Chrom-ART Accelerator™ Hardware acceleration for graphical operations	•	•	•	•
	Chrom-GRC™ Minimizing memory usage for round displays	•	-	-	-
	JPEG CODEC Optimized video playback	•	-	•	•
	Quad-SPI Connecting QSPI Flash	•	•	•	•
Memory	Octo-SPI Connecting Octo SPI flash or Octal RAM	•	•	-	-
interfaces	FMC Connecting parallel flash, SDRAM, PSRAM	•	•	•	•
	SDMMC Connecting eMMC, MMC,	•	•	•	•
	LCD-TFT display controller	•	•	•	•
Display interfaces	MIPI-DSI	-	-	•	-
intoriacos	Parallel 8080/6800	•	•	•	•
Embedded	Embedded SRAM for framebuffers	Up to 1024 Kbytes	Up to 364 Kbytes	Up to 512 Kbytes	Up to 512 Kbytes
memory	Embedded flash for code and data	128 Kbytes to 2048 Kbytes	128 Kbytes to 1024 Kbytes	1024 Kbytes to 2048 Kbytes	128 Kbytes to 2048 Kbytes

X-CUBE-TouchGFX Graphical User Interface development





STM32 Graphics Extended Ecosystem

TouchGFX Implementers

World-wide network for TouchGFX expertise and design services

Advanced Graphics Solutions

Software partners taking the full advantages of STM32 graphic capabilities





































Solutions for STM32H7 Artificial Intelligence





Embed AI in your applications with STM32H7





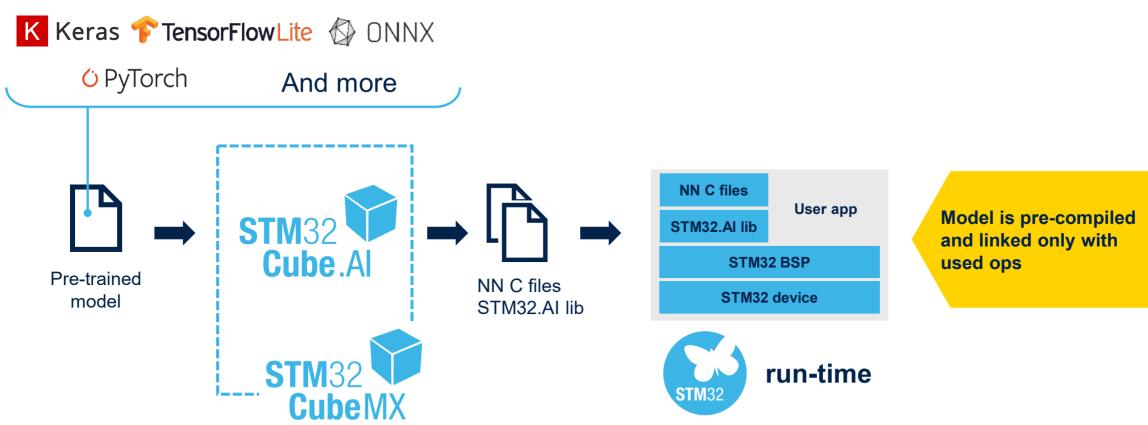






The key steps behind Neural Networks on STM32

Optimized C code generated by STM32Cube.Al





Solutions for STM32H7 Functional safety



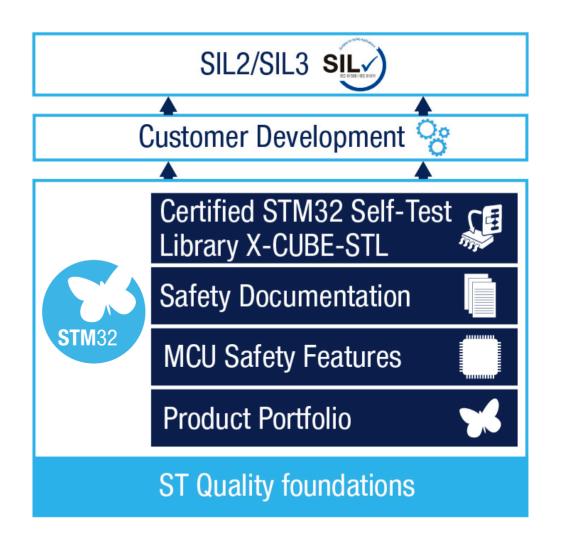




SIL functional safety package for STM32

Reduce time and cost to build STM32-based systems certified to IEC 61508 industrial safety standard









SIL functional safety package for STM32



ST provides a complete, certified offering to

- Lower project costs
- Reduce design complexity
- Ease SIL certification assessment



design package

without design package





STM32 high performance built-in safety features

Features	STM32F2/F4	STM32F7	STM32H7
Dual watchdogs: Independent watchdog and system window watchdog	•	•	•
Backup clock circuitry with clock security system (CSS)	•	•	•
Hardware CRC unit / Programmable polynomial	• / -	• / •	• / •
Supply monitoring (POR, BOR, PVD)	•	•	•
I/O function locking	•	•	•
PWM critical register protections (write-once registers)	•	•	•
Memory protection unit (MPU) 8 zones – to ensure data integrity from invalid behavior	•	•	•
Multiple Flash memory protection levels	•	•	•
ECC Error Code Correction (SECDED) for SRAM	-	-	•
ECC Error Code Correction (SECDED) for Flash memory	-	-	•

Note: Arm Cortex-M cores also have built-in safety features (dual stack pointer, fault exceptions, and debug module).



Solutions for STM32H7 Motor control





Drive your motor with STM32H7





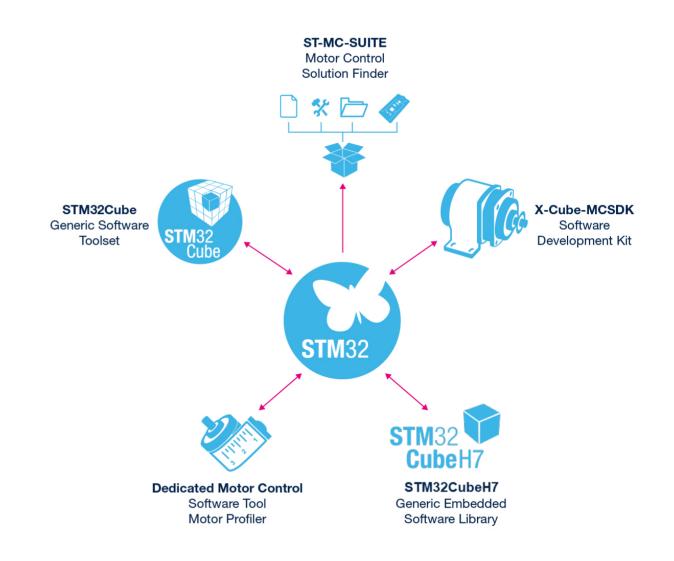






FOC (field-oriented control) for BLDC/PMSM motors

STM32 tools and software provide an integrated development environment to ease and support the design of motor control solutions.





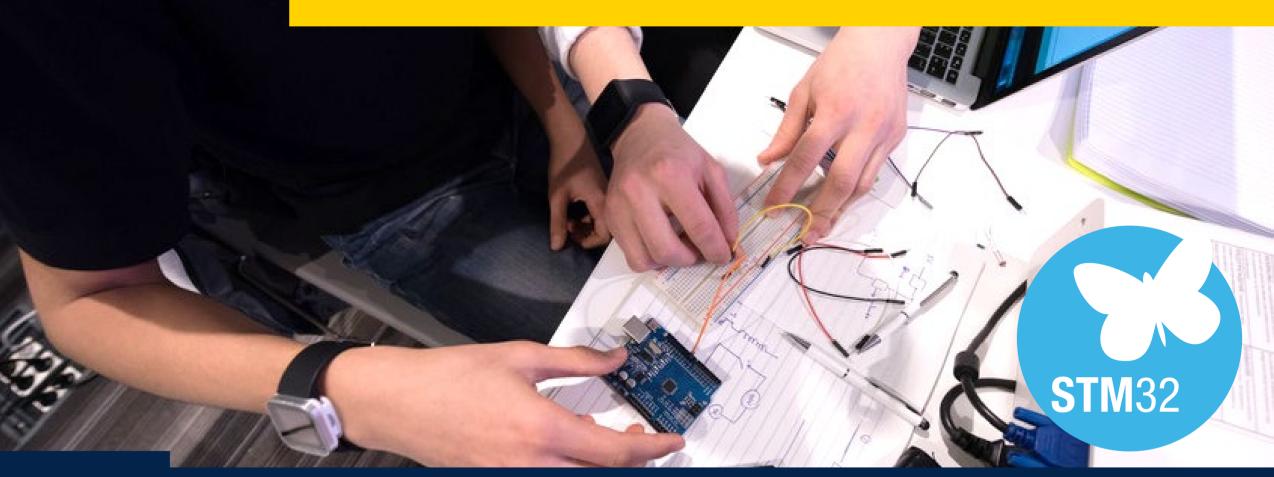
STM32H7 features for Motor control

Features	STM32H742/743/745/747/750	STM32H723/725/730	STM32H7A3//7B0	Benefit
Core	Cortex-M7 + Cortex-M4(*)	Cortex-M7	Cortex-M7	Performance and efficiency
FPU	yes	yes	yes	Performance and efficiency
MPU	yes	yes	yes	Safety
Freq CPU max	480 MHz +240 MHz(*)	550 MHz	280 MHz	Performance and efficiency
DMIPS	1027 (single core), 1027+300(*)	1177	599	Performance and efficiency
Flash / SRAM data size	128KB to 2MB / Up to 1MB	128KB to 1MB / 564KB	128KB to 2MB / 1.4MB	Performance and integration/cost
Including : ITCM/DTCM RAM	64KB / 128KB	Up to 256KB (configurable) / 128KB	64KB / 128KB	Performance and efficiency
Error Code Correction	SECDED on full memory map	SECDED on full memory map	SECDED on Flash and partial RAM (I/D-TCM and caches)	Safety
ADC SAR	3 x 16-bit 3.6 Msps	2x16-bit 3.6 Msps, 1x12-bit 5 Msps	2x16-bit 3.6 Msps	Efficiency
Other Analog	2x Comp, 2x PGA, 2xDAC, 1xDFSDM	2x Comp, 2x PGA, 2xDAC, 1xDFSDM	2x Comp, 2x PGA, 2xDAC, 2xDFSDM	Integration/cost
Advanced Motor Control timer	2x (240 MHz)	2x (275 MHz)	2x (280 MHz)	Performance and efficiency
Cache and Accelerator	16KB+16KB L1 cache + ART (*) Graphic, Crypto(**)	32KB+32KB L1 cache Graphic, Cordic, FMAC, Crypto(**)	16KB+16KB L1 cache Graphic, Crypto(**)	Performance and efficiency
Security Services (SFI and SB-SFU)	yes(**)	yes(**)	yes(**)	System Integrity
Package	LQFP100/144/176/208; BGA100/169/176/240; WLCSP156	VFQFPN68; LQFP100/144/176; BGA100/144/169/176; WLCSP115	LQFP64/100/144/176: BGA100/169/176/216/225; WLCSP132	Cost/Integration/flexibility
Max Temperature range °C	[-40 +125] Tj max 140 ° C	[-40 +125] Tj max 140 ° C	[-40 +85] Tj max 130 ° C	Integration and cost

(*) : on Dual core versions

(**) : on crypto part numbers

A complete ecosystem for single and dual-core architecture







Supported by the STM32 ecosystem

Software Hardware Embedded C Software Software 🚿 Tools STM32 STM32 Nucleo boards **Cube**MX **Cube**MCU Packages Configuration Packages **CubeIDE** Development Discovery kits **STM**32 **Cube**Programmer Programming Expansions **Cube** Monitor **Evaluation boards Cube** Expansion Monitor

Customer support













Software tools for STM32H7

Complete support of Arm Cortex-M architecture









STM32CubeMX

STM32CubeMX enhanced for Dual-core

- Configure and generate Code
- Multi-core resources allocation
- · Peripherals configuration

IDEsCompile and Debug

Multi-Core Solutions

- Partners IDE
- Free IDE based on Eclipse
- Multi-core debugging

STM32 Programming & Monitoring tools

STM32CubeProg STM32CubeMonitor

- Program the application into the chip
- Sign the application and generate license
- · Monitor variables at run-time





STM32H7 hardware solutions

Speed-up evaluation, prototyping and design (board selection guide available at the end of this presentation)













5 Evaluation Boards

6 Discovery Kits

Starting at **\$69**

6 Nucleo Boards

Full feature STM32H7 evaluation

Flexible prototyping & demo

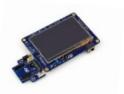
Affordable and quick prototyping





Pick the right STM32H7 development tool









STM32H7 class	Cores/Speed	Part numbers	Evaluation boards	Discovery Kits	Nucleo boards
Single Core 480 MHz STM32H74/5 Dual Core 480 MHz + 240 MHz	_	STM32H743	STM32H743I-EVAL2	-	NUCLEO-H743ZI2
		STM32H753, Crypto enabled	STM32H753I-EVAL2	-	NUCLEO-H753ZI
		STM32H750 Value Line, Crypto enabled	-	STM32H750B-DK	-
	STM32H745	-	STM32H745I-DISCO	NUCLEO-H745ZI-Q	
	480 MHz +	STM32H747	STM32H747I-EVAL	STM32H747I-DISCO STM32H747I-DISC1	-
		STM32H755/757, Crypto enabled	STM32H757I-EVAL	-	NUCLEO-H755ZI-Q
SIMMORIA I	280 MHZ	STM32H7A3	-	-	NUCLEO-H7A3ZI-Q
		STM32H7B3, Crypto enabled	STM32H7B3I-EVAL	STM32H7B3I-DK	-
		STM32H7B0, Value line, Crypto enabled	STM32H7B3I-EVAL *	STM32H7B3I-DK *	-
STM32H72/3	Single Core 550 MHz	STM32H723/733	-	-	NUCLEO-H723ZG
		STM32H725/735	-	STM32H735G-DK	-
		STM32H730, Value line, Crypto enabled	-	STM32H735G-DK *	-

^{*} Recommended board (no dedicated board for this part number)





Software, tools and services a broad ecosystem to support development



Large selection of partners already engaged for:

- Embedded software
- Software tools
- Graphics UI
- Security
- Training and services







Releasing your creativity



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STM32H7 online training



STM32H7 blog article

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