



3-Phase Inverter Reference Design Using the GD3162 with BOSCH CSL B-Sample SiC B6 Bridge

RDGD3162CSL3PEVM NEW

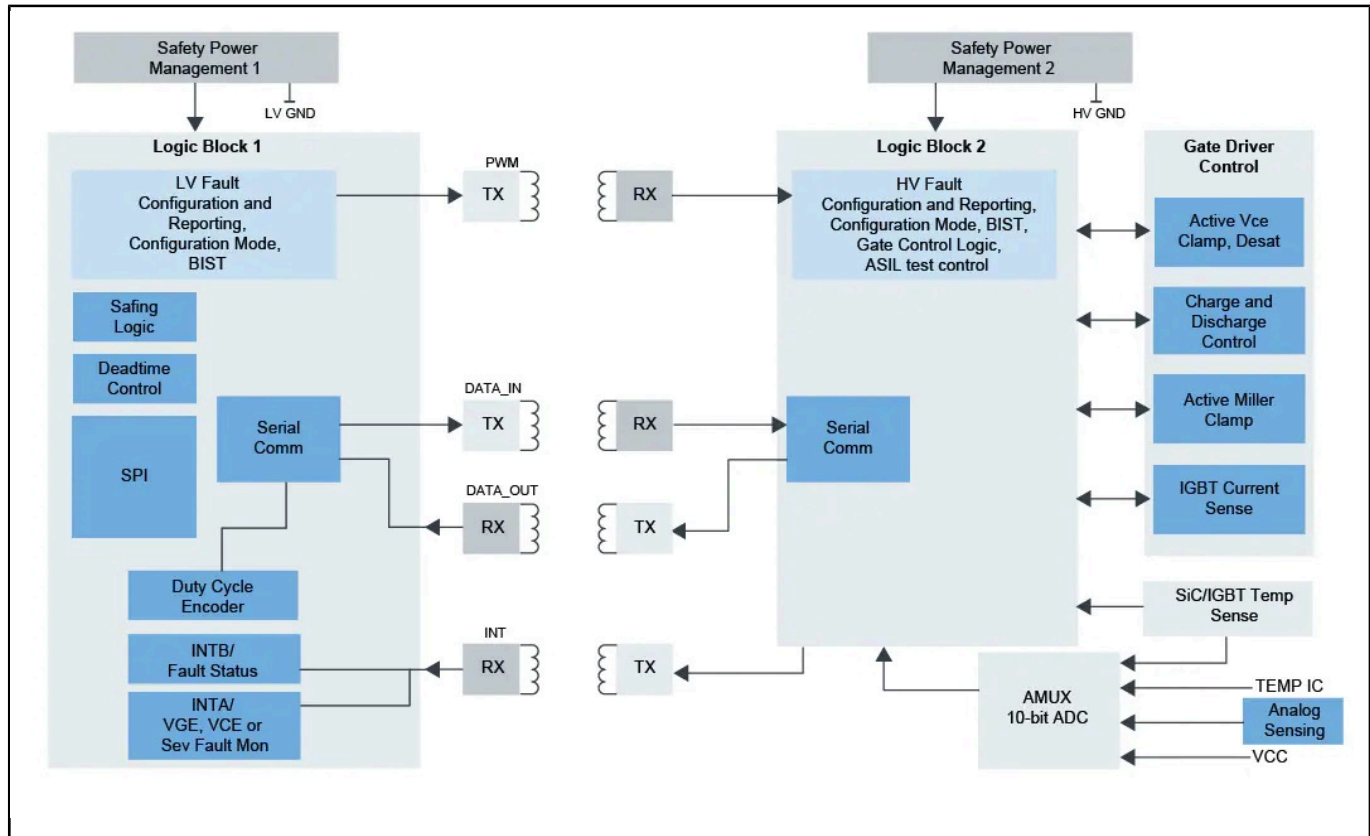
Preproduction

This page contains information on a preproduction product. Specifications and information herein are subject to change without notice. For additional information [contact support](#) or your sales representative.

Last Updated: Nov 27, 2023

RDGD3162CSL3PEVM is a three phase inverter reference design and evaluation board populated with six [GD3162](#) single channel IGBT/SiC MOSFET gate drive devices. The evaluation board is designed to connect to a compatible [BOSCH CSL B-sample module](#) for full three phase inverter applications development and testing. Included with the board is a KITGD316xTREVB MCU/translator for communication and testing using Flex GUI software.

GD3162 Block Diagram



View additional information for [3-Phase Inverter Reference Design Using the GD3162 with BOSCH CSL B-Sample SiC B6 Bridge](#).

Note: The information on this document is subject to change without notice.

www.nxp.com

NXP and the NXP logo are trademarks of NXP B.V. All other product or service names are the property of their respective owners. The related technology may be protected by any or all of patents, copyrights, designs and trade secrets. All rights reserved. © 2023 NXP B.V.