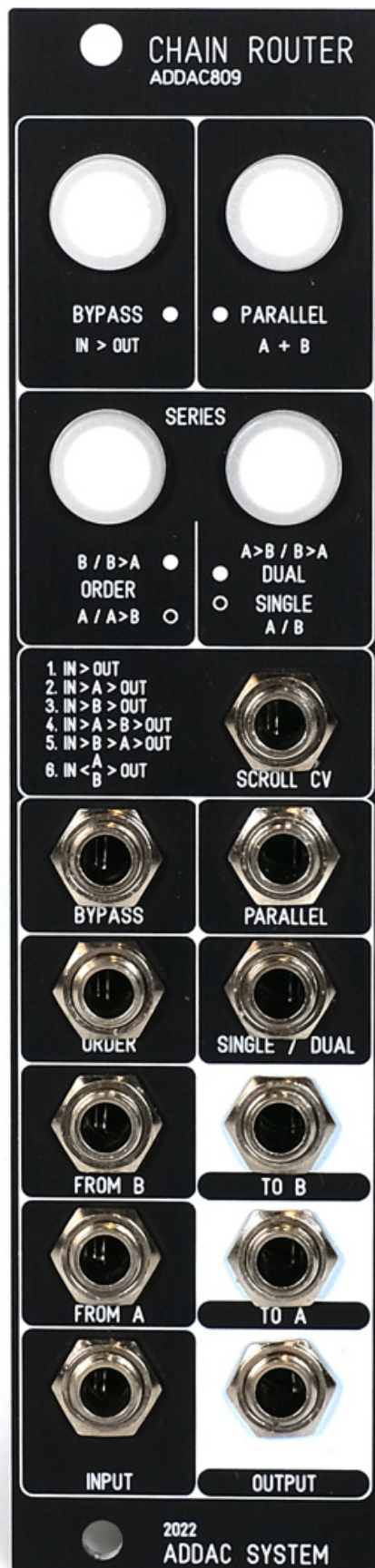




ADDAC System
Instruments for Sonic Expression
Est.2009

INTRODUCING ADDAC809 CHAIN ROUTER

USER'S GUIDE . REV01
June.2022



From Portugal with Love!

Welcome to: ADDAC809 CHAIN ROUTER

Revision.01 June.2022

USER'S GUIDE

DESCRIPTION

ADDAC809 is a dynamic CV operated I/O router that allows one source (Audio or CV) to be routed through 2 different chains (of one or more modules) before being sent to an output.

Six routing patterns are allowed:

1. IN > OUT
2. IN > CHAIN A > OUT
3. IN > CHAIN B > OUT
4. IN > CHAIN A > CHAIN B > OUT
5. IN > CHAIN B > CHAIN A > OUT
6. IN > CHAIN A > CHAIN B > OUT

A practical example is to have an audio source, a delay and a looper. And the question: should the delay be placed before or after the looper? Sometimes you may need the delay to be before the looper as you may want to sample the audio with the delay or sample the pure audio source and apply the delay afterwards. This small utility module solves this issue on the fly without having to repatch anything.

INPUTS & OUTPUTS

TO CHAIN B: _____
Connect to your Chain B input

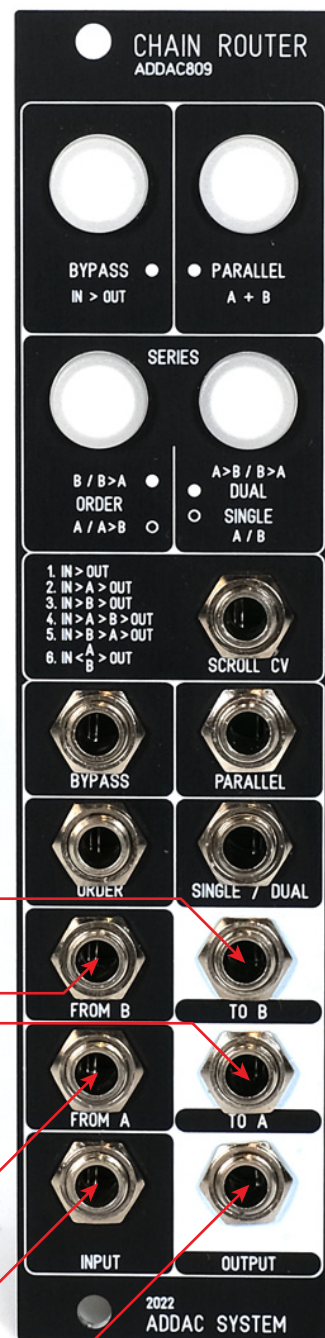
FROM CHAIN B: _____
Connect to your Chain B output

TO CHAIN A: _____
Connect to your Chain A input

FROM CHAIN A: _____
Connect to your Chain A output

INPUT: _____
Connect to your Input Source

OUTPUT: _____
Connect to your Output Source



ROUTING PATTERNS

There are 2 ways to address the six routing patterns:

Buttons: using the frontpanel push buttons and/or dedicated trigger inputs to latch between 2 states.

BYPASS:

Chooses between Pattern 1 and any other pattern

PARALLEL:

Chooses between Pattern 6 and any other pattern

ORDER:

Chooses the order A before B or B before A

DUAL/SINGLE:

Chooses to use a Dual (A>B or B>A) or Single chain (A or B)

CV:

The dedicated CV input expects a 0 to +5v input and allows to jump to the precise routing pattern using a specific voltage interval in increments of aprox. 0.83V:

Pattern 1: 0v to 0.83v

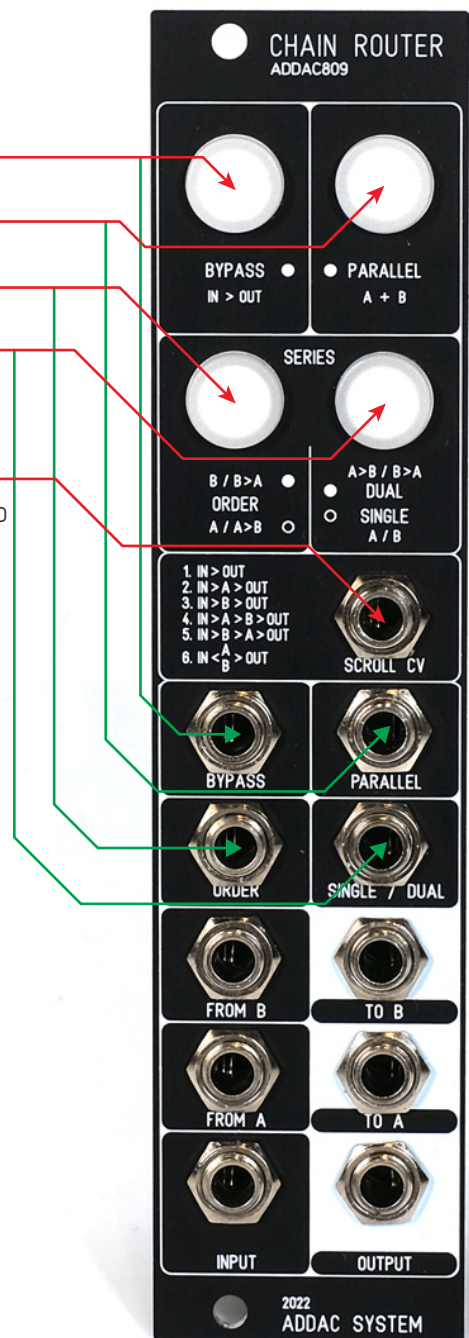
Pattern 2: 0.83v to 1.66v

Pattern 3: 1.66v to 2.5v

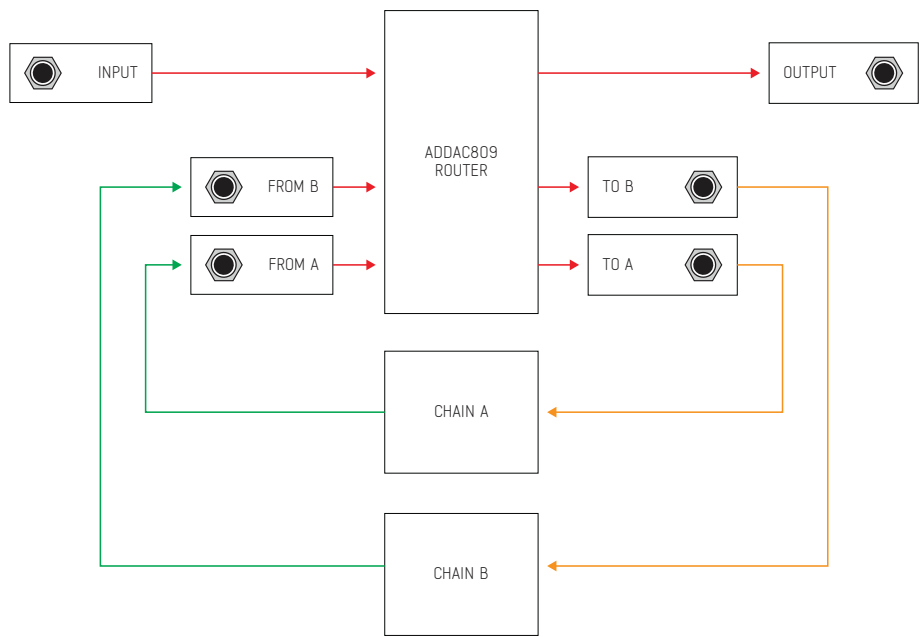
Pattern 4: 2.5v to 3.33v

Pattern 5: 3.33v to 4.16V

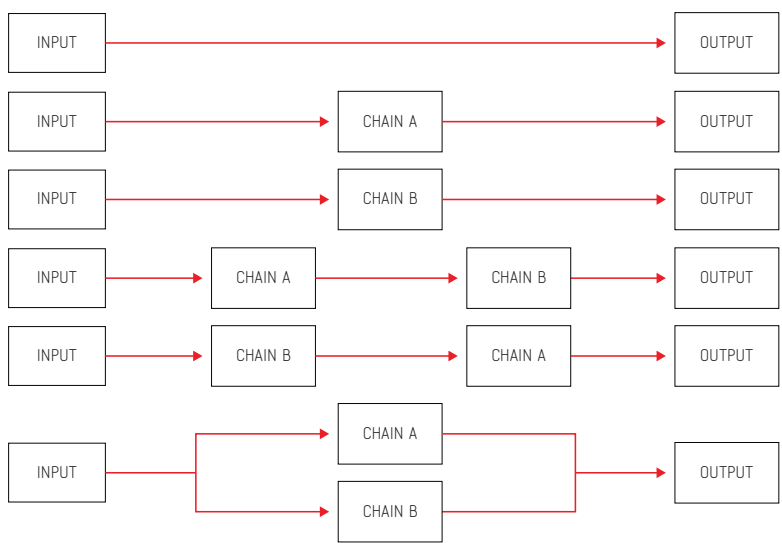
Pattern 6: 4.16v to 5V



I/O FLOW DIAGRAM



STATES



For feedback, comments or problems please contact us at:
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