

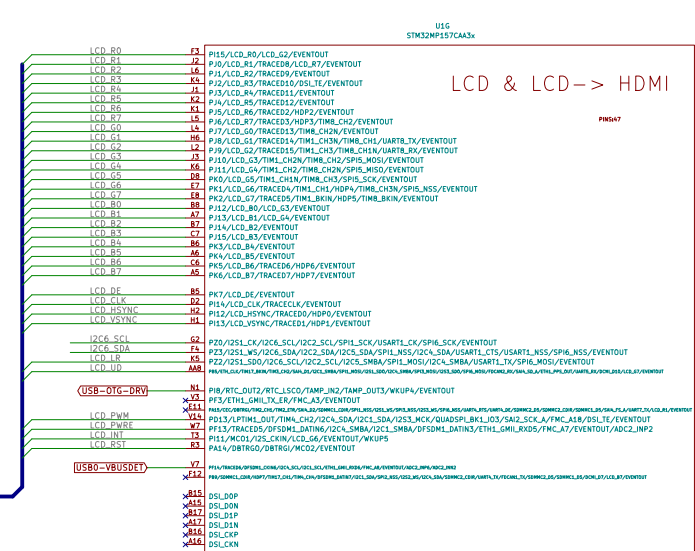
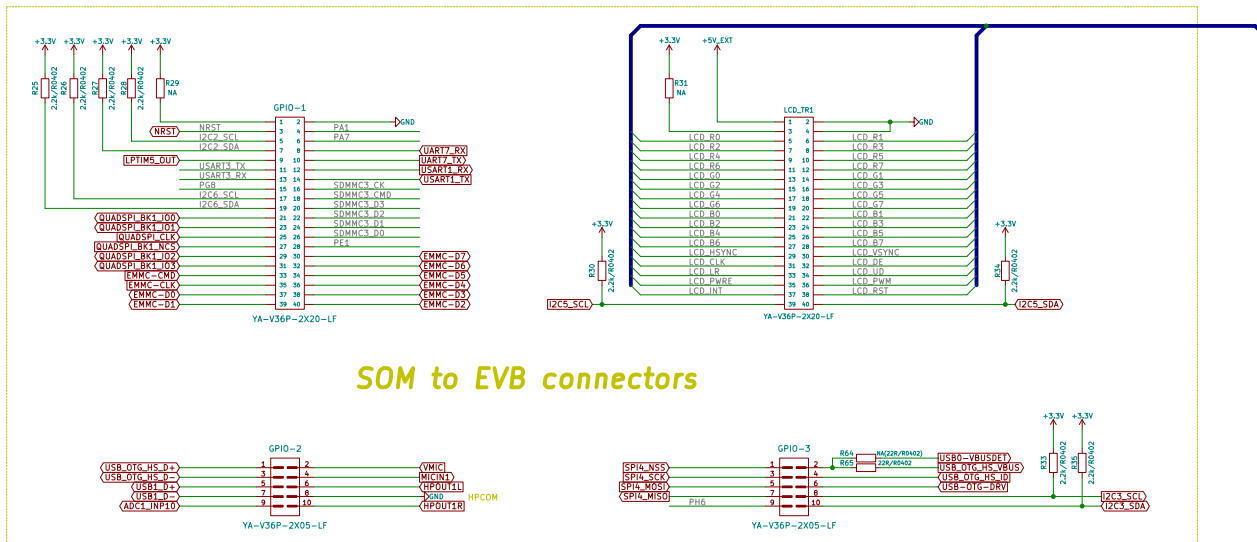
DDR3L:512MBytes

Note:
=====
We have used a number of fully compatible, but different DDR3 memories due to supply unavailability.
In such cases the memory part name in the schematic might remain outdated.
It is recommended to always refer to the exact memory name printed on the component itself.

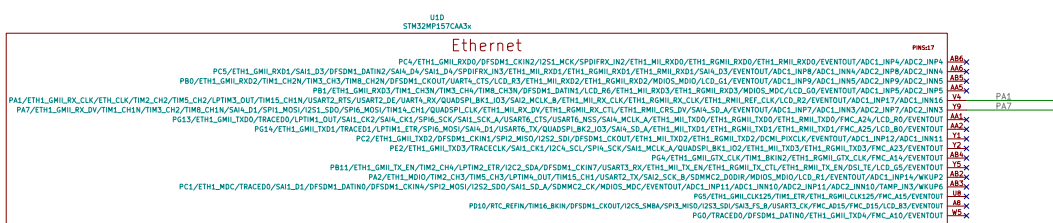
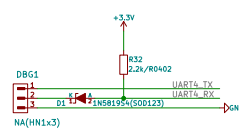
DDR3L Memory
 <<> 2021
 OLIMEX LTD, Bulgaria
 Sheet: /
 File: STMP1-SOM_Rev_C.sch
Title: STMP1-SOM
 Size: A3 Date: 2021-01-15 Rev: C
 KiCad E.D.A. kicad 5.1.6-c6e7f7d87ubuntu18.04.1 Id: 1/5

We have used a number of fully compatible, but different DDR3 memories due to supply unavailability. In such cases the memory part name in the schematic might remain outdated. It is recommended to always refer to the exact memory name printed on the component itself.

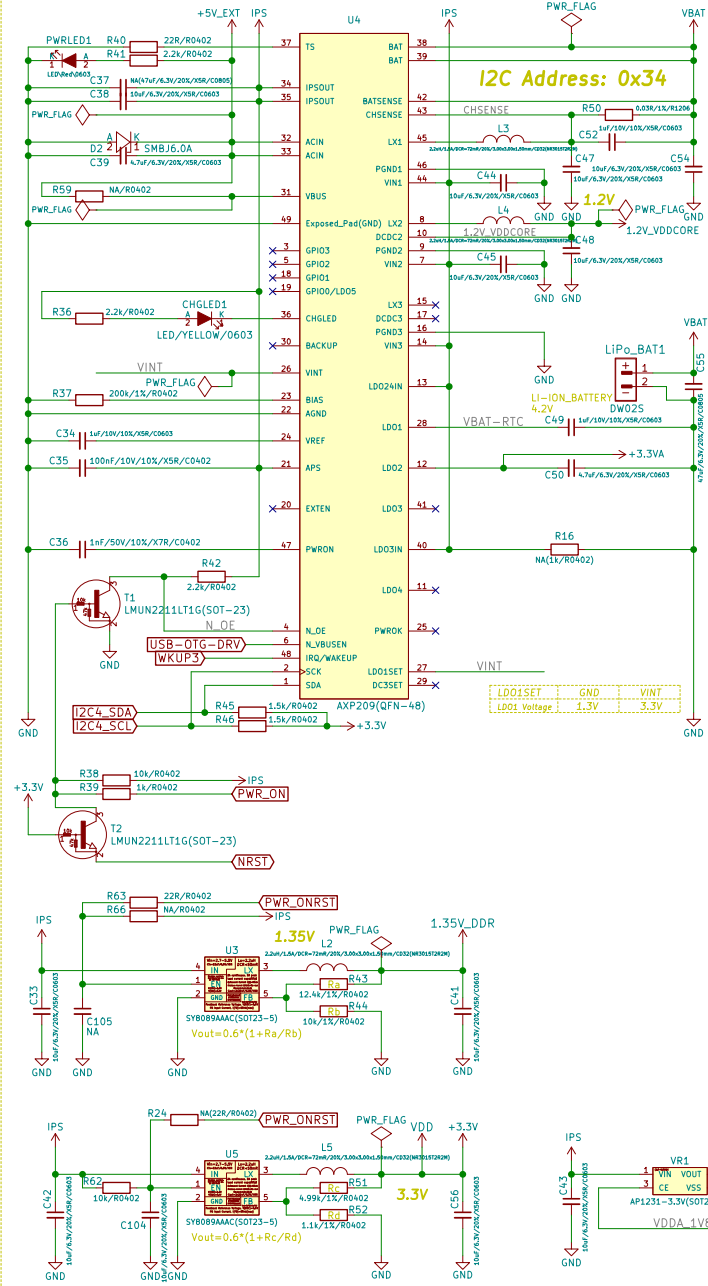
Rev: C
Id: 1/5



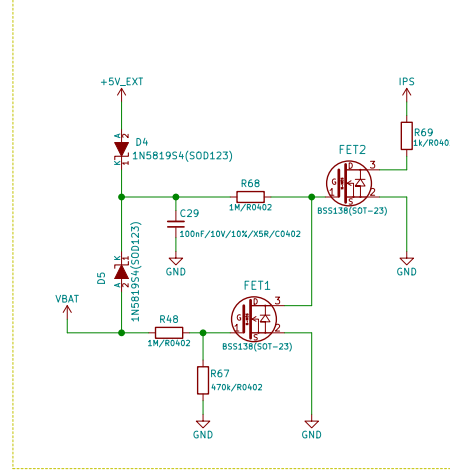
Debug UART



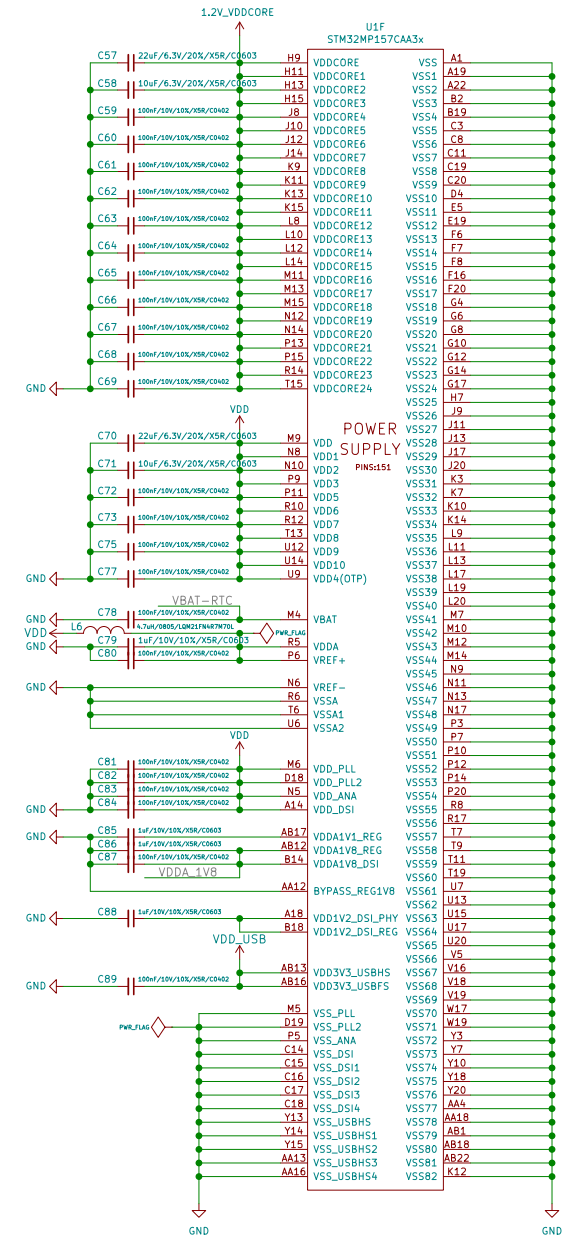
Power Supply



IPS Discharge Circuit

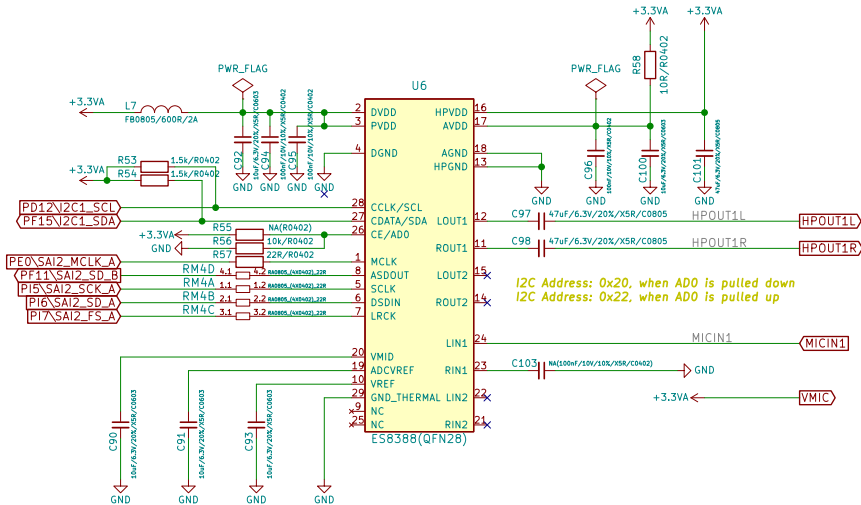


Fiducials:



<c> 2021
OLIMEX LTD, Bulgaria
Sheet: /Power_Supply/
File: Power_Supply.sch
Title: STMP1-SOM
Size: A3 Date: 2021-01-15
KiCad E.D.A. KiCad 5.1.6-c6e7f7d87ubuntu18.04.1
Rev: C
Id: 4/5

Audio



Rev: C
Id: 5/5