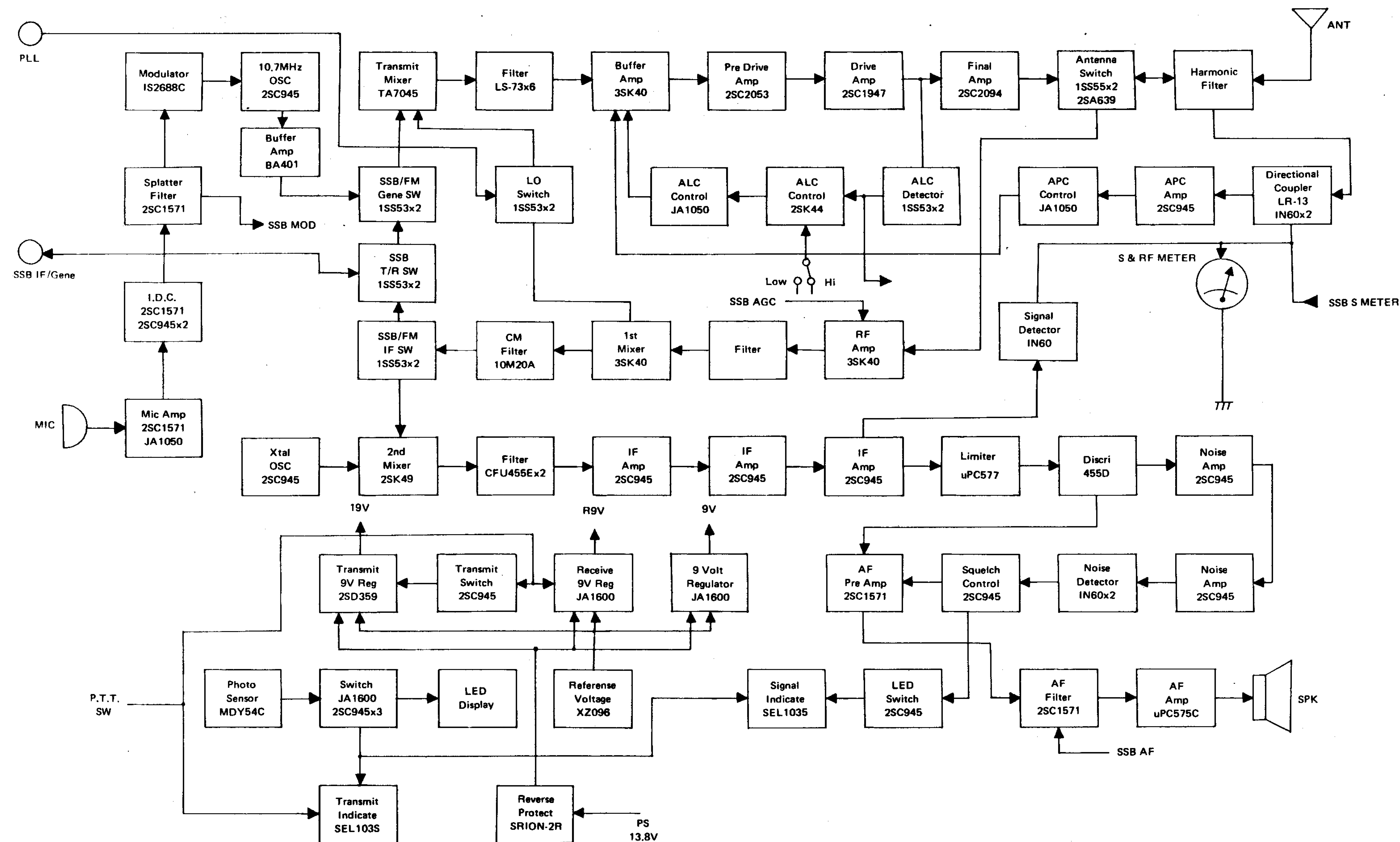
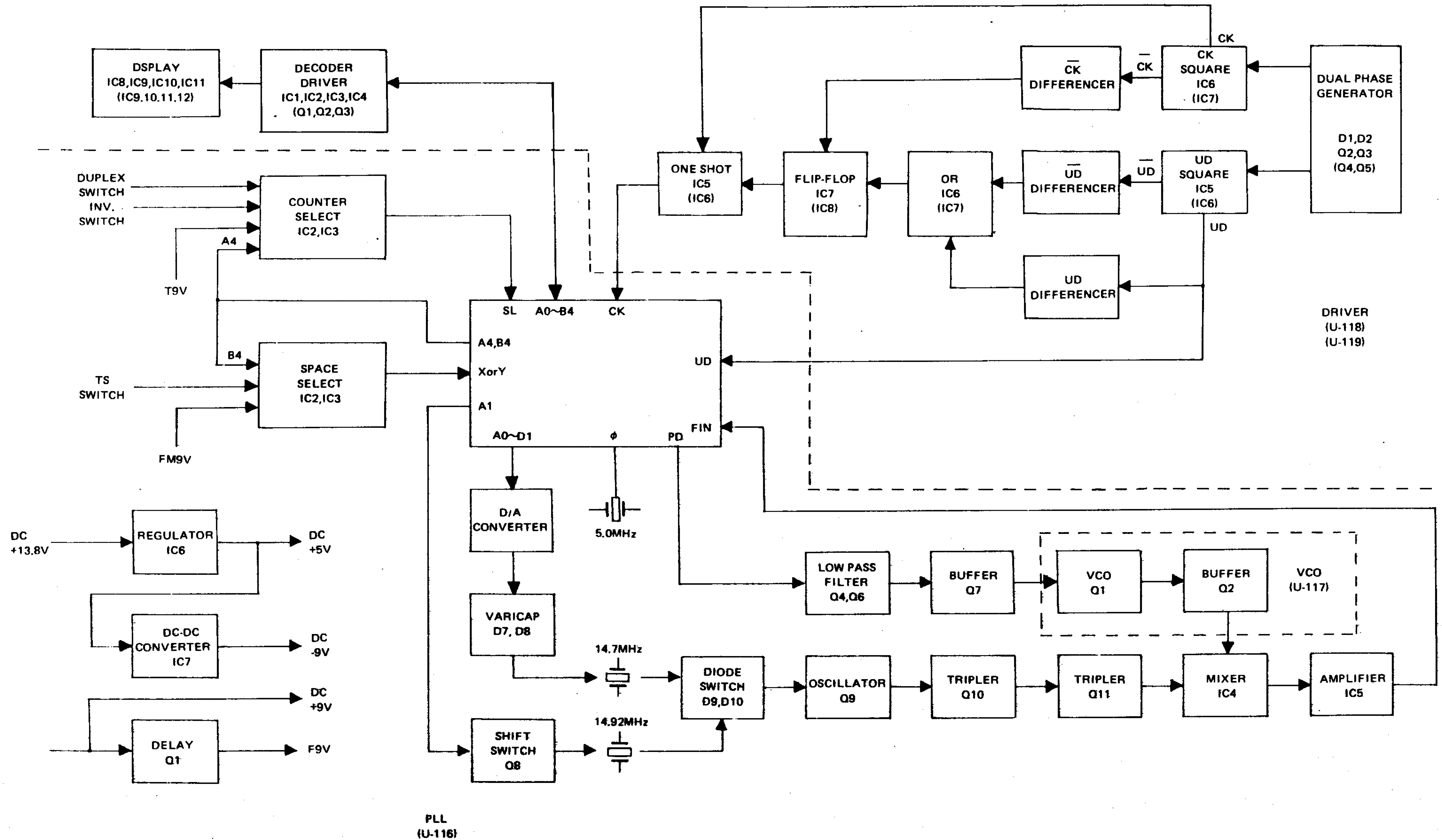
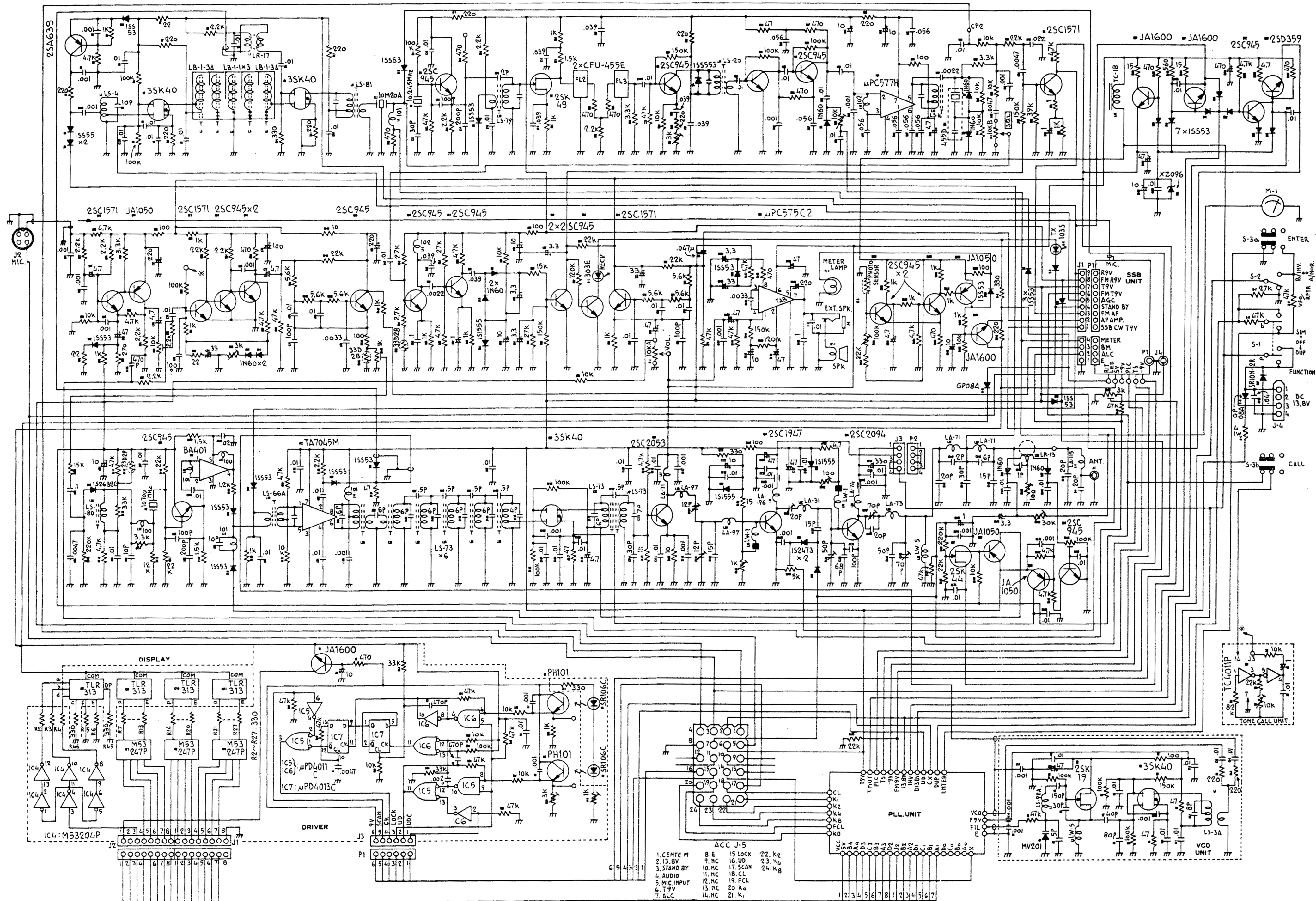


SCHEMA A BLOCCHI



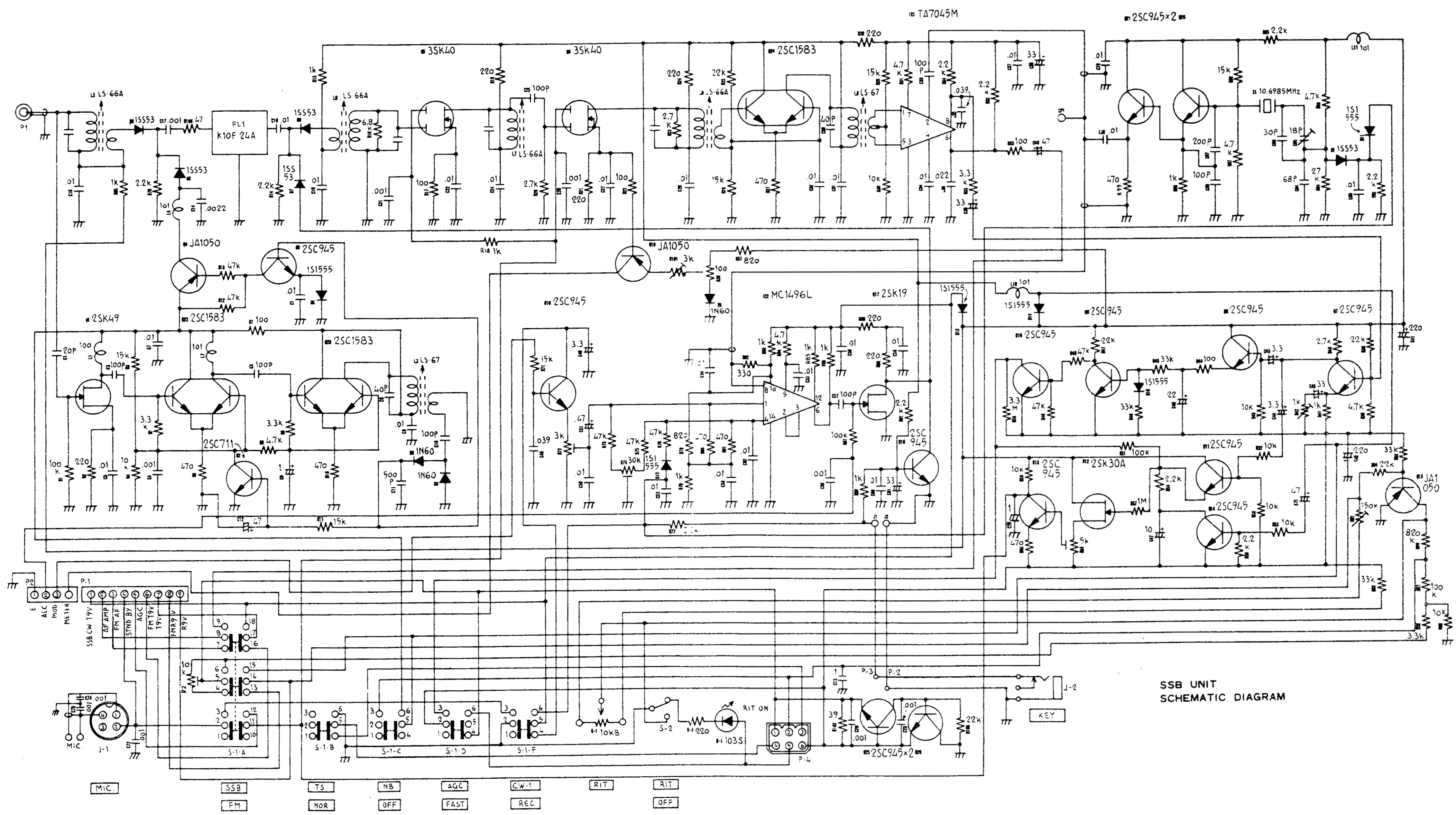
SCHEMA A BLOCCHI – CIRCUITI LOGICI





Unit	No.	Transmit														Notes
		1	2	3	4	5	6	7	8	9	10	11	12	13	14	
U-116 PLL	IC-1	(see below)														1=B 2=C 3=E
	IC-2	0	0	5	0	0	5	0	0	5	5	5	0~5	0	5	
	IC-3	5~0	0	0	0	5	5	0	5	0	5	0~5	5	5	5	
	IC-4	5	2.6	E	1.9	5	6.7	7.5	7.5							
	IC-5	5.1	1.9	1.9	E	5.4	3	6								
	IC-6	10.6	E	5.0												

Unit	No.	Receive														Notes
		1	2	3	4	5	6	7	8	9	10	11	12	13	14	
U-116 PLL	IC-7	5	-9.3													1=Output 2=Input 1=B 2=C 3=E
	IC-8	13.8	0	8.4												
U-116 PLL	IC-1	1	2	3	4	5	6	7	8	9	10	11	12	13	14	21~28 D.LSW.ON-5V T.S.W.ON-0 29~33 D.L.SW.ON-5V
		5	1.5	0.8	1.4	5	0	0	0	0	0	0	0	0	0	
		15	16	17	18	19	20	21	22	23	24	25	26	27	28	
		0	0	0	0	0~5	0	0~5	0~5	0~5	0~5	0~5	0~5	0~5	0~5	
		29	30	31	32	33	34	35	36	37	38	39	40			
		0~5	0~5	0~5	0~5	0~5	0	5	0	0	0	0	2.8			



SSB UNIT
SCHEMATIC DIAGRAM

