



| SECURE CONNECTIONS
FOR A SMARTER WORLD |

QUICK START GUIDE

MC56F80000-EVK

MC56F80000 EVALUATION KIT



GET TO KNOW THE MC56F80000-EVK

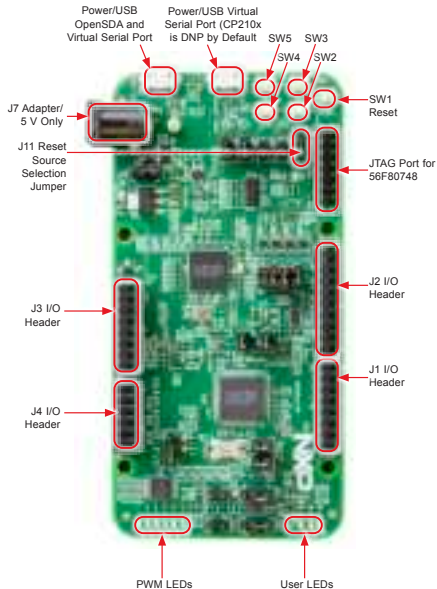


Figure 1: MC56F80000-EVK Callouts

NC-2	1-GF1	NC-19	20-GC15
VDD-4	3-GE7	NC-17	18-GC14
RESET-6	5-GE4	ADC_MID-15	16-AREF
VDD-8	7-GE5	GB1_OPB_P-13	14-GND
P5V-10	9-GE2	GA1_OPAT_P-11	12-GC9
GND-12	11-GE3	GB2_RC-9	10-GC8
GND-14	13-GE0	GA2_RC-7	8-GC10
P5V_DC_IN-16	15-GE1	GA3/GB3_RC-5	6-GE7
		GB0_IN-3	4-GE6
		GA0_IN-1	2-GF6
ANA3/3B-2	1-NC	NC-15	16-GC5
GA4-4	3-NC	NC-13	14-GC7
GA5-6	5-NC	NC-11	12-GF3
GB1-8	7-NC	NC-9	10-GC4
GB4-10	9-NC	NC-7	8-GF4
GB5-12	11-NC	NC-5	6-GF5
		GC6-3	4-GC2
		NC-1	2-GC3

Figure 2: MC56F80000-EVK Pin-Outs

OPERATION NOTES

1. MC56F80000-EVK can be powered by either of the USB connectors (J12, J26), or the adapter input (J7). Beware that only an adapter with 5V output can be used.
2. When the board is powered up, a green LED D2 will illuminate, indicating 3.3V is on. If the board is powered by USB connector (J12), an orange LED D4 will also illuminate, indicating K26 is powered.
3. J12 is the on-board OPENSDA (realized by K26) connector which can be used to debug/program MC56F80748. It provides a virtual serial port as well.
4. J26 is the virtual serial port connector. CP210x USB to UART bridge VCP Drivers are needed. CP210x is not populated by default on the board.
5. J10 is the JTAG connector for MC56F80748. Remember to remove four jumpers on J13 when this JTAG is used. This is to avoid the impact of on-board OPENSDA circuit.
6. Pin2 and pin3 of J11 are connected by default to enable the external reset from SW1.



GET STARTED

Download installation software and documentation under **“Jump Start Your Design”** at **www.nxp.com/MC56F80000-EVK**.

SUPPORT

Visit **www.nxp.com/support** for a list of phone numbers within your region.

WARRANTY

Visit **www.nxp.com/warranty** for complete warranty information.



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