1. JY-6312 Specifications

1.1 Overview

The JY-6312 is an advanced channel-to-channel isolated thermocouple input module that offers exceptional performance and versatility for a wide range of applications. With its 16 channels of measurements, it allows simultaneous measurement of multiple thermocouples, making it ideal for various industrial and testing environments. The module boasts high accuracy of up to 0.05% full-scale, ensuring precise and reliable temperature measurements. Its 24-bit resolution captures even the slightest temperature variations with great detail. Additionally, the JY-6312 incorporates 50 Hz/60 Hz noise rejection capabilities, minimizing interference from power line frequencies commonly found in industrial settings. It also includes open thermocouple detection, alerting users to any broken or disconnected thermocouple circuits. The module supports a wide range of voltage inputs, making it compatible with various thermocouple types. With its 60 Vrms channel-to-channel isolation, the JY-6312 provides reliable measurements even in noisy and demanding environments. Whether for white goods testing, in-vehicle data logging, battery stack testing, or other industrial applications, the JY-6312 offers the accuracy, reliability, and flexibility required for precise thermocouple measurements.

Please download JYTEK <JYPEDIA>, you can quickly inquire the product prices, the key features and available accessories.

1.2 Main Features

- 16 channels simultaneous measurement
- Up to 220 ppm full scale accuracy
- 24 bits resolution
- 50 Hz/60 Hz noise rejection
- 4 cold-junction compensation channels provided by 1 TB-6312 terminal blocks
- 60 Vrms channel-to-channel isolation
- Open Thermocouple Detection
- Voltage range supported: ±1.25V / ±625mV / ±312.5mV ±156.2mV/ ±78.125 mV
- Support R/S/B/J/T/E/K/N/C/A types of thermocouples
- Simultaneous measurement of thermocouples and voltages

1.3 Hardware Specifications

1.3.1 Input Characteristic

Number of channels	16 channels
ADC resolution	24 bits
Type of ADC	Σ-Δ
Sensor support	B/S/R/J/T/E/K/N/C/A thermocouple
Sampling mode	Simultaneous sampling
Sampling rate	0.5 Sa/s to 100 Sa/s
Voltage Range	±1.25V / ±625mV / ±312.5mV / ±156.2mV/ ±78.125 mV
Temperature Range	Full I.E. T.E, N.B. E.S thermocouple range
Overvoltage protection	±20 V between IC+ and IC-
ESD protecting	2 kV
Synchronous acquisition	Yes
Storage depth	64Mbit Samples
Differential input impedance	> 1 GΩ
Maximum DC linearity	±15 ppm

Table 1 Input Characteristic

1.3.2 Basic Voltage Accuracy

Sample Rate: 8 Sample/s

JY-6312 Basic Accuracy = ±(% Reading + % Range)

Nominal Range (V)	24 Hour Tcal ±1°C			90 Days Tcal±5°C			Temperature Coefficient (per °C)		24 Hour Offset Accuracy (μV)	90 Days Offset Accuracy (μV)
±0.078125	0.016	+	0.005	0.024	+	0.005	0.0015	+	40	72
0.1562	0.015	+	0.002	0.019	+	0.002	0.0009	0.0001	28	52
±0.25	0.016	+	0.002	0.021	+	0.002	0.0014	+	45	70
0.625	0.016	+	0.002	0.022	+	0.002	0.0011	0.0001	120	160
1.25	0.016	+	0.001	0.020	+	0.001	0.0011	0.0001	250	300

Typical 90 days offset accuracy by 1°C step (ppm)

Table 2 Basic Voltage Accuracy (@ 8 Sample/s)

Sample Rate: 100 Sample/s

JY-6312 Basic Accuracy = ±(% Reading + % Range)

Nominal Range (V)	24 Hour Tcal ±1°C			90 Days Tcal±5°C			Temperature Coefficient (per °C)		24 Hour Offset Accuracy (μV)	90 Days Offset Accuracy (μV)
±0.078125	0.017	+	0.007	0.024	+	0.007	0.0015	+	40	74
0.1562	0.017	+	0.005	0.021	+	0.005	0.0009	0.0001	41	47
±0.25	0.017	+	0.005	0.023	+	0.005	0.0014	+	80	86
0.625	0.016	+	0.005	0.022	+	0.005	0.0011	0.0001	140	170
1.25	0.016	+	0.005	0.020	+	0.005	0.0011	0.0001	220	270

Typical 90 days offset accuracy by 1°C step (ppm)

Table 3 Basic Voltage Accuracy (@ 100 Sample/s)

Sample Rate: 160 Sample/s

JY-6312 Basic Accuracy = ±(% Reading+% Range)

Nominal Range (V)	24 Hour Tcal ±1°C			90 Days Tcal±5°C			Temperature Coefficient(/°C)			24 Hour Full Scale Accuracy (μV)	90 Days Full Scale Accuracy (μV)
[illegible]	[illegible]	+	[illegible]	[illegible]	+	[illegible]	[illegible]	–	[illegible]	[illegible]	[illegible]
0.1562	0.018	+	0.007	0.022	+	0.007	0.0009		0.0001	39	45
[illegible]	[illegible]	+	[illegible]	[illegible]	+	[illegible]	0.0014	–	0.0001	[illegible]	[illegible]
0.625	0.017	+	0.001	0.023	+	0.005	0.0011		0.0001	140	160
1.25	0.016	+	0.012	0.030	+	0.012	0.0011		0.0001	250	400

[illegible]

Table 4 Basic Voltage Accuracy (@ 160 Sample/s)

1.3.3 Temperature Measurement Accuracy

Temperature Measurement Accuracy(℃)		
Thermocouple Type	Temperature Range(℃)	Sample Rate (S/s)
		8
J	-210 to 0	0.2
	0 to 760	0.2
	760 to 1200	0.3
K	-200 to 0	0.2
	0 to 500	0.2
	500 to 1300	0.5
N	-200 to 0	0.3
	0 to 600	0.2
	600 to 1300	0.4
T	-200 to 0	0.2
	0 to 400	0.1
E	-200 to 0	0.2
	0 to 1000	0.3
R	-50 to 250	0.8
	250 to 1064	0.5
	1064 to 1664.5	0.6
	1664.5 to 1768.1	0.7
S	-50 to 250	0.8
	250 to 1064	0.5
	1064 to 1664.5	0.7
	1664.5 to 1768.1	0.8
B	250 to 700	3.2
	700 to 1820	0.7
C	0 to 2315	1.3
A	100 to 480	0.3
G	0 to 2315	1.8
D	0 to 2315	1.4
Test at 78.125 mV range, using data over 90 days (±5°C)		
Reference (Cold Junction Temperature): same as Operating Temperature		
The measurement errors do not include the errors from the thermocouple		
Terminal Block: TB-6312; Cable: ACL-2026868-01		

Table 5 Temperature measurement accuracy

1.3.4 Channel Isolation

JY-6312 has new designs including 60 Vrms channel-to-channel isolation and noise reduction.

1.3.5 Simultaneous Measurement of Thermocouples and Voltages

JY-6312 not only measures thermocouples but also has the capability to measure ±1.25V voltage (with a total of 5 voltage ranges), allowing simultaneous measurement of thermocouples and voltages.

1.3.6 Open Thermocouple Detection (OTD)

OTD selection:	Software
OTD detection:	Per channel
OTD enabled burnout current:	100nA
OTD disabled input current:	1 nA(SE) 200 pA(DS)

Table 6 Open Thermocouple Detection (OTD)

1.3.7 Common Mode Voltage Range

Channel-to-channel	± 60 VDC
COM-Earth ground	± 60 VDC

Table 7 Common Mode Voltage Range

1.3.8 CMRR

Rejection of channel-to-channel common mode voltages	
Sample Rate <= 8 S/s, best 50 Hz rejection, best 60 Hz rejection	148 dB
Sample Rate > 8 S/s	94 dB
Rejection of channel-to-earth ground common mode voltages	
Sample Rate <= 8 S/s best 50 Hz rejection, best 60 Hz rejection	195 dB
Sample Rate > 8 S/s	97 dB

Table 8 CMRR

1.3.9 PFI Specifications

Number of channels	2 channels PFI<0..1>
External digital trigger	Trigger voltage: 5V TTL Trigger edge: rising/falling
Direction	Input

JY-6312's PFI is only used for external digital triggering

Table 9 PFI Specifications

1.3.10 System Noise

Sample Rate(Sample/s)	Range(V)	SystemNoise(uVrms)
8	0.078125	0.42
8	0.1562	0.49
8	0.3125	0.81
8	0.625	0.93
8	1.25	4.4
100	0.078125	0.99
100	0.1562	3.7
100	0.3125	2.6
100	0.625	4.6
100	1.25	32
160	0.078125	1.1
160	0.1562	2
160	0.3125	2.3
160	0.625	4.6
160	1.25	42

Table 10 System Noise

1.3.11 Power Line Noise Rejection

Sampling Rate(Sample/s)	50Hz Rejection(dB)	60Hz Rejection(dB)
>= 10	0	0
9	0	-90
8	-65	-65
6	-66	-66
4	-70	-70
3	-72	-72
2	-74	-74

Table 11 Power Line Noise Rejection

1.3.12 Digital Trigger

	PXIe-6312	PCIe-6312	TX-6312	USB-6312
Trigger source	PXI_TRIG<0..7> PXI_STAR PFI<0..1>	SSI<0..7> PFI<0..1>	SSI<0..7> PFI<0..1>	SSI<0..7> PFI<0..1>
Trigger Mode	Start Reference ReTrigger	Start Reference ReTrigger	Start Reference ReTrigger	Start Reference ReTrigger
Polarity	Software selectable	Software selectable	Software selectable	Software selectable
*SSI<0..7> definition please refer to section 6.7				

Table 12 Digital Trigger

1.3.13 Bus Interface

	PXIe-6312	PCIe-6312	USB-6312
Bus Type	x4 PXI Express peripheral module Specification V1.0 compliant	x4 PCI Express 2.0	Type-C USB 3.0

Table 13 Bus Interface

1.3.14 Physical Characteristics

Product Model	Size(mm)	Weight(g)
PXIe 6312	160 x 100	196
PCIe-6312	167.7 x 111.2	177
*Length including connectors		

Table 14 Physical Characteristics

1.3.15 Power Requirements

3.3V	500 mA
12V	520 mA

Table 15 Power Requirements

1.3.16 Environment Specifications

Operating Environment

Ambient temperature range	0 °C to 50 °C
Relative humidity range	20% to 80%, noncondensing

Table 16 Operating Environment

Storage Environment

Ambient temperature range	-20 °C to 80 °C
Relative humidity range	10% to 90%, noncondensing

Table 17 Storage Environment

2. Order Informations

- PXIe-6312 (PN: JY8475773-01)

  16-ch 24-bit PXIe ch-to-ch isolated thermocouple input module

- PCIe-6312 (PN: JY2704408-01)

  16-ch 24-bit PCIe ch-to-ch isolated thermocouple input module

- USB-6312 (PN: JY9335442-01)

  16-ch 24-bit USB ch-to-ch isolated thermocouple input module

- Accessories

  Teminal Block:

  TB-6312 (PN: JY1227130-01) 68-Pin SCSl Shielded l/0 Connector Block with cold junction sensor and shunt resistor

  Cable:

  ACL-2026868-1 (PN: JY2026868-01) 1 M 68pin VHDCI68M-SCSI68M 100 ohm all shielded cable

  ACL-2026868-2 (PN: JY2026868-02) 2 M 68pin VHDCI68M-SCSI68M 100 ohm all shielded cable

Table of Contents

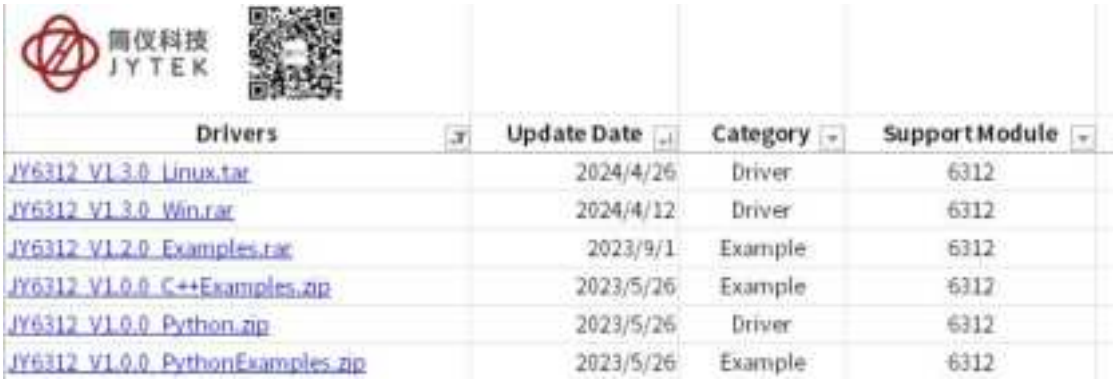
3. Introduction

3.1 Overview

The JY-6312 is an advanced channel-to-channel isolated thermocouple input module that offers exceptional performance and versatility for a wide range of applications. With its 16 channels of measurements, it allows simultaneous measurement of multiple thermocouples, making it ideal for various industrial and testing environments.

3.2 JYPEDIA

We provide many sample programs for this device. Please download the sample programs for this device. You can download a JYPEDIA excel file from our web www.jytek.com. Open JYPEDIA and search for JY-6312 in the driver sheet, select **JY6312 Examples.zip**. In addition to the download information, JYPEDIA also has a lot of other valuable information, JYTEK highly recommend you use this file to obtain information from JYTEK.



Drivers	Update Date	Category	Support Module
JY6312_V1.3.0_Linux.tar	2024/4/26	Driver	6312
JY6312_V1.3.0_Win.rar	2024/4/12	Driver	6312
JY6312_V1.2.0_Examples.rar	2023/9/1	Example	6312
JY6312_V1.0.0_C++Examples.zip	2023/5/26	Example	6312
JY6312_V1.0.0_Python.zip	2023/5/26	Driver	6312
JY6312_V1.0.0_PythonExamples.zip	2023/5/26	Example	6312

Figure 1 JYPEDIA Information

4. Hardware Specifications

4.1 Front Panel

Figure 2 JY-6312 Front Panel

JY-6312 provides 16 channels of thermocouple measurements and 2 digital input channels (for digital triggering).

4.2 Pin Definition

Connector 0

Channel	Pin	Defination
CH0	P35	TC-, Voltage measurament high side
	P1	TC-, Voltage measurement low side
CH1	P37	TC-, Voltage measurament high side
	P3	TC-, Voltage measurement low side
CH2	P38	TC-, Voltage measurament high side
	P4	TC-, Voltage measurement low side
CH3	P40	TC-, Voltage measurament high side
	P6	TC-, Voltage measurement low side
CH4	P41	TC-, Voltage measurament high side
	P7	TC-, Voltage measurement low side
CH5	P42	TC-, Voltage measurament high side
	P9	TC-, Voltage measurement low side
CH6	P44	TC-, Voltage measurament high side
	P10	TC-, Voltage measurement low side
CH7	P46	TC-, Voltage measurament high side
	P12	TC-, Voltage measurement low side
CH8	P48	TC-, Voltage measurament high side
	P15	TC-, Voltage measurement low side
CH9	P51	TC-, Voltage measurament high side
	P17	TC-, Voltage measurement low side
CH10	P52	TC-, Voltage measurament high side
	P18	TC-, Voltage measurement low side
CH11	P54	TC-, Voltage measurament high side
	P20	TC-, Voltage measurement low side
CH12	P55	TC-, Voltage measurament high side
	P21	TC-, Voltage measurement low side
CH13	P58	TC-, Voltage measurament high side
	P24	TC-, Voltage measurement low side
CH14	P59	TC-, Voltage measurament high side
	P25	TC-, Voltage measurement low side
CH15	P61	TC-, Voltage measurament high side
	P27	TC-, Voltage measurement low side

Channel	Pin	Defination
GND	P30	GND
	P31	
	P32	
	P33	
	P34	
PFI	P64	PFI 0
	P65	PFI 1
Other	P66	Reseved. Do not connect
	P67	Reseved. Do not connect
	P2	Reseved. Do not connect
	P36	Reseved. Do not connect
	P5	Reseved. Do not connect
	P39	Reseved. Do not connect
	P8	Reseved. Do not connect
	P42	Reseved. Do not connect
	P11	Reseved. Do not connect
	P45	Reseved. Do not connect
	P13	Reseved. Do not connect
	P47	Reseved. Do not connect
	P14	Reseved. Do not connect
	P49	Reseved. Do not connect
	P16	Reseved. Do not connect
	P50	Reseved. Do not connect
	P19	Reseved. Do not connect
	P53	Reseved. Do not connect
	P22	Reseved. Do not connect
	P56	Reseved. Do not connect
	P23	Reseved. Do not connect
	P57	Reseved. Do not connect
	P26	Reseved. Do not connect
	P60	Reseved. Do not connect
	P28	Reseved. Do not connect
	P62	Reseved. Do not connect
	P29	Reseved. Do not connect
	P63	Reseved. Do not connect
	P68	+ 5V

Table 18 Pin Defination

4.3 Temperature Measurement Accuracy

The accuracy of the temperature measurement depends on the thermocouple, the connectors, the terminal block, and the measuring device. This chapter provides the temperature measurement accuracy specifications by JY-6312 and specified terminal block only. The effect of the thermocouple is not included.

4.3.1 Thermocouple Measurement Basics

A Thermocouple temperature measurement utilizes the "Seebeck effect", and its basic measurement principle is shown in the Figure 1.

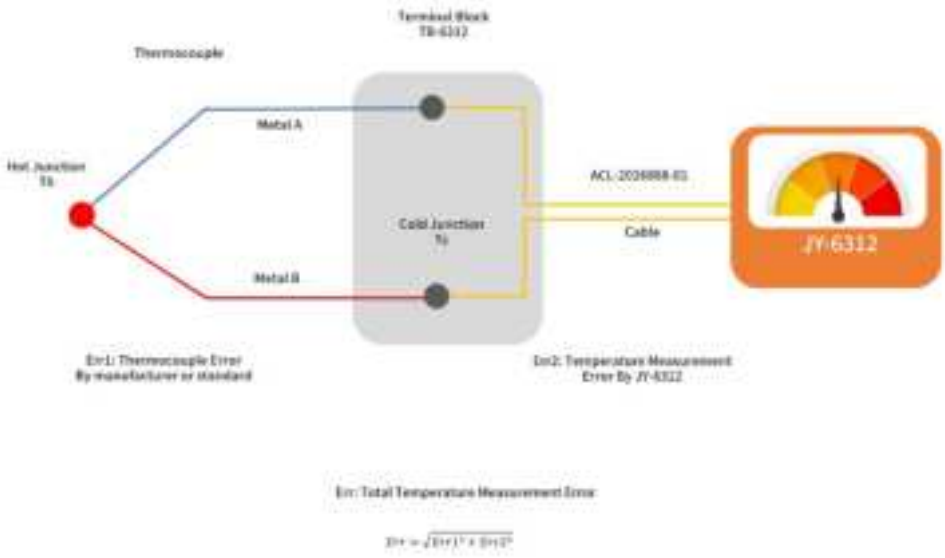


Figure 3 Thermocouple Temperature Measurement Principle

Two different types of metals A and B are connected to each other, and the temperature difference between the temperature measuring contact Th (Metal Junction) and the reference contact Tc (Thermocouple Display Instrument Contact) is used to generate the corresponding voltage, also called the Electromotive Force （EMF） in the standards. This voltage is measured by JY-6312 and is then converted the temperature values using a conversion formula defiend by the standard.

4.3.2 Thermocouple Accuracy

A thermocouple has its own accuracy, also called the error tolerance in many international standards. Table 19 shows the accuracies of common thermocouple types by two commonly used standards. Using the K-type as an example. Each K-type thermocouple falls into one of the three classes. The maximum accuracy of class 1 of the K-type thermocouple is ±1.5°C or ±(0.004*|T|), whichever is bigger. T is the measured temperature value in °C.

If the measured temperature is -30°C, 0.004*|-30| = 0.12°C, so the accuracy is ±1.5°C. If T=1000°C, 1000*0.004=4°C, so the accuracy is ±4°C.

Thermocouple Type	Tolerance Class		Temperature Range (°C)	Thermocouple Error (°C) (Larger between two columns)	
J	IEC-EN 60584-1	Class 1	-40 < T < 750	±1.5	±(0.004 × |T|)
		Class 2	-40 < T < 750	±2.5	±(0.0075 × |T|)
		Class 3	–	–	–
	ASTM E230 ANSI MC96.1	Special	0 < T < 760	±1.1	±(0.004 × |T|)
		Standard	0 < T < 760	±2.2	±(0.0075 × |T|)
K	IEC-EN 60584-1	Class 1	-40 < T < 1000	±1.5	±(0.004 × |T|)
		Class 2	-40 < T < 1200	±2.5	±(0.0075 × |T|)
		Class 3	-200 < T < 40	±2.5	±(0.015 × |T|)
	ASTM E230 ANSI MC96.1	Special	0 < T < 1260	±1.1	±(0.004 × |T|)
		Standard	-200 < T < 0 0 < T < 1250	±2.2 ±2.2	±(0.02 × |T|) ±(0.0075 × |T|)
N	IEC EN 60584-1	Class 1	-40 < T < 1000	±1.5	±(0.004 × |T|)
		Class 2	-40 < T < 1200	±2.5	±(0.0075 × |T|)
		Class 3	-200 < T < 40	±2.5	±(0.015 × |T|)
	ASTM E230 ANSI MC96.1	Special	0 < T < 1260	±1.1	±(0.004 × |T|)
		Standard	-270 < T < 0 0 < T < 1300	±2.2 ±2.2	±(0.02 × |T|) ±(0.0075 × |T|)
T	IEC EN 60584-1	Class 1	-40 < T < 350	±0.5	±(0.004 × |T|)
		Class 2	-40 < T < 350	±1.0	±(0.0075 × |T|)
		Class 3	-200 < T < 40	±1.0	±(0.015 × |T|)
	ASTM E230 ANSI MC96.1	Special	-200 < T < 0 0 < T < 350	±0.5 ±0.5	±(0.008 × |T|) ±(0.004 × |T|)
		Standard	-200 < T < 0 0 < T < 350	±1.0 ±1.0	±(0.015 × |T|) ±(0.0075 × |T|)
E	IEC-EN 60584-1	Class 1	-40 < T < 800	±1.5	±(0.004 × |T|)
		Class 2	-40 < T < 900	±2.5	±(0.0075 × |T|)
		Class 3	-200 < T < 40	±2.5	±(0.015 × |T|)
	ASTM E230 ANSI MC96.1	Special	-200 < T < 0 0 < T < 900	±1.0 ±1.0	±(0.005 × |T|) ±(0.004 × |T|)
		Standard	-200 < T < 0 0 < T < 900	±1.7 ±1.7	±(0.01 × |T|) ±(0.005 × |T|)
R & S	IEC-EN 60584-1	Class 1	0 < T < 1600	±1.0	±[1 + 0.003 × (|T| − 1100)]
		Class 2	-40 < T < 1600	±1.5	±(0.0025 × |T|)
		Class 3	–	±4.0	±(0.005 × |T|)
	ASTM E230 ANSI MC96.1	Special	0 < T < 1450	±0.6	±(0.001 × |T|)
		Standard	0 < T < 1450	±1.5	±(0.0025 × |T|)
B	IEC-EN 60584-1	Class 1	–	–	–
		Class 2	600 < T < 1700	±1.5	±(0.0025 × |T|)
		Class 3	600 < T < 1700	±4.0	±(0.005 × |T|)
	ASTM E230 ANSI MC96.1	Special	870 < T < 1700	–	±(0.0025 × |T|)
		Standard	870 < T < 1700	–	±(0.005 × |T|)
C	IEC-EN 60584-1	Class 1	–	–	–
		Class 2	426 < T < 2315	–	±(0.01 × |T|)
		Class 3	–	–	–
	ASTM E230 ANSI MC96.1	Special	–	–	–
		Standard	0 < T < 2315	–	±(0.01 × |T|)

Table 19 Err1：Thermocouple Tolerance Class Information

The accuracies given by Table 19 are valid for thermocouple material only. It is important that users verify the accuracy of the thermocouple from the thermocouple manufacturer.

4.3.3 Temperature Measurement Accuracy by JY-6312

A thermocouple converts a temperature reading to a voltage which is then measured by JY-6312. The standard provides the conversion formula for different thermocouples and for different temperature ranges. Table 20 shows the temperature measurement accuracy using JY-6312 for each type and each range of thermocouple. The operating conditions are also listed in the table.

Temperature Measurement Accuracy(℃)		
Thermocouple Type	Temperature Range(°C)	Sample Rate (S/s) 8
J	-210 to 0	0.2
	0 to 760	0.2
	760 to 1200	0.3
K	-200 to 0	0.2
	0 to 500	0.2
	500 to 1300	0.5
N	-200 to 0	0.3
	0 to 600	0.2
	600 to 1300	0.4
T	-200 to 0	0.2
	0 to 400	0.1
E	-200 to 0	0.2
	0 to 1000	0.3
R	-50 to 250	0.8
	250 to 1064	0.5
	1064 to 1664.5	0.6
	1664.5 to 1768.1	0.7
S	-50 to 250	0.8
	250 to 1064	0.5
	1064 to 1664.5	0.7
	1664.5 to 1768.1	0.8
B	250 to 700	3.2
	700 to 1820	0.7
C	0 to 2315	1.3
A	100 to 480	0.3
G	0 to 2315	1.8
D	0 to 2315	1.4
Test at 78.125 mV range, using data over 90 days (±5 °C)		
Reference (Cold Junction Temperature): same as Operating Temperature		
The measurement errors do not include the errors from the thermocouple		
Terminal Block: TB-6312; Cable: ACL-2026868-01		

Table 20 Err2：Temperature Measurement Accuracy

It is important to note that the accuracy data only includes the measurement errors by JY-6312, using the specified ACL-2026868-01 cable and the TB-68CJ termial block. It does not include the errors of the thermocouple itself. To get the total measurement accuracy, users must check with thermocouple error specifications from the thermocouple manufacturer. Section 4.3.4 provides the information how to calculate the total accuracy.

4.3.4 Total Temperature Measurement Accuracy

The total temperature measurement accuracy consists of the errors due to the thermocouple and measurement errors by JY-6312 as shown in Figure 3. It can be calculated by:

$$Total\ Accuracy\ Error = \sqrt{Err_1^2 + Err_2^2}$$

Err1 is the thermocouple error from Table 19 of Section4.3.2. Err2 is the temperature measurement accuracy from Table Table 20 of Section 4.3.3.

Table 21 shows two calculations for the total accuracies when using a class 1 K-type thermocouple to measure 100 °C and 800° C temperatures with 8 Sample/s sample rate. The two temperatures fall into different range. Hence the temperature measurement errors by JY-6312 are different.

Total Temperature Measurement Error

Sample Rate (Samples)	8	
Thermocouple Type and Class	K-1	K-1
Temperature Being Measured (°C)	100	800
Error Error from K-1 Standard (°C)	1.50	1.50
Calculated from K-1 standard, | T − 23.2041 | (°C)	0.70	3.20
Err1: Total Thermocouple Error for K-1 Standard, Class of above two lines (°C)	1.50	3.20
Err2: JY-6312 Temperature Measurement Error for K-1 (°C)	0.20	0.50
Total Accuracy, sqrt(Err1^2+Err2^2) (°C)	1.5	3.24

Table 21 Calculating Total Error

4.3.5 Accuracy Not Listed

Table 20 lists the temperature measurement accuracies for most common applications. There are other factors affecting the measurement accuracy. Most important factors are 1) when the operating temperature is beyond the Tcal±1°C range, and 2) when the cold junction reference temperature are different from the operating temperature as assumed in Table 20. It is not possible to list all these accuracies. JYTEK provides a utility in JY-6312 C# example to calculate the accuracy for those conditions. Users can enter required operating parameters to obtain accuracy not listed in Table 20

5. Software

5.1 System Requirements

JY-6312 boards can be used in a Windows or a Linux operating system.

Microsoft Windows: Windows 7 32/64 bit, Windows 10 32/64 bit.

Linux Kernel Versions: There are many Linux versions. It is not possible JYTEK can support and test our devices under all different Linux versions. JYTEK will at the best support the following Linux versions.

Linux Version Ubuntu LTS
16.04：4.4.0-21-generic(desktop/server)
16.04.6：4.15.0-45-generic(desktop) 4.4.0-142-generic(server)
18.04：4.15.0-20-generic(desktop) 4.15.0-91-generic(server)
18.04.4：5.3.0-28-generic(desktop) 4.15.0-91-generic(server)
Localized Chinese Version
[illegible] 3.10.0-862.9.1.nd7.cx.18.x86_64
[illegible] 3.10.0-957.el7.x86_64

Table 22 Supported Linux Versions

5.2 System Software

When using the JY-6312 in the Window environment, you need to install the following software from Microsoft website:

Microsoft Visual Studio Version 2015 or above,

.NET Framework version is 4.0 or above.

.NET Framework is coming with Windows 10. For Windows 7, please check .NET Framework version and upgrade to 4.0 or later version.

Given the resources limitation, JYTEK only tested JY-6312 be with .NET Framework 4.0 with Microsoft Visual Studio 2015. JYTEK relies on Microsoft to maintain the compatibility for the newer versions.

5.3 C# Programming Language

All JYTEK default programming language is Microsoft C#. This is Microsoft recommended programming language in Microsoft Visual Studio and is particularly suitable for the test and measurement applications. C# is also a cross platform programming language.

5.4 JY-6312 Hardware Driver

After installing the required application development environment as described above, you need to install the JY-6312 hardware driver.

JYTEK hardware driver has two parts: the shared common driver kernel software (FirmDrive) and the specific hardware driver.

Common Driver Kernel Software (FirmDrive): FirmDrive is the JYTEK's kernel software for all hardware products of JYTEK instruments. You need to install the FirmDrive software before using any other JYTEK hardware products. FirmDrive only needs to be installed once. After that, you can install the specific hardware driver.

Specific Hardware Driver: Each JYTEK hardware has a C# specific hardware driver. This driver provides rich and easy-to-use C# interfaces for users to operate various JY-6312 function. JYTEK has standardized the ways which JYTEK and other vendor's DAQ boards are used by providing a consistent user interface, using the methods, properties and enumerations in the object-oriented programming environment. Once you get yourself familiar with how one JYTEK DAQ card works, you should be able to know how to use all other DAQ hardware by using the same methods.

Note that this driver does not support cross-process, and if you are using more than one function, it is best to operate in one process.

5.5 Install the SeeSharpTools from JYTEK

To efficiently and effectively use JY-6312 boards, you need to install a set of free C# utilities, SeeSharpTools from JYTEK. The SeeSharpTools offers rich user interface functions you will find convenient in developing your applications. They are also needed to run the examples come with JY-6312 hardware. Please register and down load the latest SeeSharpTools from our website, www.jytek.com.

5.6 Running C# Programs in Linux

Most C# written programs in Windows can be run by MonoDevelop development system in a Linux environment. You would develop your C# applications in Windows using Microsoft Visual Studio. Once it is done, run this application in the MonoDevelop environment. This is JYTEK recommended way to run your C# programs in a Linux environment.
If you want to use your own Linux development system other than MonoDevelop, you can do it by using our Linux driver. However, JYTEK does not have the capability to support the Linux applications. JYTEK completely relies upon Microsoft to maintain the cross-platform compatibility between Windows and Linux using MonoDevelop.

6. Calibration

JY-6312 Series boards are precalibrated before the shipment. We recommend you recalibrate JY-6312 board periodically to ensure the measurement accuracy. A commonly accepted practice is one year. If for any reason, you need to recalibrate your board, please contact JYTEK.

7. Using JY-6312 in Other Software

While JYTEK's default application platform is Visual Studio, the programming language is C#, we recognize there are other platforms that are either becoming very popular or have been widely used in the data acquisition applications. Among them are Python, C++ and LabVIEW. This chapter explains how you can use JY-6312 DAQ card using one of this software.

7.1 Python

JYTEK provides and supports a native Python driver for JY-6312 boards. There are many different versions of Python. JYTEK has only tested in CPython version 3.5.4. There is no guarantee that JYTEK python drivers will work correctly with other versions of Python.

If you want to be our partner to support different Python platforms, please contact us.

7.2 C++

We recommend our customers to use C# drivers because C# platform deliver much better efficiency and performance in most situations. We also provide C++ drivers and examples in the Qt IDE, which can be downloaded from web. However, due to the limit of our resources, we do not actively support C++ drivers. If you want to be our partner to support C++ drivers, please contact us.

7.3 LabVIEW

LabVIEW is a software product from National Instruments. JYTEK does not support LabVIEW and will no longer provide LabVIEW interface to JY-6312 boards. Our third-party partners may have LabVIEW support to JY-6312 boards. We can recommend you if you want to convert your LabVIEW applications to C# based applications.

8. Appendix

8.1 Typical Measurement Error

Typical measurement error is a term used to describe the variation or uncertainty in a measurement that is repeated under the same conditions. It can be caused by random errors (chance differences between observed and true values) or systematic errors (consistent biases in measurement).

Typical measurement error can be expressed as a standard deviation (the typical error of measurement) or as a percentage of the mean (the coefficient of variation).

8.2 System Noise

System noise refers to any unwanted and random fluctuations or disturbances in a physical or electronic system that can interfere with its normal operation. System noise can arise from various sources such as electrical interference, thermal noise, environmental factors, and inherent limitations of the system's components.

In electronic systems, system noise can affect the accuracy and reliability of signal processing and communication. For example, in audio systems, system noise can lead to hissing or humming sounds, and in wireless communication systems, it can cause interference and reduce the quality of the signal.

Reducing system noise is an important consideration in the design and operation of many types of systems, and engineers use various techniques to mitigate its effects, including shielding, filtering, and signal processing algorithms.

8.3 Temperature Drift

Temperature drift refers to the phenomenon where the performance or behavior of a physical or electronic system changes as the temperature changes. Temperature drift can affect various parameters such as frequency, voltage, resistance, and sensitivity, and it can cause errors or inaccuracies in the system's operation.

In electronic systems, temperature drift can arise due to the temperature dependence of the properties of the system's components, such as resistors, capacitors, and transistors. For example, the resistance of a resistor increases with temperature, and this can affect the accuracy of voltage measurements in a circuit. Similarly, the frequency of an oscillator can drift due to the temperature dependence of its resonant circuit components.

Temperature drift is an important consideration in the design and operation of many types of systems, particularly those that require high accuracy and stability over a wide range of temperatures. Engineers use various techniques to compensate for temperature drift, including using temperature sensors to monitor and control the temperature, selecting components with low temperature coefficients, and implementing temperature compensation algorithms in software or firmware.

9. About JYTEK

9.1 JYTEK China

Founded in June, 2016, JYTEK China is a leading Chinese test & measurement company, providing complete software and hardware products for the test and measurement industry. The company has evolved from re-branding and reselling PXI(e) and DAQ products to a fully-fledged product company. The company offers complete lines of PXI, DAQ, USB products. More importantly, JYTEK has been promoting open-sourced based ecosystem and offers complete software products. Presently, JYTEK is focused on the Chinese market. Our Shanghai headquarters and production service center have regular stocks to ensure timely supply; we also have R&D centers in Xi'an and Chongqing. We also have highly trained direct technical sales representatives in Shanghai, Beijing, Tianjin, Xi'an, Chengdu, Nanjing, Wuhan, Guangdong, Haerbin, and Changchun. We also have many patners who provide system level support in various cities.

9.2 JYTEK Software Platform

JYTEK has developed a complete software platform, SeeSharp Platform, for the test and measurement applications. We leverage the open sources communities to provide the software tools. Our platform software is also open sourced and is free, thus lowering the cost of tests for our customers. We are the only domestic vendor to offer complete commercial software and hardware tools.

9.3 JYTEK Warranty and Support Services

With our complete software and hardware products, JYTEK is able to provide technical and sales services to wide range of applications and customers. In most cases, our products are backed by a 1-year warranty. For technical consultation, pre-sale and after-sales support, please contact JYTEK of your country.

10.Statement

The hardware and software products described in this manual are provided by JYTEK China, or JYTEK in short.

This manual provides the product review, quick start, some driver interface explanation for JYTEK JY-6312 Series family of multi-function data acquisition boards. The manual is copyrighted by JYTEK.

No warranty is given as to any implied warranties, express or implied, including any purpose or non-infringement of intellectual property rights, unless such disclaimer is legally invalid. JYTEK is not responsible for any incidental or consequential damages related to performance or use of this manual. The information contained in this manual is subject to change without notice.

While we try to keep this manual up to date, there are factors beyond our control that may affect the accuracy of the manual. Please check the latest manual and product information from our website.

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