

intel RN-1138 Nios II Embedded Design Suite User Guide

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intel RN-1138 Nios II Embedded Design Suite



About this Document

- This document provides information about the following in context to the Intel®
- Quartus® Prime software version:
- Nios® II Embedded Design Suite (EDS)
- Nios II Processor IP
- Embedded IP cores
- This document covers Nios II release information for the Intel Quartus Prime software version 16.1 and later. For any previous release information, refer to the Nios II
- Embedded Design Suite Release Notes (Archived).

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Nios II Embedded Design Suite (EDS)

Release Information for Nios II EDS

Intel Quartus Prime Softwa re Version	Intel Quartus Pri me Variant	Updates
---------------------------------------------	---------------------------------	---------

22.1	Standard Edition	Nios II Toolchain upgrades for Intel Quartus Prime Standard Edition: • Binutils • ex-pat • gcc • gdb • isl • ncurses • newlib
22.3	Pro Edition	Nios II Toolchain upgrades for Intel Quartus Prime Pro Edition: • gcc • newlib
22.2	Pro Edition	Nios II Toolchain upgrades for Intel Quartus Prime Pro Edition: • ex-pat • gdb • ncurses
22.1	Pro Edition	Nios II Toolchain upgrades for Intel Quartus Prime Pro Edition: Binutils gcc gdb
21.3	Pro Edition	Nios II Toolchain upgrades for Intel Quartus Prime Pro Edition: • ex-pat • gcc • newlib
21.2	Pro Edition	Nios II Toolchain upgrades for Intel Quartus Prime Pro Edition: • Binutils • gdb • gmp • mpc

		Nios II Toolchain upgrades for Intel Quartus Prime Standard Edition:				
	Standard Edition	Binutils				
		ex-pat				
		• gcc				
21.1		• gdb				
		• gmp				
		• mpc				
		• mfr				
		• newlib				
continued	continued					

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Intel Quartus Prime Softwa re Version	Intel Quartus Pri me Variant	Updates				
20.4	Pro Edition	 Nios II Toolchain upgrades for Intel Quartus Prime Pro Edition: gcc mfr Commercial version of MicroC/OS-II is under Apache 2.0 Open-Source Licensing, for more information refer to the Micrium Licensing Webpage. 				
20.1.1	Standard Edition	Nios II Toolchain upgrades for Intel Quartus Prime Standard Edition: — gcc — gdb — GMP — mfr — nurses — newlib				

20.1 Pro Edit		Timo i To Editori.
20.1 Pro Edit	o Edition	 Nios II Toolchain upgrades for Intel Quartus Prime Pro Edition: ex-pat The clog library is removed from the toolchain for the Intel Quartus Prime Pro Edition.
20.1 Pro	o Edition	Nios II Toolchain upgrades for Intel Quartus Prime Pro Edition: — ex-pat
20.2 Pro	o and Standard ition	 Nios II Toolchain upgrades for Intel Quartus Prime Pro Edition: binutils ex-pat gcc gdb mfr Nios II Toolchain upgrades for Intel Quartus Prime Standard Edition: binutils ex-pat gcc gdb mfr
	o Edition	 Nios II Toolchain upgrades for Intel Quartus Prime Pro Edition: gcc GMP nurses newlib
20.3 Pro	o Edition	 Nios II Toolchain upgrades for Intel Quartus Prime Pro Edition: binutils gcc gdb

ntel Quartus Prime Softwa e Version

19.2	Pro Edition	 In the Intel Quartus Prime Pro Edition, the Windows* version of Nios II EDS, Cygwin has been removed and replaced with Windows Subsystem for Linux* (WSL). For installation instructions, refer to the Installing Windows Subsystem for Linux (WSL) on Windows sections in the Nios II Software Developer Handbook. Known Issue: warpath: <nios directory="" eds="" ii="" installation="" nios="" or="" project=""> No such file or directory</nios> Nios II Toolchain upgrades for Intel Quartus Prime Pro Edition: — binutils — ex-pat — gcc — gdb — isl — mpc — mfr — nurses — newlib
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		You must manually install Eclipse from the <u>Eclipse download page</u> to get the Nios II development environment running.
		Note: Nios II Eclipse plug-ins are delivered in Intel Quartus Prime with the necessary installer and readme files.
		For installation instructions, refer to the <i>Installing Eclipse IDE into Nios II EDS</i> section in the <i>Nios II Software Developer Handbook</i> .
		Nios II Toolchain upgrades for Intel Quartus Prime Standard Edition:
		— binutils
		— ex-pat
		— gcc
		— gdb
		— GMP
	Pro and Standard Edition	— isl
19.1		— mpc
		— mfr
		— nurses
		— newlib
		The clog library is removed from the toolchain for the Intel Quartus P rime Standard Edition.
		In the Intel Quartus Prime Standard Edition, the Windows version of Nios II EDS, Cygwin has been removed and replaced with Windows Subsystem for Linux (WSL).
		For installation instructions, refer to the <i>Installing Windows Subsystem f</i> or <i>Linux (WSL) on Windows</i> sections in the <i>Nios II Software Developer Handbook</i> .
		Known issue: nios2-elf-gcc.exe: error: CreateProcess: No such file or directory
18.1	Pro and Standard Edition	Nios II Toolchain upgrades for Intel Quartus Prime Pro Edition: — gcc
continued		

18.0	Pro and Standard Edition	 Nios II Toolchain upgrades for Intel Quartus Prime Pro Edition: binutils gcc gdb GMP isl mfr newlib 				
17.1	Pro and Standard Edition	 Nios II Software Build Tools (SBT): Upgrade of Eclipse to v4.5 for Int el Quartus Prime Pro Edition and Intel Quartus Prime Standard Edition New driver for Intel XWAY PHY11G PEF7071 Ethernet PHY for Intel Quartus Prime Pro Edition and Intel Quartus Prime Standard Edition Nios II Software Build Tools (SBT): Windows 10 host support in Intel Quartus Prime Pro Edition and Intel Quartus Prime Standard Edition soft ware Nios II Toolchain upgrades for Intel Quartus Prime Pro Edition: — binutils — ex-pat — gcc — gdb — GMP — mfr — newlib • Bug fix: — The issue causing locale to be broken on new lib 2.4.0 when using s mall lib is fixed. 				
17.0	Pro and Standard Edition	Nios II Software Build Tools (SBT)—Windows 10 support added in I ntel Quartus Prime Pro Edition.				

		Nios II Toolchain upgrades:				
		— gcc				
		— isl				
		— mpc				
		— mfr				
16.1	Pro and Standard Edition	Bug fixes:				
		— The handling of the -mgpopt=option setting has been changed. It is n ow under the full control of the BSP editor and there is a flag for it in the public.mk file.				
		nios2-app-compile no longer fails when -mgpopt is set to "global" and the log level is set to "-1".				
		For more information about the GCC releases, refer to the GCC, the GN U Compiler Collection website.				

Nios II Toolchain Versions

Nios II Toolchain Versions for the Intel Quartus Prime Pro Edition

Intel Qu	Nios II Toolchain Versions										
artus Pr ime Pro Edition	binutil s	clog	ex-pat	gcc	gdb	GMP	isl	mpc	mfr	nurse s	newlib
22.3	2.37.5 0	_	2.4.6	11.3.1	11.2.9 0	6.2.1	0.20	1.2.1	4.1.0	6.3	4.2.0
22.2	2.37.5 0	_	2.4.6	11.2.1	11.2.9 0	6.2.1	0.20	1.2.1	4.1.0	6.3	4.1.0
22.1	2.37.5 0	_	2.4.1	11.2.1	11.1.9 0	6.2.1	0.20	1.2.1	4.1.0	6.2	4.1.0
21.4	2.35.5 0	_	2.4.1	10.3.1	10.1.9 0	6.2.1	0.20	1.2.1	4.1.0	6.2	4.1.0
21.3	2.35.5 0	_	2.4.1	10.3.1	10.1.9 0	6.2.1	0.20	1.2.1	4.1.0	6.2	4.1.0
21.2	2.35.5 0	_	2.2.9	10.2.1	10.1.9 0	6.2.1	0.20	1.2.1	4.1.0	6.2	3.3.0
21.1	2.33.5 0	_	2.2.9	10.2.1	9.2.90	6.2.0	0.20	1.1.0	4.1.0	6.2	3.3.0
20.4	2.33.5 0	_	2.2.9	10.2.1	9.2.90	6.2.0	0.20	1.1.0	4.1.0	6.2	3.3.0
20.3	2.33.5 0	_	2.2.9	10.1.1	9.2.90	6.2.0	0.20	1.1.0	4.0.2	6.2	3.3.0

20.2	2.32.5 1	_	2.2.9	9.3.1	8.3.1	6.2.0	0.20	1.1.0	4.0.2	6.2	3.3.0
20.1	2.32.5 1	_	2.2.9	9.2.1	8.3.1	6.1.2	0.20	1.1.0	4.0.2	6.1	3.1.0
19.4	2.31.5 1	_	2.2.6	8.3.1	8.2.1	6.1.2	0.20	1.1.0	4.0.1	6.1	3.1.0
19.3	2.31.5 1	_	2.2.7	8.3.1	8.2.1	6.1.2	0.20	1.1.0	4.0.1	6.1	3.1.0
19.2	2.31.5 1	0.18.1	2.2.6	8.3.1	8.2.1	6.1.2	0.20	1.1.0	4.0.1	6.1	3.1.0
19.1	2.28.5 1	0.18.1	2.2.4	7.3.1	8.0.1	6.1.2	0.16.1	1.0.3	3.1.6	5.9	2.5.0
18.1	2.28.5 1	0.18.1	2.2.4	7.3.1	8.0.1	6.1.2	0.16.1	1.0.3	3.1.6	5.9	2.5.0
18.0	2.28.5 1	0.18.1	2.2.4	7.2.1	8.0.1	6.1.2	0.16.1	1.0.3	3.1.6	5.9	2.5.0
17.1	2.26.5 1	0.18.1	2.2.0	6.3.0	7.11.1	6.1.1	0.14	1.0.3	3.1.4	5.9	2.4.0
17.0	2.25	0.18.1	2.1.0	5.3	7.10	6.0.0	0.14	1.0.3	3.1.3	5.9	2.2
16.1	2.25	0.18.1	2.1.0	5.3	7.10	6.0.0	0.14	1.0.3	3.1.3	5.9	2.2
16.0	2.25	0.18.1	2.1.0	5.2	7.10	6.0.0	0.12.2	1.0.2	3.1.2	5.9	2.2

Nios II Toolchain Versions for the Intel Quartus Prime Standard Edition

Intel Qu artus Pr ime Sta ndard E dition	Nios II Toolchain Versions										
	binutil s	clog	ex-pat	gcc	gdb	GMP	isl	трс	mfr	nurse s	newlib
22.1	2.37.5 0	_	2.4.8	12.1.1	11.2.9 0	6.2.1	0.25	1.2.1	4.1.0	6.3	4.2.0
21.1	2.35.5 0	_	2.4.1	10.3.1	10.1.9 0	6.2.1	0.20	1.2.1	4.1.0	6.2	4.1.0
20.1.1	2.33.5 0	_	2.2.9	10.1.1	9.2.90	6.2.0	0.20	1.1.0	4.0.2	6.2	3.3.0
20.1	2.32.5 1	_	2.2.9	9.2.1	8.3.1	6.1.2	0.20	1.1.0	4.0.2	6.1	3.1.0
19.1	2.31.5 1	_	2.2.7	8.3.1	8.2.1	6.1.2	0.20	1.1.0	4.0.1	6.1	3.1.0
18.1	2.25	0.18.1	2.1.0	5.3	7.10	6.0.0	0.14	1.0.3	3.1.3	5.9	2.2
continue	continued										

Intel Qu artus Pr ime Sta ndard E dition	Nios II Toolchain Versions										
	binutil s	clog	ex-pat	gcc	gdb	GMP	isl	трс	mfr	nurse s	newlib
18.0	2.25	0.18.1	2.1.0	5.3	7.10	6.0.0	0.14	1.0.3	3.1.3	5.9	2.2
17.1	2.25	0.18.1	2.1.0	5.3	7.10	6.0.0	0.14	1.0.3	3.1.3	5.9	2.2
17.0	2.25	0.18.1	2.1.0	5.3	7.10	6.0.0	0.14	1.0.3	3.1.3	5.9	2.2
16.1	2.25	0.18.1	2.1.0	5.3	7.10	6.0.0	0.14	1.0.3	3.1.3	5.9	2.2
16.0	2.25	0.18.1	2.1.0	5.2	7.10	6.0.0	0.12.2	1.0.2	3.1.2	5.9	2.2

Nios II Processor IP Core

Release Information for Nios II Processor IP Core

Intel Quartus Prime Software Version	Key Updates
20.4	
20.3	
20.2	
20.1	No change.
19.4	No change
19.3	Added support for Intel Agilex™ devices.
19.2	
19.1	No change.
18.1	
18.0	No change.
17.1	Added support for Intel Stratix® 10 and Intel Cyclone® 10 LP devices.
17.0	Added support for the Nios II processor in Intel Quartus Prime Pro Edition, and Pla tform Designer.
16.1	 The Nios II Processor is supported as a pre-release (beta) version in Intel Quartus Prime Pro Edition because of the changes required to support IP components in Platf orm Designer. Nios II Classic is no longer supported in Intel Quartus Prime Pro Edition.

For more information about the Nios II Processor core, refer to the Nios II Processor Reference Guide.

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Embedded IP Cores

Release Information for Embedded IP Cores

Intel Quartus Prime Software Version	Key Updates
22.3	 Added support for new IP core in Intel Quartus Prime: Lightweight UART IP Core. Added new ECC Error Injection features for AXI mode: On-Chip RAM II Intel FPG A IP Core. Added fix on supported devices: Intel FPGA GMII to RGMII Converter Core. Added supported devices: Intel FPGA HPS GMII to TSE 1000BASE-X/SGMII PCS Bridge Core. Enabled configurable flash timeout value: Intel FPGA Serial Flash Controller II Core and Intel FPGA Generic QUAD SPI Controller II Core.
22.2	Added a new ECC option to the On-Chip Memory II (RAM or ROM) component.
22.1	 Added support for a new IP core in Intel Quartus Prime: Cache Coherency Tran slator. Added support for dual AXI ports for On-Chip Memory II RAM/ROM.

	— SDRAM Controller Core
	— Tri-State SDRAM Core
	— Compact Flash Core
	EPCS Serial Flash Controller Core
	— 16207 LCD Controller Core
21.3	Scatter-Gather DMA Controller Core
	Video Sync Generator and Pixel Converter Cores
	Avalon®-ST Test Pattern Generator and Checker Cores
	Avalon-MM DDR Memory Half Rate Bridge Core
	— Modular ADC Core
	— Modular Dual ADC Core
	— Intel FPGA Avalon Mutex Core
	Vectored Interrupt Controller Core
20.4	No change.
20.3	No change.
20.2	Added a new parameter for eSPI to LPC Bridge Core.
20.1	Added support for new IP core in Intel Quartus Prime: Intel FPGA MII to RMII Converter Core.
19.4	No change.
19.3	No change.
19.2	No change.
19.1	Added support for new IP core in Intel Quartus Prime: Intel FPGA HPS EMAC to M ulti-rate PHY GMII Adapter Core.
18.1	Added support for new IP core in Intel Quartus Prime: eSPI to LPC Bridge IP Core.

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Intel Quartus Prime Software Version	Key Updates
18.0	 Added support for new IP core in Intel Quartus Prime: Intel eSPI Slave IP Core. Added a new parameter for Modular Scatter-Gather DMA Core.
	 mSGDMA optimizations for Intel Stratix 10 devices in Intel Quartus Prime Pro Editi on software version 17.1. CMSIS support for embedded IP in Intel Quartus Prime Pro Edition software versio n 17.1.
17.1	 EPCQA device support for EPCQ Controller and Generic QSPI Controller IP in Inte I Quartus Prime Standard Edition software version 17.1. Bug Fix:
	— Intel Avalon FIFO IP —Incorrect back pressure behavior during reset state and dat a loss when FIFO is an almost full issue is fixed.
	 Intel FPGA Triple-Speed Ethernet (TSE) niche driver to support mSGDMA updated Redundant software example simple_socket_server_rgmii removed

- Added new Streaming (Avalon-ST) Freeze Bridges for Partial Reconfiguration (PR) support.
 New improved data performance Serial flash controller II and Generic Quad SPI controller II IP cores.
 Added Avalon-ST Freeze Bridges as PR solution IP.
 All Embedded IP cores now support Intel Cyclone 10 device compilation.
 Bug fixes:
 - I2C Slave to Avalon-MM Master—MM master write data corruption due to overrun of internal I2C slave RX shifting logic issue fixed
 - Intel FPGA Avalon FIFO IP —Incorrect back pressure behavior during reset state and data loss when FIFO is almost full issue fixed
 - EPCQ Controller—Incorrect back pressure behavior during reset state issue fixed
 - · Generic QSPI Controller IP:
 - Modified to enable support for multiple instances in one Platform Designer design.
 - The N25Q016 flash device is now supported.
 - Serial Flash Controller IP—EPCS4 flash device is now supported.
 - The following IP cores (from Intel Quartus Prime Standard Edition) are not present in the Intel Quartus Prime Pro Edition:
 - Intel FPGA Avalon New SDRAM Controller
 - Intel FPGA SDRAM Tristate Controller
 - Intel FPGA Avalon EPCS Flash Controller
 - Intel FPGA Avalon Compact Flash Controller
 - Intel FPGA Avalon Half Rate Bridge
 - Intel FPGA Avalon Pixel Converter
 - Intel FPGA Avalon Video Sync Generator
 - Intel FPGA Avalon LCD 16207
 - Intel FPGA Avalon SGDMA
 - Intel FPGA Avalon DMA
 - Intel FPGA Modular ADC
 - Intel FPGA SM Bus Controller
 - A new IP core named Avalon I2C Master has been added to the Platform Designer (Standard) library.
 - The 16550 UART IP has been enhanced to support a user-defined TX FIFO level t rigger.
 - Freeze controller and bridge IPs have been added to the IP library.

17.0

16.1

- For more information about the respective IP cores, refer to the Embedded Peripherals
- IP User Guide.
- For information about Nios V, refer to the Nios V Processor Intel FPGA IP Release Notes.
- Related Information
- Nios V Processor Intel FPGA IP Release Notes

Embedded Peripherals IP User Guide Archives

- · For the latest and previous versions of this user guide, refer to Embedded Peripherals
- IP User Guide. If an IP or software version is not listed, the user guide for the previous IP or software version applies.
- IP versions are the same as the Intel Quartus Prime Design Suite software versions up to v19.1. From Intel Quartus Prime Design Suite software version 19.2 or later, IP cores have a new IP versioning scheme.

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Document Revision History for the Nios II and Embedded IP Release Notes

Document Version	Changes
2022.10.31	Added information for the Intel Quartus Prime Standard Edition software version 22.1.
2022.09.26	Added information for the Intel Quartus Prime Pro Edition software version 22.3.
2022.06.20	Added information for the Intel Quartus Prime software version 21.1 to 22.2.
2022.04.04	Added information for the Intel Quartus Prime software version 22.1.
2021.10.18	Added information for the Intel Quartus Prime software version 21.3.
2020.12.14	Added information for the Intel Quartus Prime software version 20.4.

2020.10.30	Added information for the Intel Quartus Prime software version 19.3 to 20.3.
2019.07.01	Added information for the Intel Quartus Prime software version 19.2.
2019.04.10	Added information for the Intel Quartus Prime software version 19.1.
2018.09.24	Added information for the Intel Quartus Prime software version 18.1.
2018.05.07	Added information for the Intel Quartus Prime software version 18.0
2017.12.05	Added information for the Intel Quartus Prime software version 17.1.
2017.05.08	Added information for the Intel Quartus Prime software version 17.0.
2016.11.07	Added information for the Intel Quartus Prime software version 16.1.

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Documents / Resources



References

- Eclipse Downloads | The Eclipse Foundation
- intel FPGA Knowledge Base Articles Search
- intel_1. Introduction
- intel 1. Nios II Embedded Design Suite Release Notes
- intel_1. About this Document
- intel 1. Introduction
- intel 1. Nios® V Processor Intel® FPGA IP Release Notes
- intel 1. Introduction
- intel /bin/bash: ./create-this-bsp: No such file or directory
- intel ISO 9001:2015 Registrations
- Micrium Software and Documentation Silicon Labs Silicon Labs

Manuals+.