

infineon TRAVEO T2G Accelerating Zone Controllers Instruction Manual

[Home](#) » [infineon](#) » infineon TRAVEO T2G Accelerating Zone Controllers Instruction Manual 

Contents

- 1 infineon TRAVEO T2G Accelerating Zone Controllers Instruction Manual
- 2 TRAVEO™ T2G: TVII-B-E-1M registers
 - 2.1 Instructions
- 3 Revision history
- 4 Documents / Resources
 - 4.1 References
- 5 Related Posts



infineon TRAVEO T2G Accelerating Zone Controllers Instruction Manual



TRAVEO™ T2G: TVII-B-E-1M registers

Body controller entry technical reference manual

About this document

Scope and purpose

The TRAVEO™ T2G body controller entry registers TRM provides reference information for registers and contains respective programming details.

Intended audience

This manual is for system designer and programmers who are designing or programming using TVII-B-E-1M parts.

TRAVEO™ T2G: TVII-B-E-1M registers

Body controller entry technical reference manual Instructions

Instructions

This document contains instructions for the location of

- TVIIBE1M_RegistersTRM.html
- TVIIBE1M_RegistersTRM_Diff_Rev0F_Rev0G.pdf and
- TVIIBE1M_RegistersTRM.pdf files.

The above mentioned files are located in the 002-19567 source folder. To use TVIIBE2M_RegistersTRM.html file, unzip the 002-19567.zip file and open the .html file in the web browser.

TRAVEO™ T2G: TVII-B-E-1M registers

Body controller entry technical reference manual

Revision history

Revision history

Document version	Date of release	Description of changes
**	2017-07-06	Specification for new silicon.
*A	2018-07-25	<p>Removed those registers which does belong to the product based on the parametrization.</p> <p>Removal of bit fields which does belong to the product based on the parametrization.</p> <p>Removal of the registers which are “Hidden”.</p>
*B	2018-09-12	Addition of CXPI registers.

*C	2019-05-16	<p>Instance table is created for each IP and the Instance number is picked from the parameter section of the XML.</p> <p>Links are created for the REGISTER table with offset from the base. All registers are displayed (same instance of the same IP).</p> <p>All bit-fields per register are rendered based on parameterization Logs captures the following three details:</p> <ul style="list-style-type: none"> a. IP and its respective instances b. IP wise Hidden registers c. IP wise Populated registers Optimization of HTML: <ul style="list-style-type: none"> a. Stable "File Header" which was earlier moving along with the contents b. Color codes for the navigation pane c. Links to the REGISTER definition from the register table.
----	------------	---

*D	2019-09-17	<ol style="list-style-type: none"> 1. All the instances of all the IPs are supported now. 2. Look and feel of the UI has been changed to make it more user friendly: <ol style="list-style-type: none"> a. Distributed into 3 panes: <ol style="list-style-type: none"> i. Sections ii. Registers iii. Content b. Default page with right fragment opens up with the references, notes, and registers structure with respective links on the Contents pane. c. Registers pane provide global search operation after feeding first 3 characters when ONLY at home. d. On the Sections pane, user must select the MMIO > IP > Instance etc. e. Registers pane provides Search options for the selected instance. f. Once a particular instance is chosen, the Registers pane provides a list of registers. User can select one of these registers and observe the register structure on the Contents pane. 3. Instance table, register name and offsets have been removed, as they are no longer needed. 4. All hidden registers and sections are not visible (similar to earlier versions). 5. There is no PDF associated with HTML as earlier. Known Issues: <ol style="list-style-type: none"> 1. There is no way to print any content as of now. 2. There could be a slight misalignment on the content pane based on the screen size.
----	------------	--

Document version	Date of release	Description of changes
*E	2020-03-04	<p>1. Extensive search options using address or register or bit fields.</p> <p>Search option is supported for “Registers”, register “Addresses” and register “Bit Fields” using prefixes “reg:” or “add:” or “bit:” respectively.</p> <p>2. Buttons “Home”, and “Up” included.</p> <p>3. Register Structure with links, all homepage notes are moved down.</p> <p>4. Some fixes which were present in the previous revision in spec system. Missing contents due to parser errors (CDT#343334).</p> <p>5. Print options for the selected registers.</p> <p>6. HTML is supported only on Chrome, Mozilla Firefox, Opera, UC Browser and Microsoft Edge browsers.</p> <p>(No support for any other OS/Mobiles etc.)</p> <p>7. Issue of content overlap while re-sizing after “restore down” option is fixed. Known Issues:</p> <p>1. Whole HTML print option not available.</p>

*F	2020-08-07	<ol style="list-style-type: none"> 1. Based on Revd. of Silicon, no major changes. 2. Addition of many Arm Core system registers and description corrections of some registers. 3. Removed some Slash and effuse registers. 4. SW Access note enhanced. 5. Added HW access note for registers behavior.
*G	2021-09-29	<ol style="list-style-type: none"> 1. Changes in line with PCN#213702 2. Refer to Registers TRM difference log with respect to previous revision for details 3. Addition of PASS/ADC registers for Temperature sensor control and trimming 4. Slash TOC2 updates for flash boot 5. Minor description updates to some registers 6. Addition of effuse CUSTOMER_DATA registers 7. Addition of marker for security enhancement

Trademarks

All referenced product or service names and trademarks are the property of their respective owners.


Edition 2021-09-29 Published by Infineon Technologies AG 81726 Munich, Germany © 2021 Infineon Technologies AG. All Rights Reserved. Do you have a question about any aspect of this document? Go to www.cypress.com/support Document reference 002-19567 Rev. *G

IMPORTANT NOTICE

The information given in this document shall in no event be regarded as a guarantee of conditions or characteristics ("Beschaffenheitsgarantie"). With respect to any examples, hints or any typical values stated herein and/or any information regarding the application of the product, Infineon Technologies hereby disclaims any and all warranties and liabilities of any kind, including without limitation warranties of non-infringement of intellectual property rights of any third party. In addition, any information given in this document is subject to customer's compliance with its obligations stated in this document and any applicable legal requirements, norms and standards concerning customer's products and any use of the product of Infineon Technologies in customer's applications. The data contained in this document is exclusively intended for technically trained staff. It is the

responsibility of customer's technical departments to evaluate the suitability of the product for the intended application and the completeness of the product information given in this document with respect to such application. For further information on technology, delivery terms and conditions and prices, please contact the nearest Infineon Technologies Office (www.infineon.com). **WARNINGS** Due to technical requirements products may contain dangerous substances. For information on the types in question please contact your nearest Infineon Technologies office. Except as otherwise explicitly approved by Infineon Technologies in a written document signed by authorized representatives of Infineon Technologies, Infineon Technologies' products may not be used in any applications where a failure of the product or any consequences of the use thereof can reasonably be expected to result in personal injury.

Documents / Resources

	infineon TRAVEO T2G Accelerating Zone Controllers [pdf] Instruction Manual TRAVEO T2G, Accelerating Zone Controllers, TRAVEO T2G Accelerating Zone Controllers, Zone Controllers, Controllers
---	--

References

- [Support: Live Chat Online, Tech Support & FAQ - Infineon Technologies](#)
- [Semiconductor & System Solutions - Infineon Technologies](#)