

FTDI FT4232HP USB Devices with Type-C Power User Guide

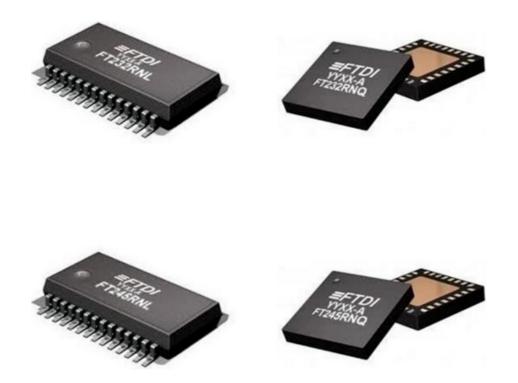
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FTDI FT4232HP USB Devices with Type-C Power



Introduction

The FT4232HP/FT232HP are high-speed USB devices with Type-C power delivery features. Within the power delivery functionality, there are multiple configurable options available, and this document describes all the configurable options. The configuration options covered in this document only cover the power delivery configuration. For USB configurations, please refer to AN 124 User Guide For FTDI FT_Prog Utility.

Overview

This document describes each configurable option and the configurable values of each parameter in the EEPROM of the FT4232HP/FT232HP. The EEPROM is external, and it is needed only if the design needs a custom configuration. If the default configuration is sufficient then there is no need to have an external EEPROM. For default values, please refer to the sections below.

Glossary of Terms

SI. No.	Term	Description
1	Sink / Consumer	When the device is consuming power from the host port, the device is said to be in "Sink" mode, or the device is said to be a "consumer".
2	Source / Provider	When the device is supplying power to the host, then the device is operating in "Source" mode. The device can change the role from Sink to Source if the device is self-powered and power role swap is enabled in the configuration.
3	Power Role Swap	The process of changing the role is called role swap. The device can switch the role from Sink to Source if the device is self-powered.

Configuration Parameters

256 bytes in the configuration EEPROM are reserved for configuration options. Table 1 gives the information for all the configurable options.

Parameter	Description	Default value	Configurable values
Sink Request Power Role Swa	Sink will initiate a PR SWAP reque st only if this option is set. Default settings do not support PR SWAP. However, if the device is se If-powered, then PR SWAP can be supported by modifying the configurations.	0 – Disabled.	0 – Disabled. 1 – Enabled.
Sink Accept PR Swap	Option to accept PR SWAP when FT4232HP /FT2232HP/FT232HP is a sink. If this option is not set, the PR_SW AP request from a source will be re jected	0 – Reject.	0 – Reject. 1 – Accept.

Source Request PR SWAP	When the device is a Source, this option is used to decide whether to swap back to sink when it sees a port2 disconnect event.	0 – Disabled.	0 – Disabled. 1 – Enabled.
Source Accept PR SWAP	When the device is a source, a PR _SWAP request from the sink can be accepted or rejected based on this option.	0 – Reject.	0 - Reject. 1 - Accept.
UConn Swap	Option to enable UConn swap sup port.	0 – Reject	0 - Reject 1 - Accept
External MCU	This is to switch over to external M CU mode.	0 – Internal MC U.	0 – Internal MCU. 1 – External MCU.
PD Auto Clock	Auto clock enable/disable. The aut o clock feature is explained in secti on 2.4.	0 – Disabled.	0 - Disabled. 1 - Enabled.
Use EFUSE	This option indicates whether to us e trim values from EFUSE or not. Keep this enabled always. The configurable option is provided for characterization purposes only.	1 – Use EFUS E.	0 – Do not use EFUSE TRIM. 1 – Use EFUSE TRIM
GPIO Count	PD Source uses GPIOs to control the power supplies. This option indicates how many GPIOs are used for power supply control.	0	0-3
			The following options are available as a drop down list in FT_PROG
GPIO Number1	GPIO number for power supply control. GPIO can also be used as an ISET in case of sink-only configuration.	'NA'	Values 0 – 3, A Text 'NA'. Select the appropriate GPIO value for this

Parameter	Description	Default value	Configurable values
			option. And if this field is unused,
			then select 'NA'.
			The following options are available as a drop down list in FT_PROG
			Values 0 – 3,
			A Text 'NA'.
GPIO Number2	GPIO number for power supply control. GPIO can also be used as an ISET in case of sink-only configuration.	'NA'	Select the appropriate GPIO value for this opt ion. If this field is unus ed,
			then select 'NA'.
			The following options are available as a drop down list in FT_PROG
			Values 0 – 3,
			A Text 'NA'.
GPIO Number3	GPIO number for power supply control. GPIO can also be used as an ISET in case of sink-only configuration.	'NA'	Select the appropriate GPIO value for this opt ion. And if this field is unused, then select 'NA'.

			The following options are available as a drop down list in FT_PROG .
			Values 0 – 3,
	GPIO number for power supply co		A Text 'NA'.
GPIO Number4	ntrol. GPIO can also be used as an ISET in case of sink-only configuration.	'NA'	Select the appropriate GPIO value for this opt ion. And if this field is unused, then select 'N A'.
			The following options are available as a drop down list in FT_PROG .
			Values 0 – 3,
			A Text 'NA'.
PD Load Enable	GPIO number configured with load enable switch for PD	2	Select the appropriate GPIO value for this opt ion. And if this field is unused, then select 'NA'.

Paramete	eter	Description	Default value	Configurable values
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			(Should not be same a s any of the GPIO Nu mber1 to Number4 val ue). The following opti ons are available as a dropdown list in FT_P ROG.
			Values 0 – 3, A Text 'NA'.
Discharge Pin	GPIO to enable the discharge circuit	'NA'	Select the appropriate GPIO value for this opt ion. And if this field is unused,
			then select 'NA'.
Discharge Enable BM	GPIO bitmap to enable the dischar ge circuit	1 – drive high	The following options are available in the dro pdown list of FT_PRO G. Drive Low Drive Hi Inp ut Mode. Don't Care.
			The following options are available in the dro pdown list of FT_PRO G.
Discharge Disable BM	GPIO bitmap to disable the dischar ge circuit	0 – drive low	Drive Low Drive Hi Inp ut Mode. Don't Care.

			(Should not be same a s any of the GPIO Nu mber1 to Number7 val ue). The following opti ons are available as a dropdown list in FT_P ROG.
			Values 0 – 3, A Text 'NA'.
CC Indication	Option to indicate CC Orientation t hrough a GPIO	Not Selected in Default Configu ration	Select the appropriate GPIO value for this opt ion. And if this field is unused, then select 'NA'.
ISET1	GPIO Number. The state of the GP IO is used to indicate which sink pr ofile is used. ISET1 is to indicate the case non-PD TYPEC_1.5A attachment.	1	Any GPIO number bas ed on the board desig n can be used. Any G PIO value 0 – 3 can be used as long as these pins

Parameter	Description	Default value	Configurable values
			are not used for any ot her purpose. If the PC B design supports PD Source, then do not us e the pins reserved for the sourc e as ISET.
ISET2	GPIO Number. The state of the GP IO is used to indicate which sink pr ofile is used. ISET2 is to indicate the case non-PD TYPEC_3A attachment.	3	Any GPIO number bas ed on the board desig n can be used. Any G PIO value 0 – 3 can be used as long as these pins are not used for a ny other purpose. If the PCB design supports PD Source, then do not use the pin s reserved for source as ISET.

ISET3	GPIO Number. The state of the GP IO is used to indicate which sink pr ofile is used. ISET3 is to indicate PD profile 1 (5 V3A).	3	Any GPIO number bas ed on the board design can be used. Any GPIO value 0 – 3 can be used as long as these pins are not used for any other purpose. If the PCB design supports PD Source, then do not use the pins reserved for the source as ISET.
ISET_ENABLED	bit to enable/disable the ISET feat ure/	1	0 – Disable the ISET f eature. All the above ISET fiel ds will be ignored. 1 – ISET Enabled.
	There are two cases for 5V. They are typically the same however, in case there is a difference, this opti on is for PDO_1.		
	5V after PD contract		
	negotiation.		- LODIO :: 4:
	vSafe5V during PR_SWAP be fore negotiating high		For each GPIO, the following options are available as a dropdown list in FT_PROG.
	power.		Drive High Drive Low. I
GPIO_BM_PDO1(vSafe5v)	In case the GPIO mapping for thes e two is not the same, then please use the mapping for	'Don't Care'	nput Mode. Don't Care.
	case 2 using the vSafe5V		

Parameter	Description	Default value	Configurable values
	option (another option available).		

GPIO_BM_PDO2	GPIO Bitmap for PDO2	'Don't Care'	Drive High Drive Low I nput Mode. Don't Care.
GPIO_BM_PDO3	GPIO Bitmap for PDO3	'Don't Care'	Drive High Drive Low. I nput Mode. Don't Care.
GPIO_BM_PDO4	GPIO Bitmap for PDO4	'Don't Care'	Drive High Drive Low. I nput Mode. Don't Care.
vSet0V	Settings to set 0V (Turn off all supp lies)	'Don't Care'	Drive High Drive Low Don't Care.
vSafe5V	vSafe5V – used in case vSafe5V i s not same as the 5V at PDO1. (ca n be different power – as vSafe5V is before negotiation)	'Don't Care'	Drive High Drive Low Don't Care.
Sink PDO1	Voltage and current profile for PD O1. Typically, PDO1 is vSafe5.	Voltage in 1mv Unit – 5000 (5v). Current in 1ma Unit – 3000 (3A)	Voltage - 5000 (5V) Current - (0-5000) (0-5A)
Sink PDO2	Voltage and current profile for PD O2.	0	O Means this profile is not used. The user is allowed to configure the profile to any valid voltage / curr ent value without conflict. A valid profile is a unique profile (The same voltage profile as another PDO is not allowed—Also the profiles should be in the descending order of voltage).
Sink PDO3	Voltage and current profile for PD O3.	0	Same as above.

Sink PDO4	Voltage and current profile for PD O4.	0	Same as above.
Sink PDO5	Voltage and current profile for PD O5.	0	Same as above.
Sink PDO6	Voltage and current profile for PD O6.	0	Same as above.
Sink PDO7	Voltage and current profile for PD O7.	0	Same as above.

Parameter	Description	Default value	Configurable values
Source PDO1	Voltage and current profile for PD O1. Typically, PDO1 is vSafe5. Default Setting does not have sour ce capability.	Voltage in 1mv Unit – 5000 (5v). And in 50mV S teps. Current in 1ma Unit – 300 (3A)	Voltage – 5000 (5V) Current – (0-5000) (0- 5A)
Source PDO2	Default Setting does not have sour ce capability.	Voltage in 50m v Unit – 0. Current in 10m a Unit – 0	
Source PDO3	Default Setting does not have sour ce capability.	Voltage in 50m v Unit – 0. Current in 10m a Unit – 0	
Source PDO4	Default Setting does not have sour ce capability.	Voltage in 50m v Unit – 0. Current in 10m a Unit – 0	
Sender Response Timer	tSenderResponse timer in the PD Spec Rev3.0.	27ms (all the ti mer values are in milli- seconds)	Any Value within Spec

Hard Reset Timer	tPSHardReset timer in the PD Spe c Rev3.0.	30ms	Any Value within Spec
Source Capability Timer	tTypeCSendSourceCap timer in the PD Spec Rev3.0.	190ms	Any Value within Spec
Discover Identity Timer	tDiscoverIdentity timer in the PD S pec Rev3.0.	45ms	Any Value within Spec
Source Recover Timer	tSrcRecover timer in the PD Spec Rev3.0.	750ms	Any Value within Spec
Transition Timer	tPSTransition timer in the PD Spec Rev3.0.	500ms	Any Value within Spec
Source off timer	tPSSourceOff timer in the PD Spec Rev3.0.	890ms	Any Value within Spec .
No Response Timer	tNoResponse timer in the PD Spec Rev3.0.	4000ms	Any Value within Spec
Sink Wait Capability Timer	tTypeCSinkWaitCap timer in the P D Spec Rev3.0.	465ms	Any Value within Spec .
Sink Activity Timer	tSinkActivity timer in the PD Spec. However, this timer is not used as i t is not required in type-c 1.2+.	135ms	Any Value within Spec
Sink Request Timer	tSinkRequest timer in the PD Spec .	110ms	Any Value within Spec
Discharge Timer	An internal timer is used to keep the discharge circuit on. During PR_SWAP and also during the Source Voltage transition, the discharge circuit	100ms	Any value is based on the discharge circuit d esign.

Parameter	Description	Default value	Configurable values
	will be turned on for the		
	duration mentioned in this timer.		
Chunk not supported timer	tChunkingNotSupported timer in the PD Spec Rev 3.0.	40ms	Any Value within Spec
I2C Address	Used for external MCU.	32 (0x20)	Any Valid address.
	Do not use this in production.		
TRIM1	Set to 0.	0	
TRIM2	Do not use this in Production. Set to 0.	0	
External DC	This option indicates the device is self-powered and has a fixed Exter nal power supply. FT4232HP/FT2232HP/FT232H P does not support power role swa p in its default settings as the role swap feature needs a power suppl y. So, if the device is externally powered, then power role swap can be supported. Use this option to in dicate an externally powered device.	UNCHECK	CHECKBOX

Power Role Swap Options

There are four different combinations for power role swap. These are the four configurable options available.

• Sink Request Power Role (PR) Swap

When this option is set, the sink initiates a power role swap request if the device is self-powered. The "External DC" option indicates if the device is self-powered.

• Sink Accept PR Swap

In case the device receives a PR_SWAP request from the source, the sink can either reject it or accept it based on this option. This option should be set only if the device is externally powered through a DC power supply.

• Source Request PR SWAP

This option does not apply to single-port devices.

• Source Accept PR SWAP

Similar to the above case, the device (source) can go back to the sink if the present sink requests a PR_SWAP. Whether to accept the request or not is based on this option.

vConn Swap

When this option is set, the device will do a vConn Swap every time it changes the power role.

External MCU

- By default, the Internal MCU will be active. If the design is using an external MCU instead of an FT4232HP internal MCU, the customer needs to indicate this through a bit in the configuration. Select the "External MCU" Option to enable external MCU. Once this bit is set, the internal MCU won't be active.
- If the design does not have an EEPROM and still needs an External MCU, then both GPIO_0 and GPIO_1 of FT4232HP can be pulled high to achieve this. This has the same behavior as setting the External MCU bit.

PD Auto Clock

To aid in power saving, the clock can be turned off to the PD device when there is no activity. With the auto clock option enabled, the clock will turn on whenever there is any activity to the PD device and will turn off after the activity.

Use EFUSE

The PD device has an internal EFUSE block, and its size is 64 bits. This EFUSE is programmed during the IC characterization time, and it is one-time programmable. The value programmed in this block is used by the software to program bandgap voltage, pull-up current, pull-down resistance, etc. The "Use EFUSE" option is enabled by default. The software uses the EFUSE value only if this option is enabled. For debugging purposes this option can be disabled but not recommended to disable it for production.

GPIO Count

- By default, FT4232HP/FT2232HP/FT232HP does not support power role swap and source operation.
 However, if the device is self-powered then it is possible to do a role swap. When the device is a source, it uses
 GPIOs to manage the power supplies. There can be multiple GPIOs to control multiple power sources (5V, 9V, 15V etc.). The total number of GPIOs used to control the power supplies should be defined in this field.
- FT4232HP/FT2232HP/FT232HP has a total of 4 GPIO Pins. So, the valid value for this field is 0 to 3. When the FT4232HP/FT2232HP/FT232HP is configured as a sink-only device, set this field to 0. Since the number of GPIOs available is only 4, the maximum profiles that can be supported are only 4.

GPIO Number1 - 4

This field represents the GPIO pins used for controlling the power supply. In case there are three GPIO pins used, then GPIO Number 1 to GPIO Number 3 will indicate those three pins.

PD Load Enable

• GPIO Pin is used to enable the PD load.

Discharge Pin

• This indicates the GPIO Pin used for enabling and disabling the discharge circuit.

Discharge Enable BM

A 2-bit bitmap indicates how to enable the discharge circuit.

This can be one of the following values. FT_PROG gives these options as a dropdown list and hence the user does not need to know the bitmap.

- Drive High
- Drive Low
- Input Mode.
- Don't Care (This means, this pin is not used).

Discharge Disable BM

A 2-bit bitmap indicates how to disable the discharge circuit.

This can be one of the following values. FT_PROG gives these options as a dropdown list and hence the user does not need to know the bitmap.

- Drive High
- Drive Low
- · Input Mode.
- Don't Care (This means, this pin is not used).

ISET

- There are three ISET options by default. ISET1, ISET2 and ISET3.
- These pins are used to indicate the power profile being used when the device is a sink. Even though there are 3 ISET options by default, the ISET feature for legacy cable has been disabled. That is, TYPE-C 1.5A (ISET1) and TYPE-C 3A (ISET2) feature has been disabled and hence only ISET3 (PD MODE) can be used.
- These are the GPIO pins.
- ISET1 Type-C 1.5A Attached. (No PD) The feature has been disabled currently.
- ISET2 Type-C 3A Attached 5V3A. (No PD) The feature has been disabled currently.
- ISET3 PD. 5V, 3A profile.
- In case more than three ISETs are needed, it is possible to configure more than three if the device is used in a sink-only configuration. In sink-only configuration, GPIOs reserved for source power supply control are not needed hence those pins can be used as ISETs.
- To do this, set the option "EXTEND_ISET" (described in the below sections).

ISET_ENABLED

All the ISET-related fields are valid only if this field is enabled. Instead of changing multiple ISET fields, this single enable/disable option helps to enable/disable the ISET feature.

GPIO_BM_PDO1 (vSafe5v)

- GPIO Bitmap to select Source PDO1. The bitmap indicates the combination of the GPIO state used to get the selected profile.
- For example, for PD01, GPIO0 Drive Hi, GPIO1 Drive Hi, GPIO2 Don't Care (NA).
- These options are available as a table in FT_PROG and the states of the GPIO as a dropdown list.

GPIO BM PDO2

- GPIO Bitmap to select Source PDO2. The bitmap indicates the combination of the GPIO state used to get the selected profile.
- For example, GPIO0- Drive Hi, GPIO1 Drive Hi, GPIO2 Don't Care (NA).
- These options are available as a table in FT PROG and the states of the GPIO as a dropdown list.

GPIO_BM_PDO3

- GPIO Bitmap to select Source PDO3. The bitmap indicates the combination of the GPIO state used to get the selected profile.
- For example, GPIO0 Drive Hi, GPIO1 Drive Hi, GPIO2 Don't Care (NA).
- These options are available as a table in FT_PROG and the states of the GPIO as a dropdown list.

GPIO_BM_PDO4

- GPIO Bitmap to select Source PDO4. The bitmap indicates the combination of the GPIO state used to get the selected profile.
- For example, GPIO0 Drive Hi, GPIO1 Drive Hi, GPIO2 Don't Care (NA).
- These options are available as a table in FT_PROG and the states of the GPIO as a dropdown list.

vSet0V

- GPIO Bitmap to set 0V. The bitmap indicates the combination of the GPIO state used to get the selected profile.
- For example, GPIO0 Drive Low, GPIO1 Drive Low, GPIO2 Don't Care (NA).
- These options are available as a table in FT_PROG and the states of the GPIO as a dropdown list.

vSafe5V

- GPIO Bitmap to select vSafe5. This can be the same as PDO1 too. The bitmap indicates the combination of the GPIO state used to get the selected profile.
- For example, GPIO0 Drive Hi, GPIO1 Drive Hi, GPIO2 Don't Care (NA).
- These options are available as a table in FT_PROG and the states of the GPIO as a dropdown list.

Sink PDO [1:7]

- Option to select Voltage and Current Profile for Sink PDO1.
- Corresponding to every PDO option, there is a voltage dropdown box and a current dropdown box in FT_PROG. Please select the voltage and current from this list for the PDO.
- The lowest voltage profile should be PDO1 and the second lowest should be PDO2 and so on. The PDO profile should be in ascending order to the voltage.

Source PDO [1:4]

Option to select Voltage and Current Profile for Source PDO1.

- Corresponding to every PDO option, there is a voltage dropdown box and a current dropdown box in FT PROG. Please select the voltage and current from this list for the PDO.
- The lowest voltage profile should be PDO1 and the second lowest should be PDO2 and so on. Basically, the PDO profile should be in ascending order to the voltage.

Sender Response Timer

This option is to configure the tSenderResponse timer. Please refer to the PD spec for tSenderResponse timer details. Timer units are in milliseconds.

Hard Reset Timer

This option is to configure the tPSHardReset timer. Please refer to the PD spec for tPSHardReset timer details. Timer units are in milliseconds.

Source Capability Timer

This option is to configure the tTypeCSendSourceCap timer. Please refer to PD Spec for more details on the tTypeCSendSourceCap timer. Timer units are in milliseconds.

Discover Identity Timer

This option is to configure the tDiscoverIdentity timer. Please refer to the PD Spec for more details on the dDiscoverIdentity timer. Timer units are in milliseconds.

Source Recover Timer

This option is to configure the tSrcRecover timer. Please refer to the PD Spec for more details on the tSrcRecover timer. Timer units are in milliseconds.

Transition Timer

This option is to configure the transition timer. Please refer to the PD Spec for more details on the transition timer. Timer units are in milliseconds.

Source off timer

This option is to configure the tPSSourceOff timer. Please refer to the PD Spec for more details on the tPSSourceOff timer. Timer units are in milliseconds.

No Response Timer

This option is to configure the tNoResponse timer. Please refer to the PD Spec for more details on tNoResponse. Timer units are in milliseconds.

Sink Wait Capability Timer

This option is to configure the tTypeCSinkWaitCap timer. Please refer to the PD Spec for more details on the tTypeCSinkWaitCap timer. Timer units are in milliseconds.

Sink Request Timer

This option is to configure the tSinkRequest timer. Please refer to the PD Spec for more details on tSinkRequest timer. Timer units are in milliseconds.

Discharge Timer

The internal timer is used to keep the discharge circuit enabled. The discharge circuit will be enabled and kept enabled for the timer duration. Timer units are in milliseconds.

Chunk Not Supported Timer

This option is to configure the tChunkingNotSupported timer. Please refer to the PD Spec for more details on tChunkingNotSupported timer. Timer units are in milliseconds.

I2C Address

This is used in the case of an external MCU. The I2C address will default to 0x20 if this is not specified.

TRIM1

For Debug purposes only – Usually, the TRIM values are taken from EFUSE. However, EFUSE can be overridden using this field.

TRIM2

For Debug purposes only – Usually, the TRIM values are taken from EFUSE. However, EFUSE can be overridden using this field.

External DC

If the device is self-powered, then this option can be set to initiate a power role swap request to switch over the role to the source. The Sink Request power role swap option also should be set along with this to achieve this.

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Appendix A – References

Document References

- https://usb.org/sites/default/files/USB%20Power%20Delivery_1.zipFT_PROG
- AN 124 User Guide For FTDI FT Prog Utility
- USB High-Speed Series ICs

Acronyms and Abbreviations

Terms	Description
ВМ	Bit Map
BOS	Binary Object Store
GPIO	General Purpose Input Output
PD	Power Delivery
PDO	Power Delivery Object
PR SWAP	Power Role Swap.
USB	Universal Serial Bus
USB-IF	USB Implementers Forum

Appendix B - List of Tables and Figures

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Appendix C - Revision History

• Document Title: AN_551 FT4232HP/FT2232HP/FT232HP Configuration Guide

• Document Reference No.: FT_001493

• Clearance No.: FTDI#562

• Product Page: USB High-Speed Series ICs

• Document Feedback: Send Feedback

Revision	Changes	Date
1.0	Initial Release	06-05-2021
1.1	Minor editorial changes for the new release version	28-11-2023

Documents / Resources



FTDI FT4232HP USB Devices with Type-C Power [pdf] User Guide

FT4232HP USB Devices with Type-C Power, FT4232HP, USB Devices with Type-C Power, Devices with Type-C Power, Type-C Power

References

- Mean Home FTDI
- Mome FTDI
- Products FTDI
- IC FTDI
- **(C FTDI**
- Utilities FTDI
- User Manual

Manuals+, Privacy Policy