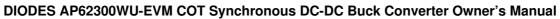
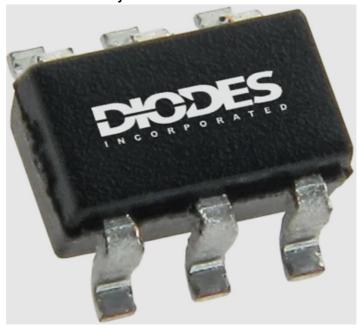


DIODES AP62300WU-EVM COT Synchronous DC-DC Buck Converter Owner's Manual

Home » DIODES » DIODES AP62300WU-EVM COT Synchronous DC-DC Buck Converter Owner's Manual





Contents

- 1 DESCRIPTION
- **2 FEATURES**
- **3 APPLICATIONS**
- **4 ABSOLUTE MAXIMUM RATINGS**
- **5 RECOMMENDED OPERATING**

CONDITIONS

- **6 SETTING OUTPUT VOLTAGE**
- **7 EVALUATION BOARD**
- **8 IMPORTANT NOTICE**
- 9 Documents / Resources
 - 9.1 References
- 10 Related Posts

DESCRIPTION

The AP62300 is a 3A, synchronous buck converter with a wide input voltage range of 4.2V to 18V. The device fully integrates a $75m\Omega$ high-side power MOSFET and a $45m\Omega$ low-side power MOSFET to provide high-efficiency step-down DC-DC conversion.

The AP62300 device is easily used by minimizing the external component count due to its adoption of Constant On-Time (COT) control to achieve fast transient response, easy loop stabilization, and low output voltage ripple.

The AP62300 design is optimized for Electromagnetic Interference (EMI) reduction. The device has a proprietary gate driver scheme to resist switching node ringing without sacrificing MOSFET turn-on and turn-off times, which reduces high frequency radiated EMI noise caused by MOSFET switching.

AP62300 is available in a TSOT26 package.

FEATURES

• VIN Range: 4.2V -18V

• Output Voltage range: 0.8V to 7V

- 3A Continuous Output Current
- $0.8V \pm 1\%$ Reference Voltage (TA = +25°C) => AP62300
- 155µA Low Quiescent Current
- 750kHz Switching Frequency
- Up to 83% Efficiency at 5mA Light Load
- · Proprietary Gate Driver Design for Best EMI Reduction
- · Protection Circuitry
 - Undervoltage Lockout (UVLO)
 - Cycle-by-Cycle Valley Current Limit
 - Thermal Shutdown
- Totally Lead-Free & Fully RoHS Compliant
- Halogen and Antimony Free. "Green" Device

APPLICATIONS

- Flat Screen TV Sets and Monitors
- Consumer Electronics
- Network Systems
- General Purpose Point of Load

ABSOLUTE MAXIMUM RATINGS

Symbol	Parameter	Rating	Unit		
VIN	Cumply Din Voltage	-0.3 to +20.0 (DC)	V		
	Supply Pin Voltage	-0.3 to 22.0 (400ms)			
VSW	Switch Din Voltage	-1.0 to VIN + 0.3 (DC)	v		
VOVV	Switch Pin Voltage	-2.5 to VIN + 2.0 (20ns)	v		
VBST	Bootstrap Pin Voltage	$V_{SW} - 0.3$ to $V_{SW} + 6.0$	V		
VEN	Enable/UVLO Pin Voltage	-0.3 to +6.0	V		
VFB	Feedback Pin Voltage	-0.3 to +6.0	V		
TST	Storage Temperature	-65 to +150	°C		
T _J	Junction Temperature	+150	°C		
TL	Lead Temperature	+260	°C		
ESD Susceptibilit	ty	·			
НВМ	Human Body Mode	2000	V		
CDM	Charge Device Model	500	V		

RECOMMENDED OPERATING CONDITIONS

Symbol	Parameter	Rating	Unit
VIN	Supply Voltage	4.2 to 18	V
VOUT	Output Voltage Range	0.8 to 7	V
T _A	Operating Ambient Temperatur e	-40 to +85	°C
TJ	Operating Junction Temperatur e	-40 to +125	°C

SETTING OUTPUT VOLTAGE

Table 1 for AP62300 shows a list of recommended component selections for common output voltages.

Table 1. Common Output Voltages

VOUT	C1	C2, C3	R1	R2	L1	C6
1.2V	10μF	2 x 22μF	4.99ΚΩ	10ΚΩ	1.5μΗ	100nF
1.5V	10μF	2 x 22μF	8.66ΚΩ	10ΚΩ	1.5μΗ	100nF
1.8V	10μF	2 x 22μF	12.4ΚΩ	10ΚΩ	2.2μΗ	100nF
2.5V	10μF	2 x 22μF	21.5ΚΩ	10ΚΩ	2.2μΗ	100nF – 220nF
3.3V	10μF	2 x 22μF	31.6ΚΩ	10ΚΩ	3.3μΗ	100nF – 330nF
5.0V	10μF	2 x 22μF	52.3ΚΩ	10ΚΩ	3.3μΗ	100nF – 330nF

EVALUATION BOARD

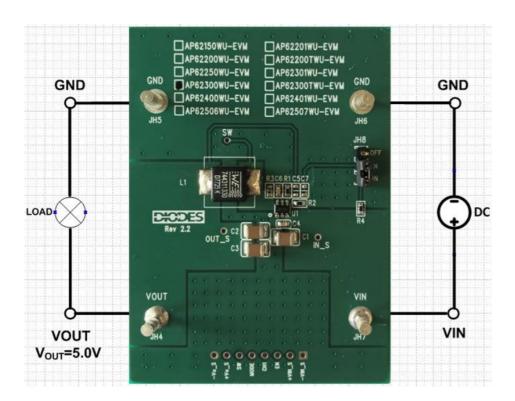


Figure 1. AP62300WU-EVM

QUICK START GUIDE

The AP62300WU-EVM has a simple layout and allows access to the appropriate signals through test points. To evaluate the performance of the AP62300WU, follow the procedure below:

- 1. Connect a power supply to the input terminals VIN and GND. Set VIN to 12V.
- 2. Connect the positive terminal of the electronic load to VOUT and negative terminal to GND.
- 3. For Enable, place a jumper at JH8 to "ON" position to connect EN pin to VIN through $100K\Omega$ resistor to enable IC or leave it OPEN. Jump to "OFF" position to disable IC.
- 4. The evaluation board should now power up with a 5.0V output voltage.
- 5. Check for the proper output voltage of 5.0V (±1%) at the output terminals VOUT and GND. Measurement can also be done with a multimeter with the positive and negative leads between VOUT and GND.
- 6. Set the load to 3A through the electronic load. Check for the stable operation of the SW signal on the oscilloscope. Measure the switching frequency

MEASUREMENT/PERFORMANCE GUIDELINES:

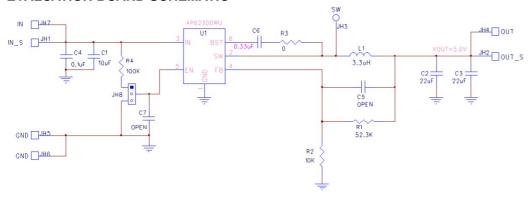
- 1. When measuring the output voltage ripple, maintain the shortest possible ground lengths on the oscilloscope probe. Long ground leads can erroneously inject high frequency noise into the measured ripple.
- 2. For efficiency measurements, connect an ammeter in series with the input supply to measure the input current.

 Connect an electronic load to the output for output current.

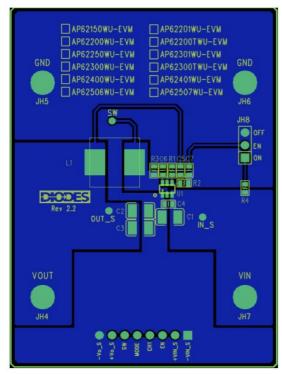
BOOTSTRAP CAPACITOR GUIDELINES:

To ensure proper operation, a ceramic capacitor must be connected between the BST and SW pins to supply the drive voltage for the high-side power MOSFET. A 100nF ceramic capacitor is sufficient for most applications. In the cases where output voltage is higher than 2.5V, a higher capacitance is recommended to help maintain stable voltage from BST to SW. Please refer to Tables 1 for details.

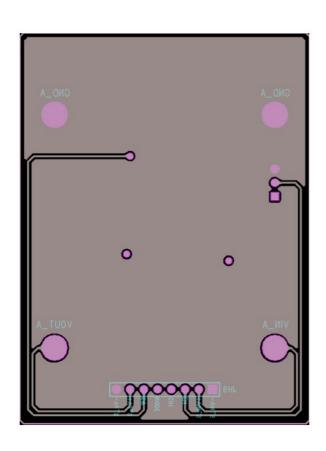
EVALUATION BOARD SCHEMATIC



PCB TOP LAYOUT



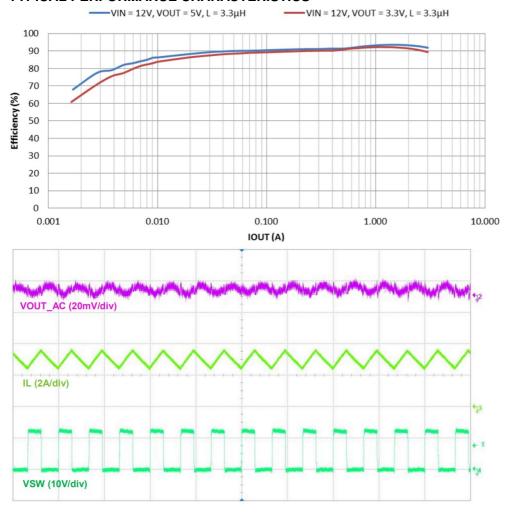
PCB BOTTOM LAYOUT



BILL OF MATERIALS for AP62300WU-EVM for VOUT=5V

Ref	Value	Description	Qty	Size	Vendor Name	Manufacturer PN
C1	10μF	Ceramic Capacitor,2 5V, X7R, 10%	1	1210	KEMET	C1210C106K3RACTU
C2, C3	22μF	Ceramic Capacitor, 2 5V, X7R, 10%	2	1210	KEMET	C1210C226K3RAC780 0
C4	0.1μF	Ceramic Capacitor,5 0V, X7R, 10%	1	0603	KEMET	C0603C104K5RACTU
C6	0.33μF	Ceramic Capacitor, 1 6V, X7R, 10%	1	0603	Samsung	CL10B334KO8NNNC
L1	3.3μΗ	DCR=10.5mΩ, Ir=7.5 A	1	10x10x 5m m	Wurth Electroni	7447714033
R1	52.3ΚΩ	SMD Resistor, 1%	1	0603	Panasonic	ERJ-3EKF5232V
R2	10ΚΩ	SMD Resistor, 1%	1	0603	Panasonic	ERJ-3EKF1002V
R3	0Ω	SMD Resistor, 1%	1	0603	Panasonic	ERJ-3GEY0R00V
R4	100ΚΩ	SMD Resistor, 1%	1	0603	Panasonic	ERJ-3EKF1003V
JH4, JH 5, JH6,J H7	1598	Terminal TurretTriple 0.094" L (Test Points)	4	Through- Hole	Keystone Circ uit	1598-2
JH8		PCB Header, 40POS	1	1X3	3M	2340-6111TG
U1	AP62300	Sync BuckDC-DC co nverter	1	TSOT26	Diodes Incorpo rated	AP62300WU-7

TYPICAL PERFORMANCE CHARACTERISTICS



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Documents / Resources



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AP62300WU-EVM COT Synchronous DC-DC Buck Converter, AP62300WU-EVM, COT Synchronous DC-DC Buck Converter, Synchronous DC-DC Buck Converter, DC-DC Buck Converter, DC-DC Buck Converter, Converter

References

- Diodes Incorporated Analog, Discrete, Logic, Mixed-Signal
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