

Danfoss PLUS+1 Compliant EMD Speed Analog Direction User Guide

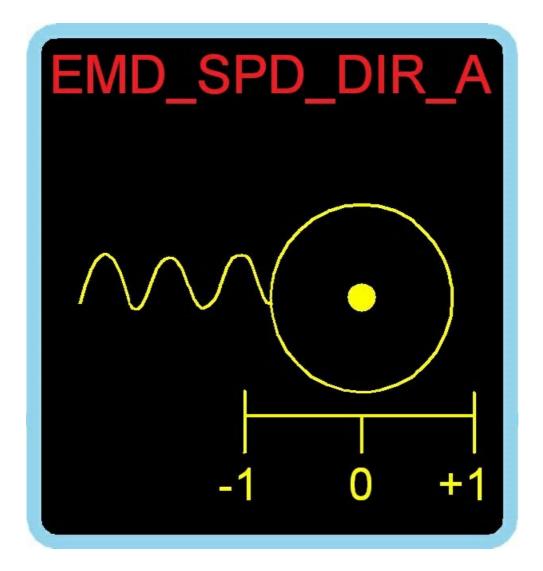
Home » Danfoss » Danfoss PLUS+1 Compliant EMD Speed Analog Direction User Guide 1

Contents

- 1 Danfoss PLUS+1 Compliant EMD Speed Analog Direction
- **2 Product Usage Instructions**
- 3 FAQ
- 4 Overview
- **5 Controller Input Requirements for EMD Function Blocks**
- **6 About Function Block Connections**
- **7 Function Block Parameters**
- **8 Controller Configurations**
- 9 Products we offer
- **10 CONTACT**
- 11 Documents / Resources
 - 11.1 References



Danfoss PLUS+1 Compliant EMD Speed Analog Direction



Product Usage Instructions

- MC Controller Configurations:
- Configure an MFIn for the Speed Input.
- Configure an AnIn for the Direction Input.
- This function block outputs rpm and directional signals based on inputs from an EMD Speed Sensor.
- Spd: Input for the measured period output by the Speed Sensor.
- Dir In: Input for the voltage signal related to the Speed Sensor's direction.

FAQ

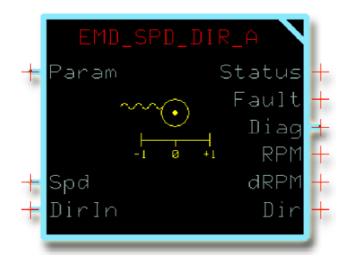
- Q: How do I troubleshoot if the rpm output is incorrect?
 - **A:** Check the input connections and ensure the Speed Sensor is functioning correctly. Verify the configuration settings on the controller.
- Q: What is the purpose of the Dir In input?
 - A: The Dir In input provides information about the direction of the speed measured by the sensor.

Revision history

Table of revisions

Date	Changed	Rev
December 2014		AA

EMD_SPD_DIR_A Function Block



Overview

This function block outputs rpm and directional signals based on inputs from an EMD Speed Sensor. On MC controllers, this function block receives its:

- Spd input through an MFIn input.
- Dirln input through either a second MFIn input, a DigAn input, or an AnIn input.

On SC controllers, this function block receives its:

- Spd input through an MFIn input.
- Dirln input through either a second MFIn input or through a DigAn input.

See

• About Function Block Connections on page 8 for an overview of this function block's connections and signals.

Controller Input Requirements for EMD Function Blocks

 The following tables list the controller input requirements for the EMD SPD DIR, EMD SPD DIR A, and EMD SPD DIR D function blocks.

Input Connections—MC Controllers

Function Block	Function Bloc k Input	Controller In put	Comment	
	Spd	MFIn	Determines speed via pulse signal from the sensor.	
EMD SPD DIR	Dirln	MFIn	Utilizes pull-up/pull-down resistors and voltage to de tect open circuit failure of the direction signal.	
	Spd	MFIn	Determines speed via pulse signal from the sensor.	
EMD SPD DIR A	Dirln	DigAn	Only detects when direction signal voltage is outside the expected ranges but lacks pull-up/pull-d own resistors for open circuit detection.	
		Anin	Only detects when direction signal voltage is outside the expected ranges but lacks pull-up/pull-d own resistors for open circuit detection.	
	Spd	MFIn	Determines speed via pulse signal from the sensor.	
EMD SPD DIR D	DigDir	Digln	Provides no fault detection for the direction signal.	
	Digoli	DigAn	Provides no fault detection for the direction signal.	

Input Connections—SC Controllers

Function Block	Function Bloc Controller In put Comment		Comment	
	Spd	MFIn	Determines speed via pulse signal from the sensor. The controller input must be labeled Dig/Ana/Freq .	
EMD SPD DIR	Dirln	MFIn	Utilizes pull-up/pull-down resistors and voltage to de tect open circuit failure of the direction signal.	
		DigAn	Utilizes pull-up/pull-down resistors and voltage to de tect open circuit failure of the direction signal.	

EMD_SPD_DIR_A Function Block Inputs

Function Block Inputs

Item	Туре	Range	Description
Param	Bus		Input for common parameters that can be applied to multiple function blocks. See <i>About the Param Input</i> on page 11 for more information.
Spd	Bus		 Volt/Voltage, Per (Period), and Count signals with the voltage, period, and count output by the Speed Sensor. A Config sub-bus with signals that configure the controller input that r eceives these signals.
Per	U32	1,250 to 10,000,00 0	The measured period output by the Speed Sensor . The function block uses the Per signal, Count signal, and Puls/Rev param eter value to calculate its RPM output. $10,000 = 1,000 \ \mu s.$
Count	U16	0 to 65,53 5	The measured count per program loop output by the Speed Sensor . The function block uses the Per signal, Count signal, and Puls/Rev param eter value to calculate its RPM output. 1,000 = 1,000.
Config	Sub-bu s		Contains the signals that configure this input.
Dir In	Bus		 A Voltage/Volt signal with the voltage output by the Speed Sensor, w hich the block uses to determine direction. A Config sub-bus with signals that configure the controller input that r eceives this signal.
Volt/Volta ge	U16	0 to 5,250	The measured voltage of the direction signals that the Speed Sensor outputs, which the block uses to determine direction.
Config	Sub-bu s		Contains the signals that configure this input.

EMD_SPD_DIR_A Function Block Outputs

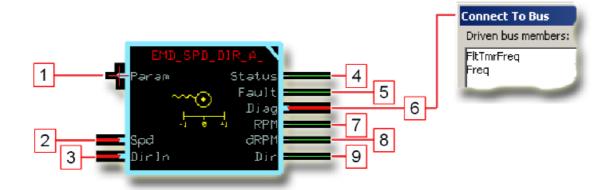
Function Block Outputs

pe Range Description	Type Range	tem
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Status	U16		Reports the function block's status. This function block uses a <u>non-standard</u> bitwise scheme to report its status and faults. • 0x0000 = Block is OK. • 0x0008 = Puls/Rev or DirLockHz parameter value is out of range.
Fault	U16		Reports the function block's faults. This function block uses a non-standard bitwise scheme to report its status and faults. • 0x0000 = Block is OK. • 0x0001 = Per signal in the function block's Spd input is too low. • 0x0002 = Volt/Voltage signal in the function block's Spd input is out of range. • 0x0004 = signal in the function block's Dir input is out of range.
Diag	Bus		Outputs a bus with Freq and FltTmrFreq signals that are available for troub leshooting.
Freq	U32	0 to 1,000, 000,000	The measured frequency of the Speed Sensor. 100,000 = 10,000 Hz.
FaultTmr Freq	U16	0 to 65,53 5	 When a frequency fault: Occurs, this output counts up the milliseconds until the function block makes a fault declaration. Clears, the output counts down the milliseconds until the function clears the fault declaration. 1,000 = 1,000 ms.
RPM	U16	0 to 2,500	Speed sensor revolutions per minute. The function block clamps this output at 2,500. 1 = 1 rpm.
dRPM	U16	0 to 25,00	Speed sensor revolutions per minute x 10 (deciRPM). The function block cla mps this output at 25,000.
Dir	S8	-1, 0, +1	The Speed Sensor's direction of rotation. -1 = Counterclockwise (CCW). -0 = Neutral. -+1 = Clockwise (CW).

EMD_SPD_DIR_A Function Block

About Function Block Connections



About Function Block Connections

Ite m	Description
1.	Input for common parameters that can be applied to multiple function blocks.
2.	 Input for a bus with: The voltage, period, and count signals output by the EMD Speed Sensor. A sub-bus with signals that configure the controller input that receives these signals.
3.	 Input for a bus with: The directional signal output by the EMD Speed Sensor. A sub-bus with the signals that configure the controller input that receives these signals.
4.	Reports the status of the function block.
5.	Reports the faults of the function block.
6.	Outputs a bus with Freq and FltTmrFreg signals that are available for troubleshooting.
7.	Speed sensor revolutions per minute.
8.	Speed sensor revolutions per minute x 10 (deciRPM).
9.	The Speed Sensor's direction of rotation. • -1 = Counterclockwise (CCW). • 0 = Neutral. • +1 = Clockwise (CW).

EMD_SPD_DIR_A Function Block Status and Fault Logic

• Unlike most other PLUS+1 compliant function blocks, this function block uses non-standard status and fault codes.

Status Logic

Status	Hex*	Bin ary	Cause	Response	Correction
A parameter is o ut of range.	0x0008	100	Puls/Rev, FaultDetTm , or DirLockHz parame ter is out of range.	The function block clam ps the out-of-range value at either its upper or lower limit.	Get the out-of-range pa rameter back within its r ange.

• Bit 16 set to 1 identifies a standard Danfoss status or fault code.

Fault Logic

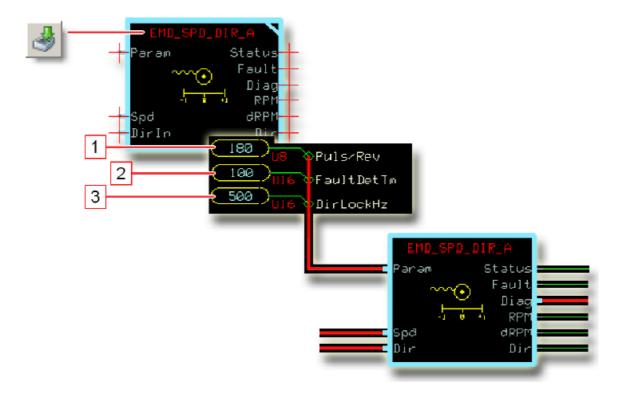
Fault	Hex*	Binary	Cause	Response	Delay [†]	Latch [‡]	Correction
Per signal in the function block's Spd input is too low.	0x0001	0001	Per signal < 1,250 Hz.	The function block outputs its maximum RPM and dR PM values.	Y	N	Check for hardware iss ues, such as electrical noise, that can produce an invalid per signal value.
Volt/Voltage sig nal in the functio n block's Spd in put is out of ran ge.	0x0002	0010	Volt/Voltage signal is betw een 1,000 an d 2,500 mV and the block receives no p ulses from th e Speed Sen sor.	The function block sets its RPM and dR PM outputs t o 0.	Y	N	Check for hardware iss ues, such as electrical noise, that can produce an invalid Volt / Voltage signal value.

- Bit 16 set to 1 identifies a standard Danfoss status or fault code.
- † A delayed fault is reported if the detected fault condition persists for a specified delay time. A delayed fault cannot be cleared until the fault condition remains undetected for the delay time.
- ‡ The function block maintains a latched fault report until the latch releases.

EMD_SPD_DIR_A Function Block

Function Block Parameters

• Enter the top-level page of the EMD_SPD_DIR function block to view and change this function block's parameters.



Function Block Parameters

Item	Typ e	Range	Description
1. Puls/Rev	U8	20–120, 180	Number of pulses per revolution of the Speed Sensor. Refer to the <i>EMD Speed Sensor Technical Information</i> (Danfoss part L1017287) for the correct value.
2. FaultDetT m	U16	0–65,535	 Sets the time between when the function block detects a: Fault condition and then make a fault declaration. Cleared fault condition and then cleared the fault declaration. 1,000 = 1,000 ms.
3. DirLoc kHz	U16	0-8,000	Sets the frequency above which the function block's Dir output locks. Above this frequency, the function block does not report changes in direction. 1,000 = 1,000 Hz.

EMD_SPD_DIR_A Function Block About the Param Input

• Use the Param input to input external parameter values to this function block.

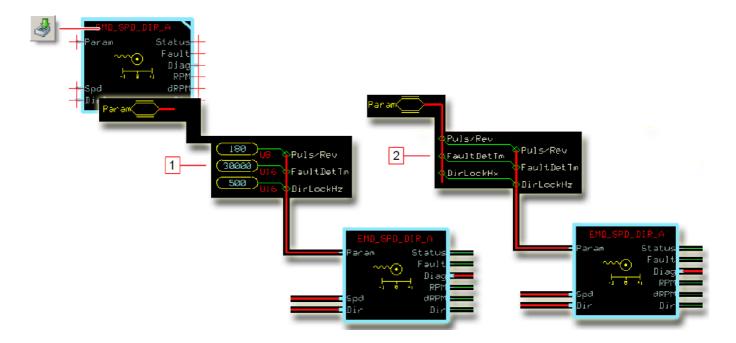


Figure Details

Ite m	Description
1.	Inside the function block's top-level page before you modify this page to accept common parameters thro ugh its Param input.
2.	Inside the function block's top-level page, you modify this page to accept common parameters through its Param input.

Controller Configurations

Inputs on MC and SC controllers require configuration to work with this function block. See:

- MC Controller Configurations
- SC Controller Configurations

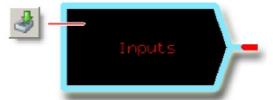
MC Controller Configurations

Input Configurations

Function Block Input	Compatible Inpu t Type	Configuration Action
Spd	MFIn	PinConfig0 route.PinConfig1 route.
	MFIn	No configuration is required.
	DigAn	No configuration is required.
Dir In	Anin	Delete the: • PinConfig route.

How to Configure an MFIn for the Spd Input

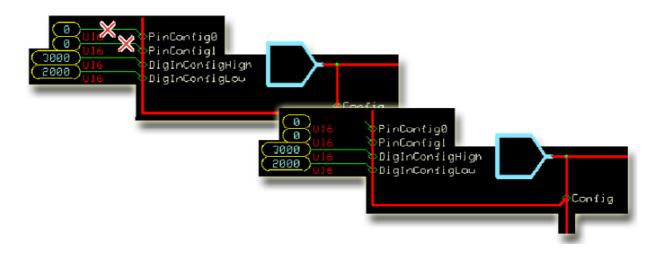
1. In the GUIDE template, enter the Inputs page.



2. Enter the MFIn that receives the input signal.



3. Make the changes that are shown in the following figure.



How to Configure an AnIn for the Dirln Input

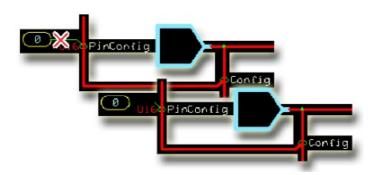
1. In the GUIDE template, enter the Inputs page.



2. Enter the AnIn that receives the input signal.



3. Make the changes that are shown in the following figure.



SC Controller Configurations

Input Configurations

Function Block Input	Compatible Input Type	Configuration Action
Spd	MFIn*	Delete the:
		Bias route.
		Range route.
		Input Mode route.†
Dirln	MFIn	Delete the:
		Bias route.
		Range route.
	DigAn	Delete the:
		Bias route.
		Range route.

- * The MFIn that you use must be labeled Dig/Ana/Freq.
- † If present.

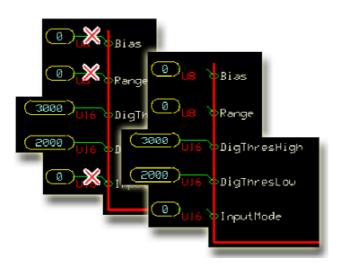
1. In the GUIDE template, enter the Inputs page.



2. Enter the MFIn that receives the input signal.



3. Make the changes that are shown in the following figure.



How to Configure a DigAn for the Dirln Input

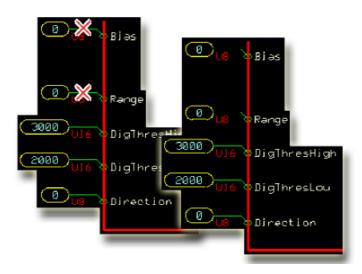
1. In the GUIDE template, enter the Inputs page.



2. Enter the DigAn that receives the input signal.



3. Make the changes that are shown in the following figure.



How to Configure a MFIn for the Dirln Input

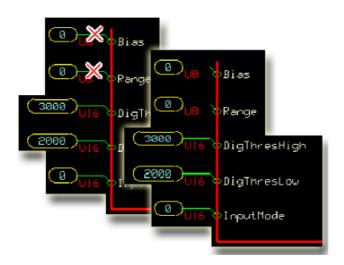
1. In the GUIDE template, enter the Inputs page.



2. Enter the MFIn that receives the input signal.



3. Make the changes that are shown in the following figure.



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