

Danfoss Compliant EMD Speed Direction Function Block User Manual

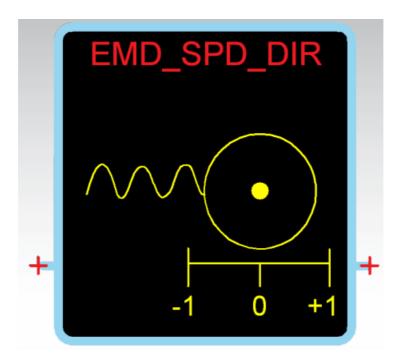
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Danfoss Compliant EMD Speed Direction Function Block



Specifications

• Product Name: PLUS+1 Compliant EMD Speed Sensor Direction Function Block

· Output: RPM and directional signals

• Input Range:

Speed (Spd): 1,250 to 10,000,000Direction (Dir In): 0 to 5,250 volts

Product Usage Instructions

Controller Configurations

The EMD_SPD_DIR Function Block outputs rpm and directional signals based on inputs from an EMD Speed Sensor. It can be used on both MC and SC controllers.

Controller Input Requirements

The controller input requirements for the EMD SPD DIR function block are as follows:

- MC Controllers:
 - Spd MFIn DirIn
- SC Controllers:
 - ∘ Spd MFIn DirIn DigAn

Function Block Inputs

The EMD_SPD_DIR Function Block inputs are as follows:

• Spd (Speed): Bus Per U32 Count U16 – Range:

1,250 to 10,000,000

• Dir In (Direction): Bus Volt/Voltage U16 -

Range: 0 to 5,250 volts

Function Block Outputs

The EMD_SPD_DIR Function Block outputs are as follows:

• Status: U16 - Range: 0 to 65,535

• Fault: U16 – Range: 0 to 1,000,000,000

RPM: U16 – Range: 0 to 25,000
dRPM: U16 – Range: 0 to 2,500

• Dir: S8 - Values: -1, 0, +1

FAQ

What is the purpose of the EMD_SPD_DIR Function Block?
 The EMD_SPD_DIR Function Block outputs rpm and directional signals based on inputs from an EMD Speed Sensor.

- What are the input requirements for the EMD_SPD_DIR function block on MC Controllers?

 The input requirements for MC Controllers are Spd, MFIn, and Dirln.
- What is the voltage range for the Direction input (Dir In) of the EMD_SPD_DIR Function Block?

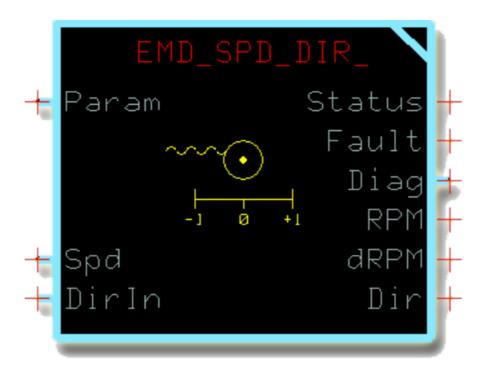
 The voltage range for the Direction input is from 0 to 5,250 volts.

Revision history

Table of revisions

Date	Changed	Rev
December 2014		AA

EMD_SPD_DIR Function Block



This function block outputs rpm and directional signals based on inputs from an EMD Speed Sensor. On both MC and SC controllers, this function block receives its:

- Spd input through an MFIn input.
- Dirln input through either a second MFIn input or a DigAn input.

Controller Input Requirements for EMD Function Blocks

The following tables list the controller input requirements for the EMD SPD DIR, EMD SPD DIR A, and EMD SPD DIR D function blocks.

Input Connections—MC Controllers

Function Block	Function Bloc Controller In put		Comment	
	Spd	MFIn	Determines speed via pulse signal from the sensor.	
EMD SPD DIR	Dirln	MFIn	Utilizes pull-up/pull-down resistors and voltage to de tect open circuit failure of the direction signal.	
	Spd	MFIn	Determines speed via pulse signal from the sensor.	
EMD SPD DIR A	Dirln	DigAn	Only detects when direction signal voltage is outside the expected ranges but lacks pull-up/pull-d own resistors for open circuit detection.	
	Dillii	Anin	Only detects when direction signal voltage is outside the expected ranges but lacks pull-up/pull-d own resistors for open circuit detection.	
	Spd	MFIn	Determines speed via pulse signal from the sensor.	
EMD SPD DIR D	DigDir	DigIn	Provides no fault detection for the direction signal.	
		DigAn	Provides no fault detection for the direction signal.	

Input Connections—SC Controllers

Function Block Function Block k Input		Controller In put	Comment	
	Spd	MFIn	Determines speed via pulse signal from the sensor. The controller input must be labeled Dig/Ana/Freq .	
EMD SPD DIR	Dirln	MFIn	Utilizes pull-up/pull-down resistors and voltage to de tect open circuit failure of the direction signal.	
		DigAn	Utilizes pull-up/pull-down resistors and voltage to de tect open circuit failure of the direction signal.	

Function Block Inputs

Item	Туре	Range	Description
Param	Bus		Input for common parameters that can be applied to multiple function blocks. See <i>About the Param Input</i> on page 11 for more information.
Spd	Bus		 Volt/Voltage, Per (Period), and Count signals with the voltage, period, a nd count output by the Speed Sensor. A Config sub-bus with signals that configure the controller input that rece ives these signals.
		1,250 to	The measured period output by the Speed Sensor .
Per	U32	10,000,00	The function block uses the Per signal, Count signal, and Puls/Rev param eter value to calculate its RPM output. 10,000 = 1,000 μs.
Count	U16	0 to 65,53 5	The measured count per program loop output by the Speed Sensor . The function block uses the Per signal, Count signal, and Puls/Rev param eter value to calculate its RPM output. 1,000 = 1,000.
Config	Sub-bu s		Contains the signals that configure this input.
Dir In	Bus		 Input for a bus with: A Voltage/Volt signal with the voltage output by the Speed Sensor, which the block uses to determine direction. A Config sub-bus with signals that configure the controller input that receives this signal.
Volt/Volta ge	U16	0 to 5,250	The measured voltage of the direction signal that the Speed Sensor output s, which the block uses to determine direction.
Config	Sub-bu s		Contains the signals that configure this input.

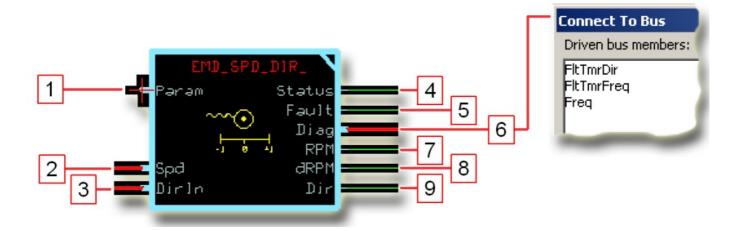
Outputs

Function Block Outputs

Item	Туре	Range	Description
Status	U16		Reports the function block's status. This function block uses a <u>non-standard</u> bitwise scheme to report its status and faults. • 0x0000 = Block is OK. • 0x0008 = Puls/Rev or DirLockHz parameter value is out of range.

Fault	U16		Reports the function block's faults. This function block uses a <u>non-standard</u> bitwise scheme to report its status and faults. • 0x0000 = Block is OK. • 0x0001 = Per signal in the function block's Spd input is too low. • 0x0002 = Volt/Voltage signal in the function block's Spd input is out of range. • 0x0004 = Volt/Voltage signal in the function block's Dir input is out of range.
Diag	Bus		Outputs a bus with Freq , FltTmrDir , and FltTmrFreq signals that are availa ble for troubleshooting.
Freq	U32	0 to 1,000, 000,000	The measured frequency of the Speed Sensor. 100,000 = 10,000 Hz.
FaultTmr Freq	U16	0 to 65,53	 When a frequency fault: Occurs, this output counts up the milliseconds until the function block ma kes a fault declaration. Clears, the output counts down the milliseconds until the function clears t he fault declaration. 1,000 = 1,000 ms.
FltTmrDir	U16	0 to 65,53 5	 When a direction fault: Occurs, this output counts up the milliseconds until the function block ma kes a fault declaration. Clears, this output counts down the milliseconds until the function clears t he fault declaration. 1,000 = 1,000 ms.
RPM	U16	0 to 2,500	Speed sensor revolutions per minute. The function block clamps this output at 2,500. 1 = 1 rpm.
dRPM	U16	0 to 25,00	Speed sensor revolutions per minute x 10 (deciRPM). The function block cla mps this output at 25,000.
Dir	S8	-1, 0, +1	The Speed Sensor's direction of rotation. • -1 = Counterclockwise (CCW). • 0 = Neutral. • +1 = Clockwise (CW).

About Function Block Connections



About Function Block Connections

Ite m	Description
1.	Input for common parameters that can be applied to multiple function blocks.
	Input for a bus with:
2.	The voltage, period, and count signals output by the EMD Speed Sensor.
	A sub-bus with signals that configure the controller input that receives these signals.
	Input for a bus with:
3.	The directional signal output by the EMD Speed Sensor.
	A sub-bus with the signals that configure the controller input that receives these signals.
4.	Reports the status of the function block.
5.	Reports the faults of the function block.
6.	Outputs a bus with Freq, FltTmrDir, and FltTmrFreq signals that are available for troubleshooting.
7.	Speed sensor revolutions per minute.
8.	Speed sensor revolutions per minute x 10 (deciRPM).
	The Speed Sensor's direction of rotation.
	• -1 = Counterclockwise (CCW).
9.	• 0 = Neutral.
	• +1 = Clockwise (CW).

Status and Fault Logic

Unlike most other PLUS+1 compliant function blocks, this function block uses non-standard status and fault codes.

Status Logic

Status	Hex*	Bin ary	Cause	Response	Correction
A parameter is o ut of range.	0x0008	100	Puls/Rev, FaultDetTm , or DirLockHz parame ter is out of range.	The function block clam ps the out-of-range value at either its upper or lower limit.	Get the out-of-range pa rameter back within its r ange.

^{*} Bit 16 set to 1 identifies a standard Danfoss status or fault code.

Fault Logic

Fault	Hex*	Binary	Cause	Response	Delay†	Latch‡	Correction
Per signal in the function block's Spd input is too low.	0x0001	0001	Per signal < 1,250 Hz.	The function block outputs its maximum RPM and dR PM values.	Y	N	Check for hardware iss ues, such as electrical noise, that can produce an invalid Per signal value.
Volt/Voltage sig nal in the functio n block's Spd in put is out of ran ge.	0x0002	0010	Volt/Voltage signal is betw een 1,000 an d 2,500 mV and the block receives no p ulses from th e Speed Sen sor.	The function block sets its RPM and dR PM outputs t o 0.	Y	N	Check for hardware iss ues, such as electrical noise, that can produce an invalid Volt / Voltage signal value.
Volt/Voltage sig nal in the functio n block's Dir inp ut is out of rang e.	0x0004	0100	Volt/Voltage signal is betw een 1,000 an d 2,500 mV.	The function block sets its Dir output to 0.	Υ	N	Check for hardware iss ues, such as electrical noise, that can produce an invalid Volt / Voltage signal value.

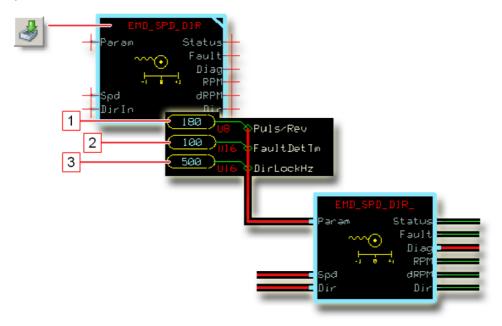
^{*} Bit 16 set to 1 identifies a standard Danfoss status or fault code.

Function Block Parameters

[†] A delayed fault is reported if the detected fault condition persists for a specified delay time. A delayed fault cannot be cleared until the fault condition remains undetected for the delay time.

[‡] The function block maintains a latched fault report until the latch releases.

Enter the top-level page of the **EMD_SPD_DIR** function block to view and change this function block's parameters.



Function Block Parameters

Item	Typ e	Range	Description
1. Puls/Rev	U8	20–120, 180	Number of pulses per revolution of the Speed Sensor. Refer to the <i>EMD Speed Sensor Technical Information</i> (Danfoss part L1017287) for the correct value.
2. FaultDetT m	U16	0–65,535	 Sets the time between when the function block detects a: Fault condition and then makes a fault declaration. Cleared fault condition and then clears the fault declaration. 1,000 = 1,00 0 ms.
3. DirLoc kHz	1 - 11 - 13 1116 0-8 000		Sets the frequency above which the function block's Dir output locks. Above this frequency, the function block does not report changes in direction. 1,000 = 1,000 Hz.

About the Param Input

Use the Param input to input external parameter values to this function block.

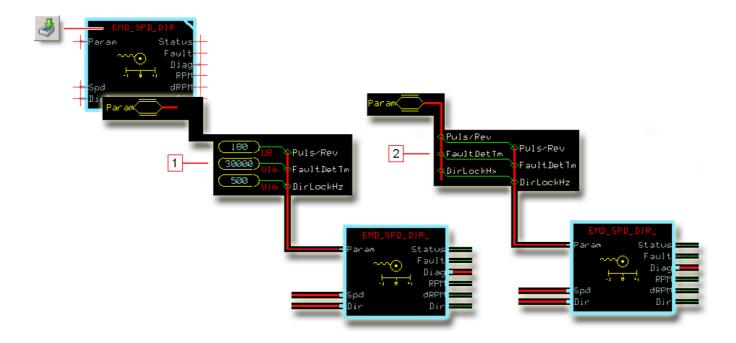


Figure Details

Ite m	Description
1.	Inside the function block's top-level page before you modify this page to accept common parameters thro ugh its Param input.
2.	Inside the function block's top level page after you modify this page to accept common parameters throug h its Param input.

Controller Configurations

Inputs on MC and SC controllers require configuration to work with this function block. See:

- MC Controller Configurations on page 12.
- SC Controller Configurations on page 16.

MC Controller Configurations

Input Configurations

Function Block Input	Compatible Inpu t Type	Configuration Action
Spd	MFIn	PinConfig0 route.PinConfig1 route.
Dirln	MFIn	PinConfig0 route.PinConfig1 route.
	DigAn	PinConfig0 route.PinConfig1 route.

Controller Configurations

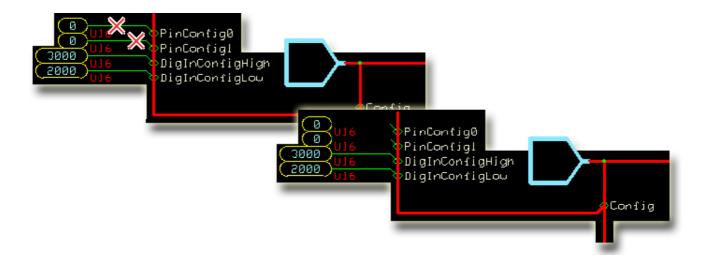
How to Configure an MFIn for the Spd Input

1. In the GUIDE template, enter the Inputs page.



2. Enter the MFIn that receives the input signal.





Controller Configurations

How to Configure a MFIn for the Dirln Input

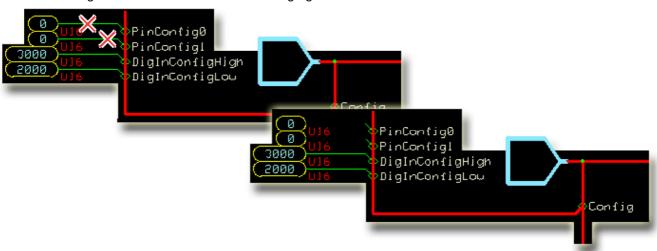
1. In the GUIDE template, enter the Inputs page.



2. Enter the MFIn that receives the input signal.



3. Make the changes that are shown in the following figure.



How to Configure a DigAn for the Dirln Input

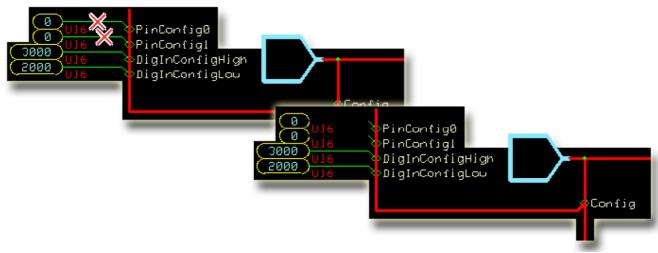
1. In the GUIDE template, enter the Inputs page.



2. Enter the DigAn page that receives the input signal.



3. Make the changes that are shown in the following figure.



SC Controller Configurations

Input Configurations

Function Block Input	Compatible Input Type	Configuration Action
Spd	MFIn*	Delete the: • Bias route. • Range route. • Input Mode route. †
Dirln	MFIn	Delete the: • Bias route. • Range route. • Input Mode route.†
	DigAn	Delete the: • Bias route. • Range route.

^{*} The MFIn that you use must be labeled Dig/Ana/Freq.

How to Configure an MFIn for the Spd Input

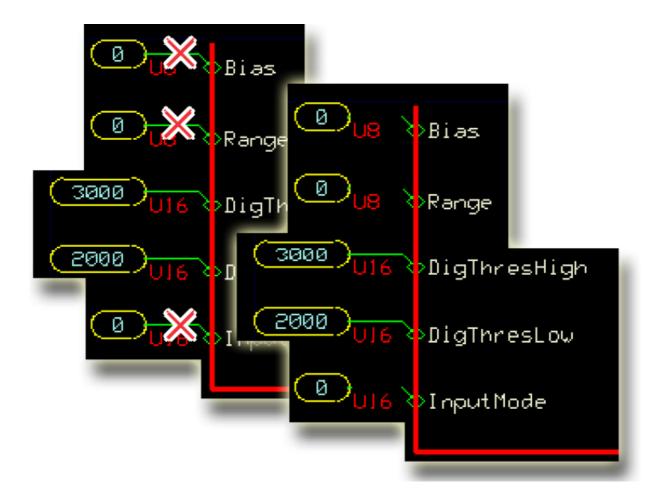
1. In the GUIDE template, enter the Inputs page.



2. Enter the MFIn that receives the input signal.



[†] If present.

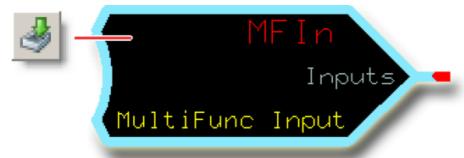


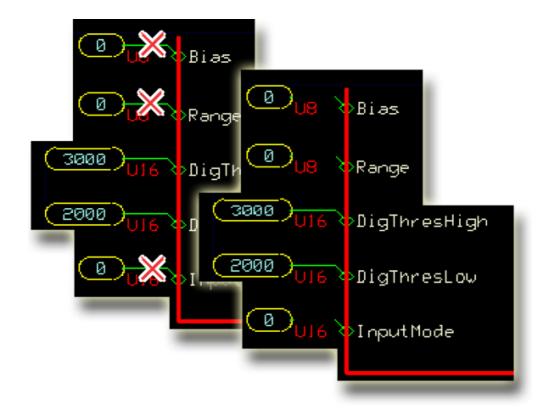
How to Configure an MFIn for the Dirln Input

1. In the GUIDE template, enter the Inputs page.



2. Enter the MFIn that receives the input signal.





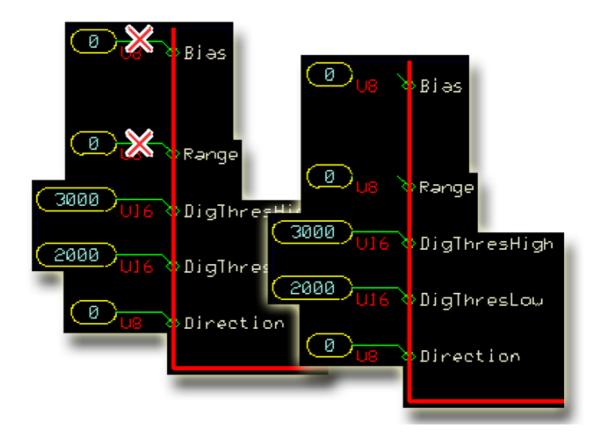
How to Configure a DigAn for the DirIn Input

1. In the GUIDE template, enter the Inputs page.



2. Enter the DigAn that receives the input signal.





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Documents / Resources



<u>Danfoss Compliant EMD Speed Direction Function Block</u> [pdf] User Manual Compliant EMD Speed Direction Function Block, Speed Direction Function Block, Direction Function Block, Function Block, Block

References

- ATO Inverter, Solar Inverter, Home Power Inverter | inverter.com
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