

Waveshare Core357608000

Waveshare Luckfox Core3576 Module User Manual

Model: Core357608000

1. INTRODUCTION

The Waveshare Luckfox Core3576 Module is a high-performance, low-power AIoT processor core module based on Rockchip's RK3576. It features a sophisticated big.LITTLE architecture, integrating quad-core Arm Cortex-A72 at 2.2GHz and quad-core Arm Cortex-A53 at 2.0GHz. This module is equipped with an ARM Mali G52 MC3 GPU for graphics and an NPU delivering 6 TOPS (Tera Operations Per Second) of computing power, making it suitable for a wide range of embedded applications requiring advanced AI capabilities and multimedia processing.

The Core3576 supports 4K encoding and decoding, various display outputs including HDMI, MIPI DSI, and eDP, and a rich set of peripheral interfaces such as PCIe, CSI, USB, I2C, SPI, and GPIO, providing extensive connectivity and expansion options for diverse project requirements.

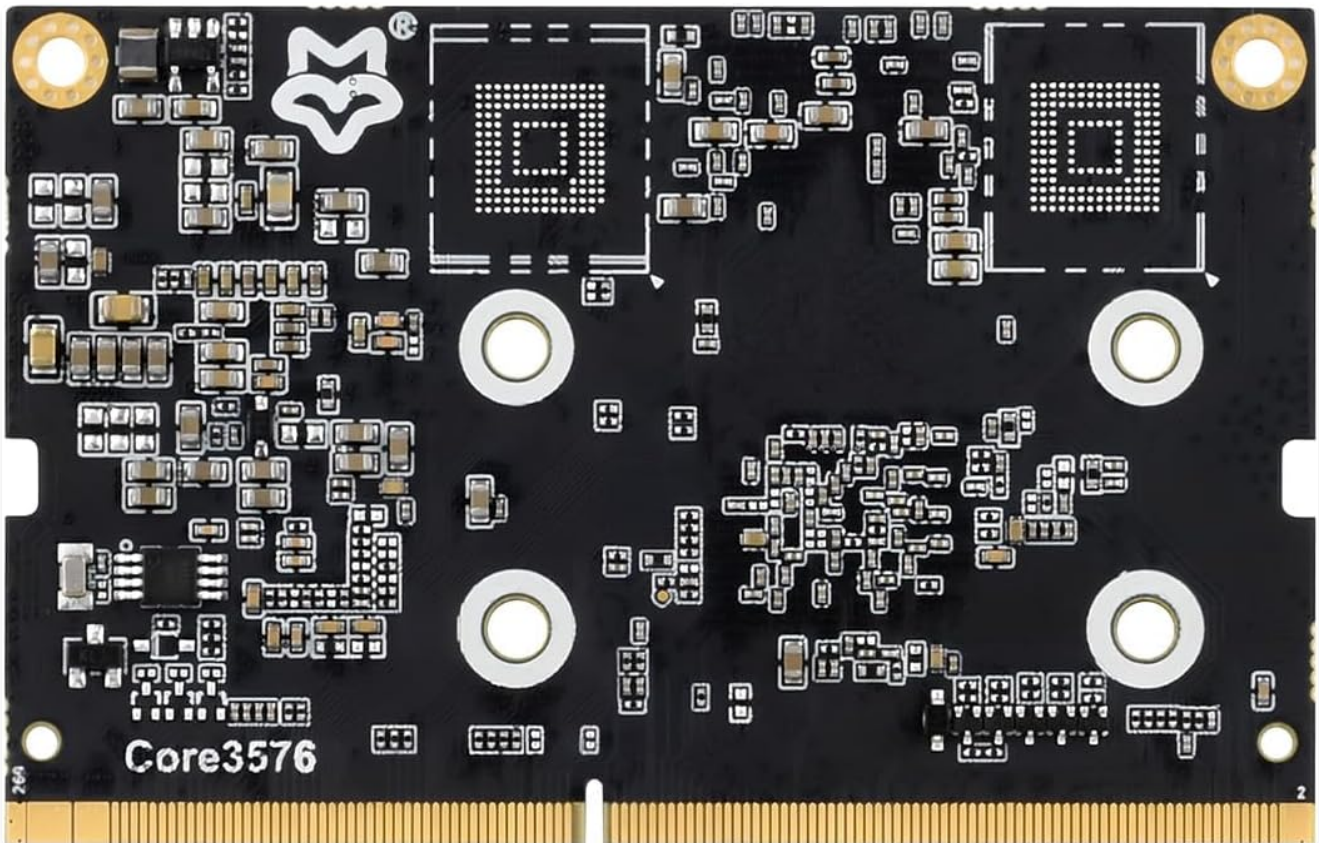


Figure 1.1: Top view of the Waveshare Luckfox Core3576 Module.

2. PACKAGE CONTENTS

Verify that all items are present and in good condition upon opening the package. If any items are missing or damaged, please contact customer support.

- Core357608000 Module x 1

Package Content

Core3576 module x 1

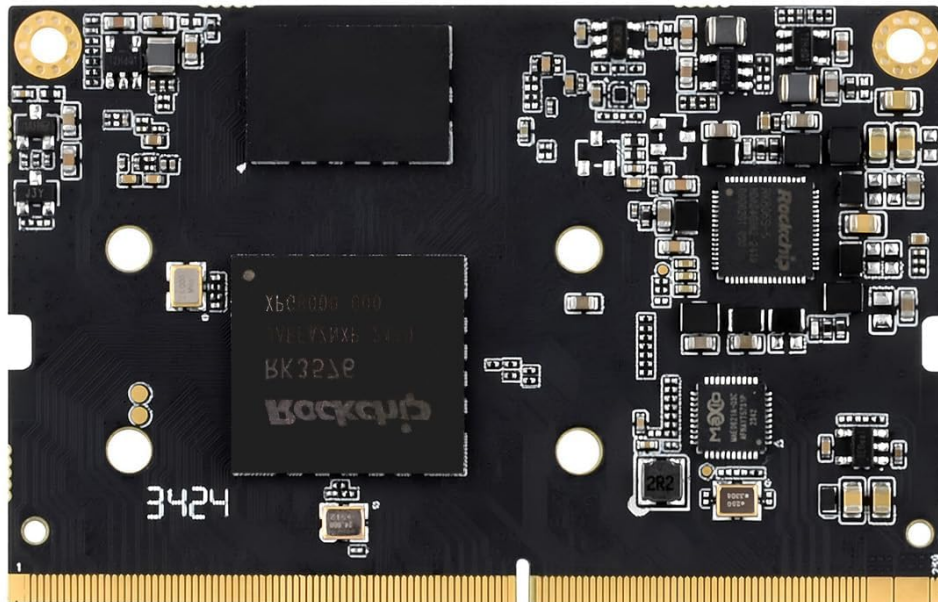
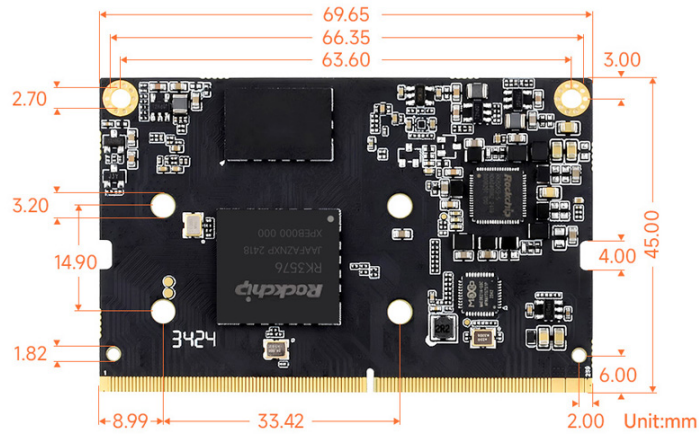


Figure 2.1: Illustration of the package content, showing the Core3576 module.

3. SPECIFICATIONS

The following table details the technical specifications of the Core3576 Module:

Outline dimensions



Core3576 Module Technical Specifications

| Feature | Description |
|---------------------|--|
| Processor | Quad-core Arm Cortex-A72@2.2GHz + Quad-core Arm Cortex-A53@2.0GHz |
| RAM | 8GB LPDDR4X RAM |
| Storage | 0GB eMMC Flash (Options for 0 / 32 / 64GB eMMC Flash) |
| NPU | 6 TOPS@INT8, supports INT4, INT16, FP16, BF16, FP32 mixed operation |
| GPU | ARM Mali G52 MC3@0.9GHz, supports OpenGL ES 1.1/2.0/3.2, OpenCL 2.0 and Vulkan 1.1, and built-in high-performance 2D/3D acceleration hardware |
| ISP | Built-in 16MP ISP, supports multiple algorithm accelerators such as HDR, 3A, CAC, 3DNR, 2DNR, Sharpening, Dehaze, Enhance, Debayer, and small-angle lens distortion correction, etc. |
| Video | Decoding: 4K@120fps (H.265/HEVC, VP9, AVS2, AV1), 4K@60fps (H.264/AVC) Encoding: 4K@60fps (H.265/HEVC, H.264/AVC) |
| Connectivity | Supports 1000M Ethernet × 2 Expandable for 2.4GHz/5GHz dual-band WiFi 6, Bluetooth 5.2 |
| Video Output | HDMI2.1 (4K@120fps) × 1 / eDP1.3 (4K@60fps) × 1 4-lane MIPI DSI (2K@60fps) × 1 |
| Video Input | MIPI CSI DPHY × 2 MIPI CSI D/CPHY |
| PCIe/SATA | PCIe 2.1/SATA3.1 × 2 |
| USB | USB3.2 Gen1 OTG (5Gbps), supports DP protocol USB2.0 interface |
| SDIO | SDIO3.0 × 2 |
| CAN | CAN FD × 2 |
| ADC | SARADC × 8 |

| Feature | Description |
|----------------------|---|
| Expansion Interfaces | I2C × 10; I3C × 2; UART × 12; SPI × 5; CAN × 2; Multiple GPIOs |
| Connector | 260 Pin SO-DIMM Connector |
| Power Input | 5V DC |
| Outline Dimensions | 69.6mm × 45mm |
| Others | Supports 38KHz infrared input Low power RTC chip with backup battery input |

Note: The actual number of available interfaces may be reduced due to pin multiplexing.

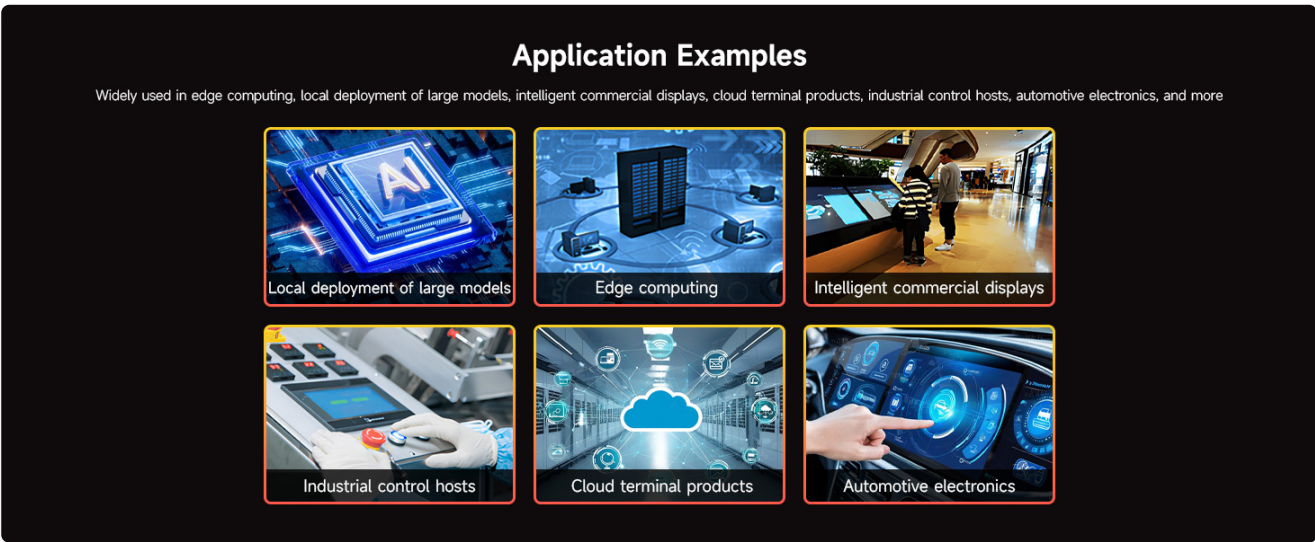


Figure 3.1: Outline dimensions of the Core3576 Module.

4. KEY FEATURES

4.1 Low Power Octa-Core AIoT Processor

The Core3576 is equipped with the high-performance Rockchip RK3576 processor, integrating quad-core Cortex-A72 and quad-core Cortex-A53. This configuration provides robust performance while maintaining high energy efficiency, making it ideal for AIoT applications.

Specifications

| | | | |
|---------------------|--|-----------------------------|--|
| Processor | Quad-core Arm Cortex-A72@2.2GHz + Quad-core Arm Cortex-A53@2.0GHz | USB | USB3.2 Gen1 OTG (5Gbps), supports DP protocol USB3.2 Gen1 interface (5Gbps) USB2.0 interface |
| RAM | Options for 4 / 8GB LPDDR4X RAM | SDIO | SDIO3.0 × 2 |
| Storage | Options for 0 / 32 / 64GB eMMC Flash | CAN | CAN FD × 2 |
| NPU | 6 TOPS@INT8, supports INT4, INT8, INT16, FP16, BF16, TF32 mixed operation | ADC | SARADC × 8 |
| GPU | ARM Mali G52 MC3@0.9GHz, supports OpenGL ES 1.1/2.0/3.2, OpenCL 2.0 and Vulkan 1.1, and built-in high-performance 2D/3D acceleration hardware | Expansion interfaces | I2C × 10; I3C × 2; UART × 12; SPI × 5; CAN × 2; Multiple GPIOs |
| ISP | Built-in 16MP ISP, supports multiple algorithm accelerators such as HDR, 3A, CAC, 3DNR, 2DNR, Sharpening, Dehaze, Enhance, Debayer, and small-angle lens distortion correction, etc. | Connector | 260 Pin SO-DIMM Connector |
| Video | Decoding: 4K@120fps (H.265/HEVC, VP9, AVS2, AV1); 4K@60fps (H.264/AVC) Encoding: 4K@60fps (H.265/HEVC, H.264/AVC) | Power input | 5V DC |
| Connectivity | Supports 1000M Ethernet × 2 Expandable for 2.4GHz/5GHz dual-band Wi-Fi 6, Bluetooth 5.2 | Outline Dimensions | 69.6mm × 45mm |
| Video Output | HDMI2.1 (4K@120fps) × 1 / eDP1.3 (4K@60fps) × 1 4-lane MIPI DSI (2K@60fps) × 1 | Others | Supports 38KHz infrared input Low power RTC chip with backup battery input |
| Video Input | MIPI CSI DPHY × 2 MIPI CSI D/CPHY | | |
| PCIe/SATA | PCIe 2.1/SATA3.1 × 2 | | |

Figure 4.1: Overview of the RK3576 processor architecture.

4.2 6 TOPS Computing Power NPU

The integrated Neural Processing Unit (NPU) offers 6 TOPS of computing power, facilitating the efficient conversion and execution of various neural network models based on popular frameworks such as TensorFlow, MXNet, PyTorch, and Caffe. This enables advanced AI applications like object recognition, face recognition, vehicle recognition, pedestrian detection, text recognition, and semantic segmentation.

Low Power Octa-Core AIoT Processor

Equipped with high-performance RK3576 processor, integrated with quad-core Cortex-A72 and quad-core Cortex-A53, providing strong performance and high energy efficiency

Octa-core
Quad-core Cortex-A72, Quad-core Cortex-A53

2.2GHz
Performance Core: 2.2GHz operating frequency
Efficiency Core: 2.0GHz operating frequency

0.9GHz
Graphics Frequency
ARM Mali G52 MC3 GPU





Figure 4.2: AI applications enabled by the 6 TOPS NPU.

4.3 4K Encoding and Decoding Support

The module supports high-definition video tasks with 4K@120fps decoding (H.265/HEVC, VP9, AVS2, AV1) and 4K@60fps encoding (H.265/HEVC, H.264/AVC). This capability ensures smooth handling of demanding multimedia content.

Supports 4K Encoding and Decoding

Supports 4K@120fps (H.265/HEVC, VP9, AVS2, AV1), 4K@60fps (H.264/AVC) decoding and 4K@60fps (H.265/HEVC, H.264/AVC) encoding, easy to deal with HD video tasks



4K@120fps Decoding

H.265/HEVC
VP9
AVS2
AV1

4K@60fps Encoding

H.265/HEVC
H.264/AVC

Figure 4.3: 4K video encoding and decoding capabilities.

4.4 Dual Gigabit Ethernet Interfaces

The Core3576 module features dual Gigabit Ethernet interfaces, allowing for flexible network configurations. Different types of network traffic can be distributed to separate interfaces, enhancing security and management flexibility for various applications, such as one for external internet connection and another for internal LAN.

Dual Gigabit Ethernet Interfaces

Different types of traffic can be distributed to different network interfaces: one for external Internet connection and another for internal LAN, which improves security and management flexibility



Figure 4.4: Dual Gigabit Ethernet interfaces for enhanced network flexibility.

4.5 Three-Screen Display Support

The module supports simultaneous output to three screens via MIPI DSI, HDMI2.1 (4K@120fps), or eDP1.3 (4K@60fps). Additionally, it can adapt to DP interfaces via USB, enabling versatile multi-screen display setups for various visual applications.



Figure 4.5: Multi-screen display capabilities.

4.6 Rich Peripheral Interfaces

Adopting a SO-DIMM connector, the Core3576 module supports a wide array of expansion interfaces. These include HDMI, MIPI-DSI, DP, MIPI-CSI, PCIe2.0/SATA3, SDIO, GMAC, USB3.0, USB2.0, I2C, I3C, SPI, I2S, UART, and GPIO. This extensive range of interfaces provides robust support for diverse applications, allowing users to quickly integrate the module into their projects.

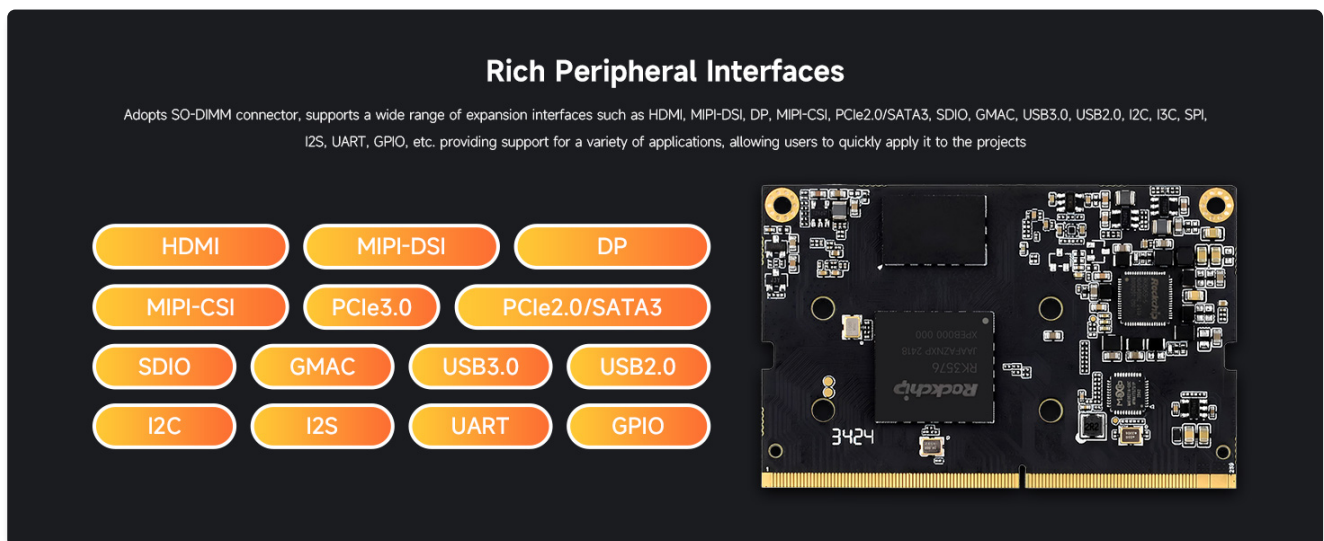


Figure 4.6: Available peripheral interfaces on the Core3576 Module.

5. APPLICATION EXAMPLES

The versatility and powerful capabilities of the Core3576 Module make it suitable for a broad spectrum of applications, including:

- Edge computing
- Local deployment of large models
- Intelligent commercial displays
- Cloud terminal products
- Industrial control hosts

- Automotive electronics

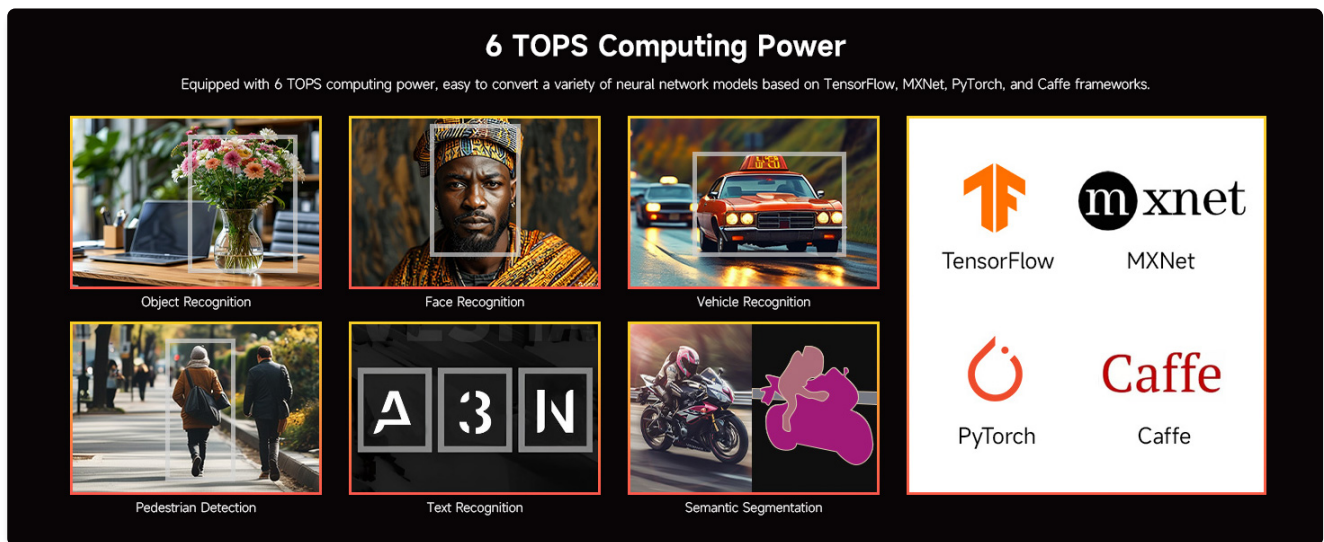


Figure 5.1: Diverse application scenarios for the Core3576 Module.

6. SETUP GUIDE

As a core module, the Core3576 requires integration with a carrier board or custom PCB for full functionality. The setup process typically involves:

1. **Hardware Integration:** Carefully insert the Core3576 module into the compatible SO-DIMM slot on your carrier board. Ensure proper alignment and secure seating.
2. **Power Supply:** Connect a stable 5V DC power supply to your carrier board. Refer to your carrier board's documentation for specific power requirements and connection points.
3. **Peripheral Connections:** Connect necessary peripherals such as display units (via HDMI, MIPI DSI, or eDP), USB devices, network cables, and other sensors or actuators to the carrier board's corresponding ports.
4. **Software Preparation:** Prepare the operating system image (e.g., Linux distribution) compatible with the RK3576 processor. This typically involves flashing the image onto an SD card or eMMC storage if available on your carrier board.
5. **Initial Boot:** Power on the system. Monitor the boot process via a serial console or connected display.

For detailed instructions on specific carrier boards or development kits, please refer to the official Waveshare Wiki resources or the documentation provided with your specific hardware setup.

7. OPERATING INSTRUCTIONS

Operating the Core3576 Module primarily involves interacting with the software running on it. Key aspects include:

1. **Operating System Interaction:** Once booted, interact with the installed operating system (e.g., Linux) via command line, graphical user interface (if available), or remote access (SSH).
2. **Software Development:** Utilize the provided SDKs and toolchains to develop applications. Leverage the NPU for AI inference tasks using frameworks like TensorFlow, PyTorch, etc.

3. **Peripheral Control:** Access and control connected peripherals through the operating system's drivers and APIs.
4. **Network Configuration:** Configure the dual Gigabit Ethernet interfaces and any wireless connectivity (if an expansion module is used) for network access and communication.
5. **Multimedia Processing:** Utilize the module's video encoding and decoding capabilities for multimedia applications.

Refer to the software documentation and examples provided by Waveshare for specific programming guides and application development.

8. MAINTENANCE

To ensure the longevity and optimal performance of your Core3576 Module, consider the following maintenance guidelines:

- **Environmental Conditions:** Operate the module within its specified temperature and humidity ranges. Avoid extreme temperatures, direct sunlight, and high humidity.
- **Dust and Debris:** Keep the module and carrier board free from dust and debris. Use compressed air or a soft brush for cleaning.
- **Power Supply:** Use a stable and appropriate power supply (5V DC) to prevent damage from voltage fluctuations.
- **Firmware/Software Updates:** Regularly check for and apply firmware and software updates from Waveshare to benefit from performance improvements, bug fixes, and security enhancements.
- **Physical Handling:** Handle the module by its edges to avoid touching sensitive components. Use anti-static precautions when handling to prevent electrostatic discharge (ESD) damage.

9. TROUBLESHOOTING

This section provides general troubleshooting tips for common issues. For more specific problems, consult the Waveshare Wiki or community forums.

- **Module Not Powering On:**
 - Verify the power supply is connected correctly and providing the required 5V DC.
 - Ensure the module is properly seated in the SO-DIMM slot.
 - Check for any visible damage to the module or carrier board.
- **No Display Output:**
 - Confirm the display cable is securely connected to both the carrier board and the monitor.
 - Ensure the correct display output (HDMI, MIPI DSI, eDP) is selected in the software configuration.
 - Try a different display cable or monitor to rule out external issues.
- **Network Connectivity Issues:**
 - Check Ethernet cable connections.

- Verify network configurations in the operating system.
- Ensure network drivers are correctly installed.
- **System Instability/Crashes:**
 - Ensure adequate cooling for the module, especially under heavy load.
 - Verify the power supply is stable and sufficient.
 - Check for corrupted software installations or incompatible drivers.

- **System Instability/Crashes:**

- Ensure adequate cooling for the module, especially under heavy load.
- Verify the power supply is stable and sufficient.
- Check for corrupted software installations or incompatible drivers.

If issues persist, consider reinstalling the operating system or contacting Waveshare technical support.

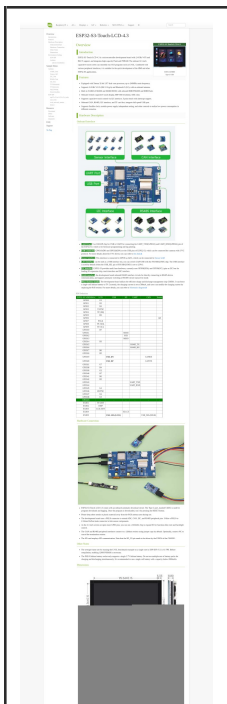
10. WARRANTY AND SUPPORT

For information regarding product warranty, technical support, and additional resources, please refer to the official [Waveshare website](#) and [Wiki](#). Waveshare provides extensive documentation and community support for their products.

- **Official Waveshare Store:** For product information and purchases, visit the [Waveshare Amazon Store](#).
- **Wiki Resources:** Waveshare provides rich Wiki resources with detailed tutorials, code examples, and technical documentation. Please contact Waveshare for more information on accessing these resources.
- **Technical Support:** For technical inquiries or assistance, refer to the support channels listed on the official Waveshare website.

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Related Documents - Core357608000





[Waveshare ESP32-S3-Touch-LCD-4.3 Development Board: Features & Guide](#)

Explore the Waveshare ESP32-S3-Touch-LCD-4.3, a powerful microcontroller development board featuring a 4.3-inch capacitive touch display, WiFi, BLE 5, and multiple interfaces like CAN, RS485, and I2C. Learn about its hardware, setup, and sample demos for HMI development.

2.4inch LCD Module

Overview

Introduction

As a 2.4-inch TFT display module with a resolution of 240 × 320, it uses the SPI interface for communication. LCD has an internal controller with basic functions, which can be used to draw points, lines, circles, and rectangles, and can display English, Chinese as well as pictures. We provide complete supporting Raspberry Pi device (BCM2835 library, wiringPi library and python device), STM32 device, and Arduino device.

Specification

- Operating voltage: 3.3V/5V (When using 5V power supply, the logic voltage is 5V; When using 3.3V power supply, the logic voltage is 3.3V)
- Interface: SPI
- LED Type: TTF
- Controller: E3941
- Resolution: 240(V) x 320(H)RGB
- Display Size: 36.72 (H) x 45.56 (V) mm
- Panel Size: 6.153 (H) x 6.153 (V) mm
- Dimension: 79.5 x 43.3(mm)

Interface Design

Raspberry Pi hardware connection

Please connect the LCD to your Raspberry Pi by the SPI cable according to the table below.

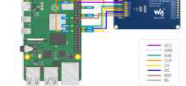
If you use the pin header or PWDIO SPI interface, you need to connect according to the following table.

Converted to Raspberry Pi

| LCD | Raspberry Pi | |
|-----|--------------|-------|
| | BCM2835 | Board |
| VCC | 3.3V | 3.3V |
| GND | GND | GND |
| DAI | MODE | DAI |
| CLK | SCLK | CLK |
| CS | CS0 | CS |
| D0 | D0 | D0 |
| D15 | D15 | D15 |
| BL | BL | BL |

The 2.4-inch LCD uses the MI01.3 SPI interface, which can be connected to the Raspberry Pi according to the above table. *Please connect according to the pin definition table. The color of the cable is the same as the color of the cable in the reference picture, and the color of the cable must be correct.*

color of the wiring in the picture is for reference only, and the actual color shall prevail.)

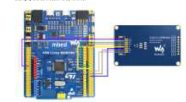


STM32 hardware connection

The example we provide is based on ST002F1330RTS, and the correction method provided is also the corresponding pin of ST002F1330RTS. If you need to transplant the program, please connect according to the actual pin.

| STM32F10507T connection pin correspondence | STM32 |
|--|-------|
| VDD | VDD |
| VCC | VCC |
| GND | GND |
| DB4 | DB7 |
| CLK | PA5 |
| CS | PA6 |
| DC | PA8 |
| RES | PA9 |

Take the [INXCLEO-FL180 development board](#) developed by our company as an example, the connection is as follows:

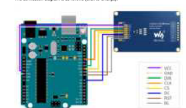


Arduino hardware connection

Antonio UNGO-Connecticut, an correspondence

| Arduino | UNO |
|---------|-----|
| VCC | 5V |
| GND | GND |
| DEN | D11 |
| CLK | D13 |
| CS | D10 |
| DC | D9 |
| RST | D8 |
| RL | D6 |

The connection diagram is as follows (click to enlarge):



Hardware Description

LCD and the controller

The LCD supports 12-bit, 16-bit, and 18-bit input color formats per pixel, namely RGB444, RGB565, and RGB666. Three color formats, this demo uses RGB565 color format, which is also a commonly used RGB format.

For most LCD controllers, the communication mode of the controller can be configured, usually with an RGB parallel interface, three-wire SPI, four-wire SPI, and other communication methods. This LCD uses a four-wire SPI communication interface, which can greatly save the GPIO port, and the communication speed will be faster.

Communication Protocol



Note: Different from the traditional SPI protocol, the data line from the slave to the master is hidden since the device only has a display requirement.

ASIC: the next pin, it should be low when powering the module and be higher at other times.

CS0: slave chip select, when CS is low, the chip is enabled.

DATA[0:data/command control], when $DC = 1$, write command; when $DC = 0$, write data.

DC: the data pin for transmitting DATA data, 1 write as the MDCR pin of SPI interface, CS0 works as the SCLK pin of SPI interface.

SPI communication has data transfer timing, which is controlled by CS# and CPOL. CPOL determines the level of the serial synchronous clock signal. When $CPOL = 0$, it is low level. However, CS# has little effect on the transmission.

CPHA determines whether the data is collected at the first clock edge or at the second clock edge of serial synchronous clock. When $CPHA = 0$, data is collected at the first clock edge. There are 4 SPI communication modes. SPI0 is commonly used, in which $CPHA = 0$, $CPOL = 0$.

Working with Raspberry Pi

- Enable SPI interface

PS: If you are using the system of the Deliaeye branch, you need to change "test-get" to "test", the system of the Buiyeye branch only supports Python3.

- Open terminal, use command to enter the configuration page

[illegible]

would you like the 94 schedule to be mailed?

Install Libraries

- [illegible]

- ```
#On the Raspberry Pi terminal set run the following command
sudo apt-get install vlcppm
After installing Pi system after Ray [10] (earlier than before, you may not need to install), you may need to upgrade:
sudo apt-get install vlcppm
```

```
curl https://github.com/Wirtingo/Wirtingo/raw/master/index.html
curl -v https://github.com/Wirtingo/Wirtingo
git -->
$ But again it will appear. If it does not appear, the test
validation is wrong
```

While you search system see the following command:

```
git clone https://github.com/Wirtingo/Wirtingo
or Wirtingo
```

- ```
# Install Python libraries
```
- ```
python3
sudo apt-get update
sudo apt-get install python3-pip
sudo apt-get install python3-gi
sudo apt-get install python3-numpy
sudo pip install jax dfl
sudo pip install tensorflow
```





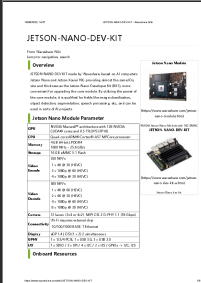
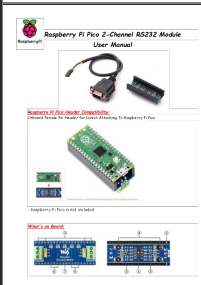





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|  <p>Waveshare Jetson Nano Dev Kit Overview, Setup, and Resources</p> <p>This document provides a comprehensive guide to the Waveshare Jetson Nano Developer Kit, covering its overview, hardware specifications, software setup using SDK Manager, camera configuration, and troubleshooting.</p>                                                                                                                                                                                                       | <p><a href="#">Waveshare Jetson Nano Dev Kit: Overview, Setup, and Resources</a></p> <p>A comprehensive guide to the Waveshare Jetson Nano Developer Kit, covering its overview, hardware specifications, software setup using SDK Manager, camera configuration, and troubleshooting.</p>                                                                                                                                                                                                                            |
|  <p>Raspberry Pi Pico 2-Channel RS232 Module User Manual</p> <p>User manual for the Raspberry Pi Pico 2-Channel RS232 Module, detailing its features, compatibility, onboard components, and pinout definition for connecting RS232 devices to the Raspberry Pi Pico.</p>                                                                                                                                                                                                                              | <p><a href="#">Raspberry Pi Pico 2-Channel RS232 Module User Manual</a></p> <p>User manual for the Raspberry Pi Pico 2-Channel RS232 Module, detailing its features, compatibility, onboard components, and pinout definition for connecting RS232 devices to the Raspberry Pi Pico.</p>                                                                                                                                                                                                                              |
|  <p>Waveshare RS232/485 to Ethernet Converter User Manual</p> <p>This is an industrial-grade RS232/485 to Ethernet module from Waveshare, enabling bidirectional transparent data transmission between RS232, RS485, and RJ45 Ethernet ports. It features a 32-bit ARM M4 processor, 10/100M Ethernet, multiple operating modes (TCP Server/Client, UDP, HTTPD), Modbus gateway, and Websocket support, offering high speed, low power consumption, and stability for industrial IoT applications.</p> | <p><a href="#">Waveshare RS232/485 to Ethernet Converter User Manual</a></p> <p>This is an industrial-grade RS232/485 to Ethernet module from Waveshare, enabling bidirectional transparent data transmission between RS232, RS485, and RJ45 Ethernet ports. It features a 32-bit ARM M4 processor, 10/100M Ethernet, multiple operating modes (TCP Server/Client, UDP, HTTPD), Modbus gateway, and Websocket support, offering high speed, low power consumption, and stability for industrial IoT applications.</p> |