

APEX WAVES PCIe-6612 Counter-Timer TIO Series Modules User Guide

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APEX WAVES PCIe-6612 Counter-Timer TIO Series Modules



Product Information

The PCIe-6612 is a Counter/Timer TIO Series Module/Device that uses the 100-Pin Shielded Connector Block. It can be connected to the SCB-100A and NI 6624 models for digital input/output operations. The product is designed to provide accurate timing and counting capabilities for a variety of applications.

Product Usage Instructions

Before using the PCle-6612, ensure that you have read and understood the user manual provided with the product. Follow the instructions below to use the product:

- 1. Connect the PCIe-6612 to the SCB-100A using the pinout label provided in the user manual.
- 2. Ensure that the connector bank layout on the label matches the actual connector bank layout on the SCB-100A hardware.
- 3. Refer to the SCB-100A Quick Reference Label for Counter/Timer NI 6624 TIO Devices while connecting the devices.
- 4. Note that 'NC' means 'No Connect' and should not be connected to any other device.
- 5. Follow the instructions provided in the user manual to configure and operate the PCIe-6612 for your specificapplication.
- 6. If you encounter any issues while using the product, refer to the troubleshooting section in the user manual or contact customer support for assistance.

COMPREHENSIVE SERVICES

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NOTE TO USERS

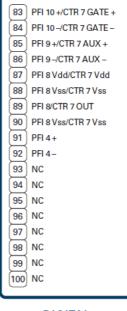
NI 66xx Pinout Label for the SCB-100A

Counter/Timer TIO Series Modules/Devices Using the 100-Pin Shielded Connector Block If you are using an NI 66xx (formerly referred to as TIO Series) counter/timer device or module with the SCB-100A, you can print and attach a connector-specific pinout label to the cover of the connector block for easy connection reference. This document provides labels for the following models: NI 6624

Use the following label

if you are connecting the SCB-100A to the NI 6624 models. Note that the connector bank layout on the label differs from the actual connector bank layout on the SCB-100A hardware. NI 6624

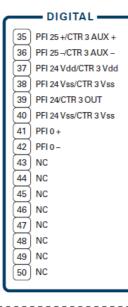




DIGITAL

DIGITAL • 19 PFI 32/CTR 1 OUT PFI 32 Vss/CTR 1 Vss 20 21 PFI 31 +/CTR 2 SOURCE + 22 PFI 31 -/CTR 2 SOURCE -23 PFI 30 +/CTR 2 GATE + 24 PFI 30 -/CTR 2 GATE -PFI 29 +/CTR 2 AUX + 25 PFI 29 -/CTR 2 AUX -26 27 PFI 28 Vdd/CTR 2 Vdd PFI 28 Vss/CTR 2 Vss 28 PFI 28/CTR 2 OUT 30 PFI 28 Vss/CTR 2 Vss 31 PFI 27 +/CTR 3 SOURCE + PFI 27 -/CTR 3 SOURCE -32 33 PFI 26 +/CTR 3 GATE + PFI 26 -/CTR 3 GATE -34

PFI 32 Vss/CTR 1 Vss



DIGITAL PFI 23 +/CTR 4 SOURCE 52 PFI 23 -/CTR 4 SOURCE -53 PFI 22 +/CTR 4 GATE + 54 PFI 22 -/CTR 4 GATE -55 PFI 21 +/CTR 4 AUX + 56 PFI 21 -/CTR 4 AUX -PFI 20 Vdd/CTR 4 Vdd 57 PFI 20 Vss/CTR 4 Vss 58 59 PFI 20/CTR 4 OUT PFI 20 Vss/CTR 4 Vss 60 PFI 19 +/CTR 5 SOURCE + 62 PFI 19 -/CTR 5 SOURCE -63 PFI 18 +/CTR 5 GATE + 64 PFI 18-/CTR 5 GATE -65 PFI 17 +/CTR 5 AUX + PFI 17 -/CTR 5 AUX -66

DIGITAL 67 PFI 16 Vdd/CTR 5 Vdd PFI 16 Vss/CTR 5 Vss 69 PFI 16/CTR 5 OUT 70 PFI 16 Vss/CTR 5 Vss 71 PFI 15 +/CTR 6 SOURCE + PFI 15 -/CTR 6 SOURCE -PFI 14 +/CTR 6 GATE + 73 74 PFI 14-/CTR 6 GATE -75 PFI 13 +/CTR 6 AUX + 76 PFI 13 -/CTR 6 AUX -77 PFI 12 Vdd/CTR 6 Vdd 78 PFI 12 Vss/CTR 6 Vss 79 PFI 12/CTR 6 OUT PFI 12 Vss/CTR 6 Vss 80 81 PFI 11 +/CTR 7 SOURCE + PFI 11 -/CTR 7 SOURCE 82

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Documents / Resources



<u>APEX WAVES PCIe-6612 Counter-Timer TIO Series Modules</u> [pdf] User Guide PCIe-6612 Counter-Timer TIO Series Modules, PCIe-6612, Counter-Timer TIO Series Modules, TIO Series Modules

References

- n Trade Compliance NI
- National Instruments Patents NI
- NI Trademarks and Logo Guidelines NI
- /W PCle-6612 National Instruments Counter/Timer Device | Apex Waves

Manuals+,