



ANALOG DEVICES DC3137A Power Management Development Boards and Kits User Guide

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FEATURES

- Fully featured evaluation board for the LT4322
- High voltage full-wave rectification
- Ideal Diode bridge
- Schottky diode bridge replacement

EVALUATION KIT CONTENTS

- DC3137A evaluation board

DOCUMENTS NEEDED

- LT4322 data sheet

EQUIPMENT NEEDED

- AC power supply
- Voltmeter
- Constant current or resistive load
- Oscilloscope

GENERAL DESCRIPTION

Demonstration circuit DC3137A features the floating, high voltage active rectifier controller LT4322 in a full-wave rectifier configuration. It is suitable for applications that require high voltage line rectification with DC outputs up to 170V. While components were chosen to optimize performance at 60Hz, the LT4322 is capable of operating at frequencies up to 100kHz.

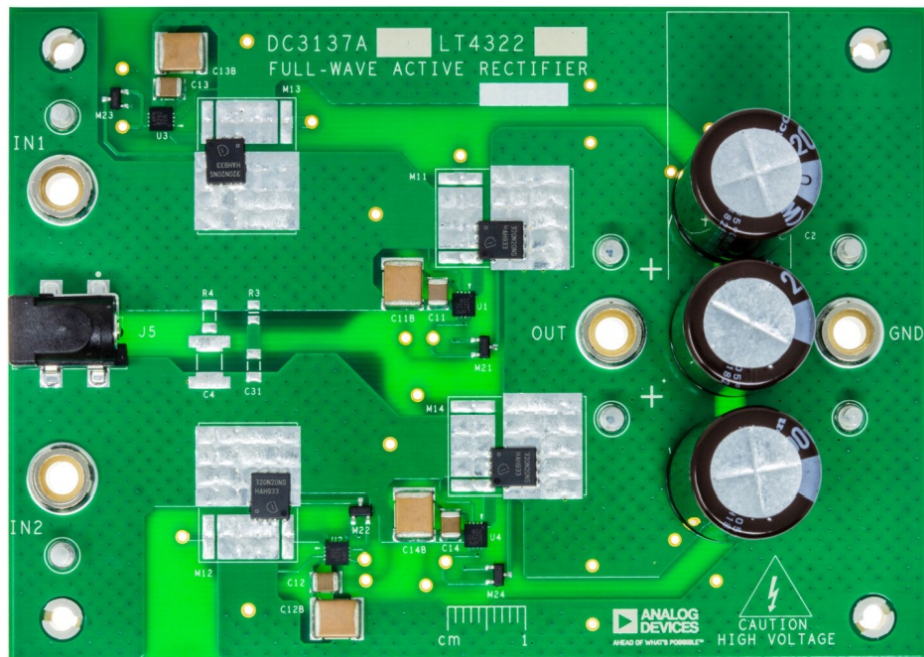
The LT4322 drives an N-Channel MOSFET to perform functionally like a diode but with much lower power dissipation. This topology eases thermal constraints and increases the usable output voltage. An N-Channel topology has multiple benefits over a P-Channel topology, including lower RDS(ON), a smaller footprint, lower cost, and a wider selection of MOSFETs. In this full-wave rectifier, four LT4322 circuits replace four diodes.

Only a few essential components are required to operate each LT4322: a single N-Channel MOSFET (M1x), a reservoir capacitor (C1xB), a gate capacitor (C5x), and in applications where the peak-to-peak input voltage exceeds 60V, an N-Channel depletion mode MOSFET (M2x). In most applications, only a single output capacitor C2 is needed to smooth the output voltage and ensure the output voltage ripple stays below a user-determined maximum value.

Design files for this circuit board are available at: <http://www.analog.com>

DC3137A EVALUATION BOARD PHOTOGRAPH

Figure 1. DC3137A Evaluation Board Photograph



PERFORMANCE SUMMARY

Specifications are at TA = 25°C, unless otherwise noted.

Table 1. Performance Summary1

Parameter	Test Conditions/Comment	Min	Typ	Max	Unit
AC Input Voltage	Shorting Resistors R11, R12, R13, and R14 Installed No Shorting Resistors	77	120	42 140	VAC(RMS) VAC (RMS)
Output Voltage	Shorting Resistors R11, R12, R13, and R14 Installed No Shorting Resistors				
Output Current	With C2, C2-2, and C2-3 Installed (660µF total), resistive load With only C2 installed (220µF total), resistive load	9.5	170	60 200	V V

QUICK START PROCEDURE

WARNING! High voltage testing should be performed by qualified personnel only. As a safety precaution, at least two people should be present during high voltage testing. There are exposed conductors on the bottom of the board, and any banana plugs present will protrude through the bottom of the board. The underlying surface should be non-conductive and clear of any wire, solder, and other conductive debris.

A simple demonstration of DC3137A operation is as follows:

1. Connect an AC power supply between IN1 and IN2, as shown in **Figure 2**. Make sure that the output voltage of the supply is within the input voltage range of the DC3137A, as shown in Table 1. Verify that shorting resistors R11 to R14 have been removed before exceeding 42VAC(RMS). Take care not to exceed 24V or 5A when using the barrel jack. Use the turrets (E1 to E6) and banana jacks (J1 to J4) in all valid current/voltage ranges.
2. Connect a load and voltmeter across OUT and GND, as shown in Figure 2. Turn down the load current to zero. Put the voltmeter in DC volt measurement mode.
3. Raise the AC input power supply voltage to the desired level. Check the output voltage with the voltmeter. For cases where the input supply is a 120VAC line voltage, the voltmeter reads ~170VDC.
4. Raise the load current to the desired level. Make sure that the load current is less than or equal to the maximum load current, as shown in Table 1. The output smoothing capacitors' (UCY2D221MHD capacitors C2, C2-2, and C2-3) combined value is equal to 660 μ F with a ripple current rating that allows for a load up to 5.2A(RMS) at 25°C. C2-3 can be removed for loads ranging from 2.9A(RMS) to 4.6A(RMS). Both C2-2 and C2-3 can be removed for output load currents below 2.9A(RMS).

Figure 2. Measurement Equipment Setup

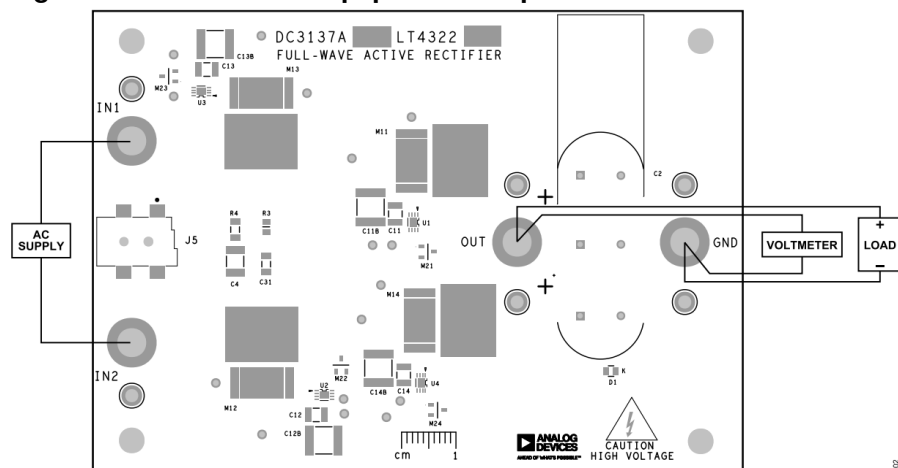
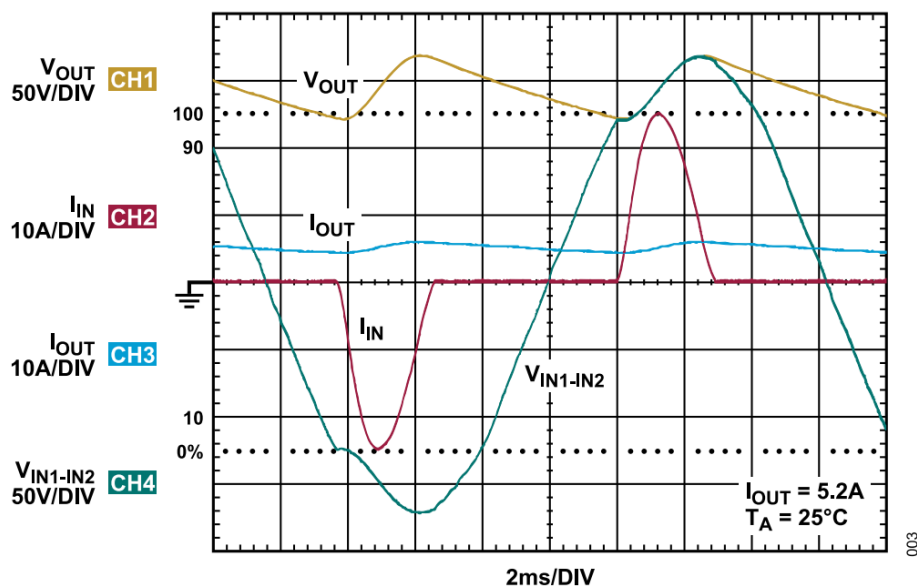


Figure 3. Typical Performance Under 5.2ARMS Resistive Load



BOARD DESCRIPTION

OVERVIEW

DC3137A features four LT4322s, each controlling an N-Channel MOSFET to provide a highly efficient, compact, and low-profile solution for full-wave rectification. Careful attention has been paid to the board layout to provide at least 104mil (2.6mm) clearance between the larger copper planes and as much clearance as possible between components and traces to ensure DC3137A operation up to the maximum voltages of the chosen components.

DC3137A is a 2-layer board with 2oz copper on each layer. The copper in the power path can carry 20A continuously, depending on ambient conditions. Furthermore, all copper planes in the power path are doubled on the bottom copper layer where possible. With the default components, however, the load current is limited to 5.2A(RMS) by the power MOSFETs.

For ease of evaluation probe points have been provided for the LT4322 pins.

The following is a brief description of the main components of DC3137A.

U1 TO U4 – THE DIODE CONTROLLERS

U1 to U4 are LT4322 controllers, each in an 8-pin, 3mm x 3mm side-wettable DFN package. For more details, refer to the LT4322 data sheet on its operation.

M1X – IDEAL DIODE MOSFETS

M1x (M11 to M14) is the Infineon N-Channel MOSFET BSC320N20NS3 in a SuperSO8 package. It was selected for its 200V drain-to-source breakdown voltage, $\pm 20V$ VGS(MAX), and 27m Ω drain-to-source on-state resistance (at 10V VGS). M1x's $\pm 20V$ VGS(max) is compatible with the 12V limit on the LT4322's gate drive. During the peaks of the AC input, the drain-to-source voltage reaches 170V. This is comfortably below M1x's 200V drain-to-source breakdown voltage specification.

M2X – DEPLETION MODE MOSFETS

M2x (M21 to M24) is the Microchip N-Channel depletion mode MOSFET DN3135 in a TO-236AB (SOT-23) package. It was selected for its 350V drain-to-source breakdown voltage and 180mA IDSS. During the peaks of the AC input, M2x's drain-to-source voltage is close to 170V, safely below its 350V breakdown. M2x's 180mA

IDSS allows the 50mA to 100mA peak current required by the LT4322 VDDC pin while refreshing its VDDA reservoir capacitor.

C1X AND C1XB – VDDA RESERVOIR CAPACITORS

Due to their strong voltage coefficient, the actual value of multilayer ceramic capacitors is often significantly less than what is stated, especially at voltages close to the capacitor's maximum voltage rating. Additionally, the voltage coefficient is a function of the capacitor's physical size. A 2220, 25V-rated ceramic capacitor is chosen for C1xB to achieve a true value of 22 μ F at the 12V operating voltage for this 60Hz application.

Alternatively, for 60Hz applications, users can populate C1x with a 0.1 μ F ceramic capacitor and solder a 22 μ F aluminum electrolytic capacitor between each LT4322's VDDA and VSSA pins instead of populating C1xB. For input frequencies \geq 200Hz, users can leave C1xB unpopulated and populate only C1x.

C5X – GATE CAPACITOR

The LT4322 is optimally compensated with a 10nF capacitance between the gate and source of the external power MOSFET. The necessity of C5x is dependent on the choice of M1 and its inherent CISS value. In the case of the BSC320N20NS3, CG1 is populated with a 10nF capacitor to improve stability in forward regulation. For more details, refer to the Gate Capacitor Selection section of the LT4322 data sheet.

C2, C2-2, C2-3 – OUTPUT CAPACITORS

Output capacitors C2, C2-2, and C2-3 provide the output load current for the majority of the AC period. For more details, refer to the Output Capacitor COUT Selection section of the LT4322 data sheet on selecting the capacitance value as a function of output load current, AC period, and maximum allowed output voltage droop. Figure 3 shows the output voltage droops from 170V to 122V for a 5.2A(RMS) resistive load and 16.7ms period (60Hz) when C2 = C2-2 = C2-3 = 220 μ F.

The user must make sure the RMS current in the capacitors does not exceed the maximum ripple current rating. This ensures the capacitor lifetime is not compromised. An electrolytic capacitor's ripple current rating is a function of RMS current, frequency, and ambient temperature. Consult the manufacturer's specifications and ensure that the selected device is suited to operate within the required frequency, temperature, and load current conditions of the application.

The user can choose to populate only one or two of these output capacitors if the output voltage droop and ripple current requirements of their application allow.

OPTIONAL COMPONENT PADS

Some components (M1x, M2x, C2, and C22) are provided with extra unstuffed pads to try out different values and sizes or other circuits from the LT4322 data sheet. Some of these extra pads are on the backside of the board.

M1 has a universal MOSFET footprint on both outer layers to accommodate power-SO8, DPAK, D2PAK, HSOF, and LPAK packages. The user can populate the top and bottom M1 footprints simultaneously to connect two power MOSFETs in parallel, thereby reducing the total MOSFET power loss by a factor of two. M2x has footprints for SOT-23 and SOT-89.

While the board is populated with three electrolytic capacitors on the output voltage by default, there is also a footprint for a multilayer ceramic capacitor C22 on the output. The user can populate various combinations of the ceramic capacitor and aluminum electrolytic capacitors to try various combinations of total output capacitance and ESR with various output current loads.

Components R3, R4, C31, C32, and C4 are provided to facilitate optional snubbing networks. Although they are populated by default, they are unnecessary in most applications. For more details, refer to the Input Snubber section of the LT4322 data sheet.

VOLTAGE, CURRENT, FREQUENCY MODIFICATIONS

For higher voltage operation, see Table 2 and ensure that the stated components meet or exceed the minimum voltage requirement for the desired input/output voltages.

To modify the board for higher current, try the following in this order, while still ensuring all board components meet the voltage requirements in **Table 2**:

- 1. Add matching FETs in parallel using the backside MOSFET footprints.
- 2. Select an M1x replacement with a lower RDS(ON) value.
- 3. After modifying the MOSFETs, raise the C2, C2-2, and C2-3 values and ripple current capacities.

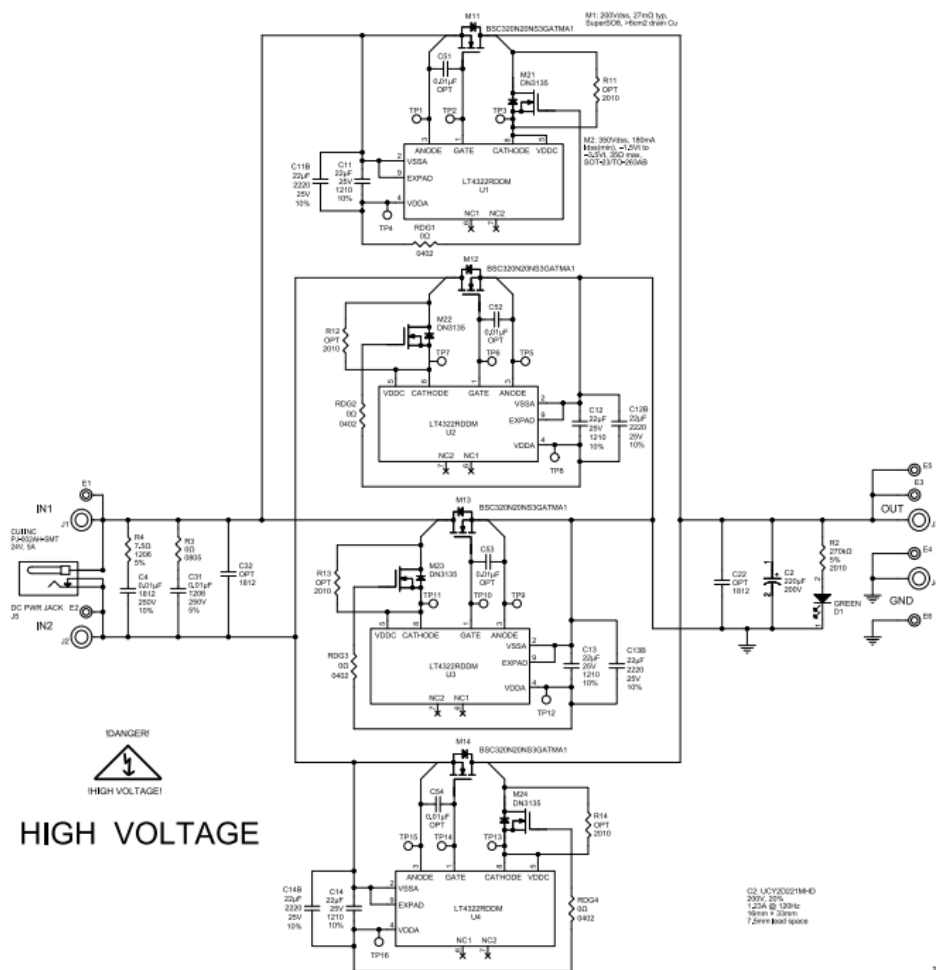
For applications using an AC input supply less than 42VRMS, R1x can be installed to short M2x from the circuit. For higher frequency AC input, it is optimal to pick a lower value C1x even though the installed value works. For frequencies below 60Hz, C1x must be increased. For more details, refer to the VDDA Capacitor Selection section of the LT4322 data sheet.

Table 2. Voltage Requirements

Part Reference	Minimum Voltage Requirement
C1x, C1xB, C5x	16V
C2, C2-2, C2-3,	
C4, C22, C31, C32	VIN(PEAK) or Desired VOUT(MAXDC)
M1x, M2x	$BVDSS \geq VIN(PEAK-PEAK)$

EVALUATION BOARD SCHEMATIC

Figure 4. DC3137A Schematic Diagram



Item	Quantity	Reference Designator	Part Description	Manufacturer, Part Number
			Capacitors, 220 μ F, Aluminium 200V, 20%,	
1	3	C2, C2-2, C2-3	16 \times 31.5mm, AEC-Q200	Nichicon, UCY2D221MHD
				AVX, 12103C226KAT2AC1 Kemet,
				210C226K3RACTU Murata,
				GRM32ER71E226KE1 Samsung,
2	4	C11, C12, C13, C14	Capacitors, 22 μ F, X7R, 25 V, 10%, 1210	5LCL32B226KAJNNNE
				Kemet C2220C226K3RAC78 Kyocera
				0022203C226KAZ2A AVX CAL-CHIP
3	4	C11B, C12B, C13B, C14B	Capacitors, CER 22 μ F 25 V X7R 2220	Electronics, GMC55X7R226K25NT
4	4	C51, C52, C53, C54	Capacitors, 0.01 μ F, X7R, 16V, 10%, 0805	Wurth Elektronik, 885012207039

			Transistors, MOSFET, N-C hannel, 200V,	Infineon, BSC320N20NS3 Infineon,
5	4	M11, M12, M13, M14	36A, TDSON-8	GBSC320N20NS3GATMA1
			Transistors, MOSFET N-C hannel	
			Depletion Mode, 350V, 72 mA, 3-PIN	
6	4	M21, M22, M23, M24	SOT-23, DN3135	Microchip Technology, DN 3135K1-G
				Nicohm, NRC04ZOTRFM CR0
		RDG1, RDG2, RDG3,		Vishay, 1MZPJ000CRCW040 , Yageo,
7	4	RDG4	Resistors, 0Ω, 1/16W, 0402	20000Z0EDRC0402JR-070RL
			IC, Active Bridge Ideal Dio de Controller,	
8	4	U1, U2, U3, U4	DFN-8	Analog Devices Inc., LT4322RDDM#PBF
Additional Demo Board Circuit Components				
9	1	C4	Capacitor, 0.01μF, X7R, 250 V, 10%, 1812	Kemet, C1812C103KARACTU
10	0	C22, C32	Capacitor, Option, 1812	
11	1	C31	Capacitor, 0.01μF, U2J, 250V, 5%, 1206	Murata, GRM31B7U2E103JW31
12	1	D1	LED, Green, Water-Clear, 0805	Würth Elektronik, 150080GS75000
		M11B, M12B, M13B,	Transistors, MOSFET, N-C hannel, 200V,	Infineon, BSC320N20NS3 Infineon,
13	0	M14B	36A, TDSON-8	GBSC320N20NS3GATMA1
			Transistors, MOSFET N-C hannel	
		M21-2, M22-2, M23-2,	Depletion Mode, 350V, 0.135A, 4pin(3 +	
14	0	M24-2	tap) SOT-89	Microchip Technology, DN 3135N8-G
			Resistor, 270kΩ , 5%, ¼W, 2010, AEC-	

15	1	R2	Q200	Panasonic, ERJ-12ZYJ274 U
16	1	R3	Resistor., 0Ω, 1/8W, 0805	Yageo, RC0805JR-070RL
17	1	R4	Resistor, 7.5Ω, 5%, ¼W, 1206	Yageo, RC1206JR-077R5 L
18	0	R11, R12, R13, R14	Resistors, Option, 2010	
Hardware: For Demo Board Only				
			Test Point, Turret, 0.094" MTG. Hole, PCB	
19	6	E1, E2, E3, E4, E5, E6	0.062" THK	Mill-Max, 2501-2-00-80-00 -00-07-0
			Connectors, Banana Jack, Female, THT,	
20	4	J1, J2, J3, J4	Non-Insulated, Swage, 0.218"	Keystone, 575-4
			Connector, DC PWR Jack, Female,	
			3Term, 1Port, 2mm ID, 6.5 mm OD,	
21	1	J5	Horizontal, R/A, SMT, 24V DC, 5A	Cui Inc., PJ-002AH-SMT-T R
22	1	LB1	Label Spec, Demo Board Serial Number	Brady, THT-96-717-10
				Keystone, Wurth Elektronik,
23	4	MP1, MP2, MP3, MP4	Standoff, Nylon, Snap-on, 0.25" (6.4mm)	8831702931000
24	1	PCB1	PCB, DC3137A	ADI Approved supplier, 600-DC3137A
		TP1, TP2, TP3, TP4,		
25	0	TP5, TP6, TP7, TP8, TP9,	Test point, 0.044", 0.275 L x 0.093 W, TH	Keystone, 1036



ESD Caution

ESD (electrostatic discharge) sensitive device. Charged devices and circuit boards can discharge without detection. Although this product features patented or proprietary protection circuitry, damage may occur on devices subjected to high energy ESD. Therefore, proper ESD precautions should be taken to avoid performance degradation or loss of functionality.

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References

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