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ANALOG DEVICES DC2784B-A Series Evaluation Board Instruction Manual



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DEMO MANUAL DC2784B-A

LTM4700 High Current, PolyPhase® Step-Down Power µModule Supply with Digital Power System Management 2 x LTM4700, 200A

DESCRIPTION

Demonstration circuit 2784B-A is a high efficiency, high density, µModule® regulator with 4.5V to 16V input range. The output voltage is adjustable from 0.5V to 1.8V and it can supply 200A maximum load current. The demo board has two LTM®4700 μModule regulators, which is a dual 50A or single 100A step-down regulator with digital power system management. See LTM4700 data sheet for more detailed information.

The DC2784B-A powers up to default settings and produces power based on configuration resistors without the need for any serial bus communication. This allows easy evaluation of the DC/DC converter. To fully explore the extensive power system management features of the part, download the graphical user interface (GUI) software LTpowerPlay® onto your PC and use Analog Devices I2C/SMBus/PMBus dongle DC1613A to connect to the board. The LTpowerPlay allows the user to reconfigure the part on-the-fly and store the configuration in EEPROM, view telemetry of voltage, current, temperature, and fault status.

The software can be downloaded from LTpowerPlay.

For more details and instructions of LTpowerPlay, please refer to LTpowerPlay GUI for LTM4700 Quick Start Guide.

Design files for this circuit board are available.

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BOARD PHOTO

Part marking is either ink mark or laser mark

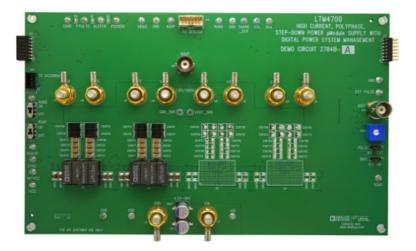


Figure 1. 2 x LTM4700; 200A DC2784B-A Demo Circuit

PERFORMANCE SUMMARY

Specifications are at $T_A = 25$ °C

PARAMETER	CONDITIONS		TYP	MAX	VALUE
Input Voltage Range		4.5 16			V
Output Voltage, V _{OUT}	V _{IN} = 4.5V to 16V, I _{OUT} = 0A to 200A	0.5 1.0 1.8			V
Maximum Output Current, I _{OUT}	V _{IN} = 4.5V to 16V, V _{OUT} = 0.5V to 1.8V		200		
Typical Efficiency	V _{IN} = 12V, V _{OUT} = 1.0V, I _{OUT} = 200A	88.7	88.7 (See Figure 5)		
Default Switching Frequency		350			kHz

Table 1. LTM4700 Demo Boards for Up to 400A Point-of-Load Regulation

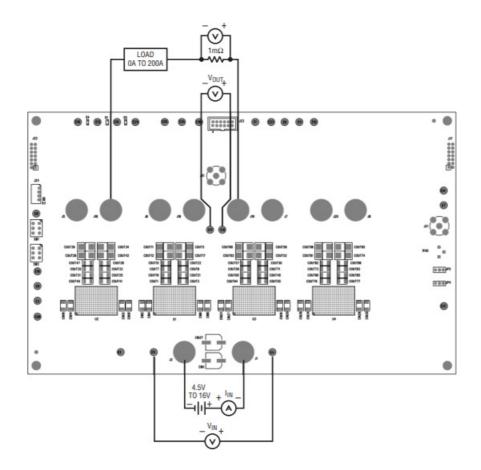
MAXIMUM OUTPUT CURRENT	NUMBER OF O UTPUTS	NUMBER OF LTM4700 μModule REGULATO RS ON THE BOARD	DEMO BOARD N UMBER
50A	2	1	DC2702A-A
100A	1	1	DC2702A-B
200A	1	2	DC2784B-A
300A	1	3	DC2784B-B
400A	1	4	DC2784B-C

Demonstration circuit 2784B-A is easy to set up to evaluate the performance of the LTM4700EY. Refer to Figure 2 for the proper measurement equipment setup and follow the procedure below.

- 1. With power off, connect the input power supply to VIN (4.5V to 16V) and GND (input return).
- 2. Connect the 1.0V output load between VOUT and GND (initial load: no load).
- 3. Connect the DVMs to the input and outputs. Set default jumper position: JP1: ON; JP2: ON; JP3: ON.
- 4. Turn on the input power supply and check for the proper output voltages. VOUT should be 1.0V ±0.5%.
- 5. Once the proper output voltages are established, adjust the loads within the operating range and observe the output voltage regulation, ripple voltage and other parameters.
- 6. Connect the dongle and control the output voltages from the GUI. See "LTpowerPlay GUI for the LTM4700 Quick Start Guide" for details.

NOTE: Internal bias circuit is enabled when VIN > 7V and JP3 is ON.

NOTE: When measuring the output or input voltage ripple, do not use the long ground lead on the oscilloscope probe. See Figure 3 for the proper scope probe technique. Short, stiff leads need to be soldered to the (+) and (-) terminals of an output capacitor. The probe's ground ring needs to touch the (-) lead and the probe tip needs to touch the (+) lead.



DC2784BA F02

Figure 2. Proper Measurement Equipment Setup

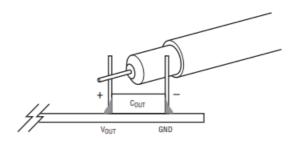
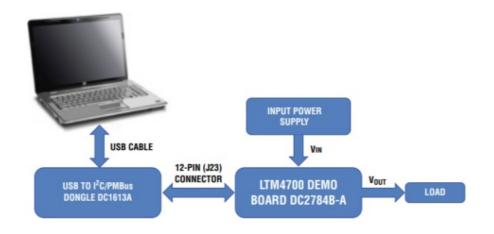


Figure 3. Measuring Output Voltage Ripple

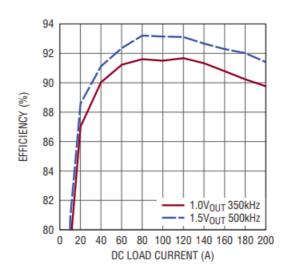
Connecting a PC to DC2784B-A

You can use a PC to reconfigure the power management features of the LTM4700 such as: nominal V $_{OUT}$, margin set points, OV/UV limits, temperature fault limits, sequencing parameters, the fault log, fault responses, GPIOs and other functionalities. The DC1613A dongle may be plugged when V_{IN} is present.



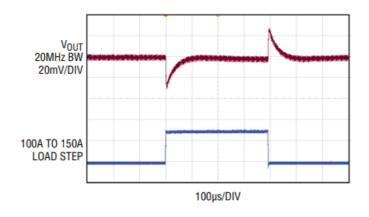
DC2784BA F04

Figure 4. Demo Setup with PC



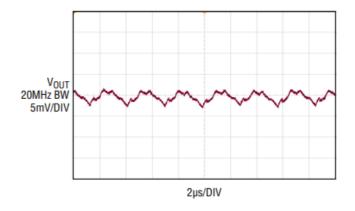
DC2784BA F05

Figure 5. Efficiency vs Load Current at V_{IN} = 12V (RUNP is ON)



DC2784BA F06

Figure 6. Output Voltage vs Load Current at V_{IN} = 12V, V_{OUT} = 1.0V



DC2784BA F07

Figure 7. Output Voltage Ripple at $V_{IN} = 12V$, VOUT = 1.0V, $I_{OUT} = 200A$

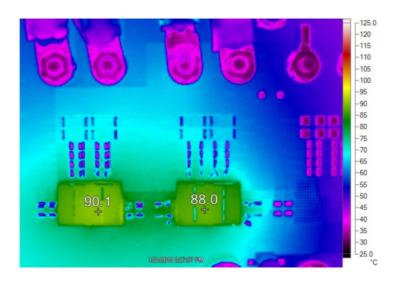


Figure 8. Thermal at V_{IN} = 12V, V_{OUT} = 1.0V, I_{OUT} = 140A, T_A = 25°C, No Airflow

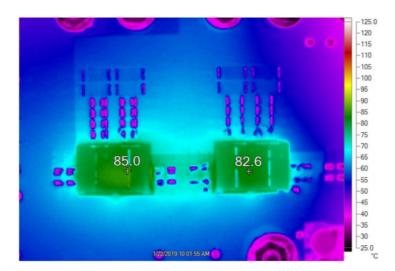


Figure 9. Thermal at V_{IN} = 12V, V_{OUT} = 1.0V, I_{OUT} = 200A, T_A = 25°C, 400LFM Airflow

LTpowerPlay SOFTWARE GUI

The LTpowerPlay is a powerful Windows-based development environment that supports Analog Devices power system management ICs and µModules, including the LTM4675, LTM4676, LTM4677, LTM4678, LTC3880, LTC3882 and LTC3883. The software supports a variety of different tasks. You can use LTpowerPlay to evaluate Analog Devices ICs by connecting to a demo board system. LTpowerPlay can also be used in an offline mode

(with no hardware present) in order to build a multichip configuration file that can be saved and reloaded at a later time. LTpowerPlay provides unprecedented diagnostic and debug features. It becomes a valuable diagnostic tool during board bring-up to program or tweak the power management scheme in a system, or to diagnose power issues when bringing up rails. LTpowerPlay utilizes the DC1613A USB-to-SMBus controller to communicate with one of many potential targets, including the LTM4675, LTM4676, LTM4677, LTM4678, LTC3880, LTC3882, LTC3883's demo system, or a customer board. The software also provides an automatic update feature to keep the software current with the latest set of device drivers and documentation. The LTpowerPlay software can be downloaded from LTpowerPlay.

To access technical support documents for Analog Devices Digital Power Products visit the LTpowerPlay Help menu. Online help also available through the LTpowerPlay.

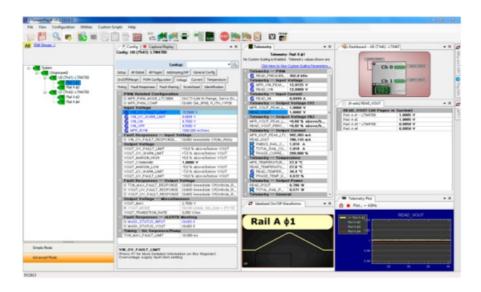
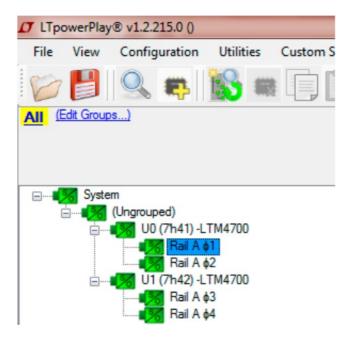


Figure 10. LTpowerPlay Main Interface

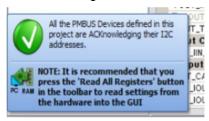
LTpowerPlay QUICK START PROCEDURE

The following procedure describes how to use LTpowerPlay to monitor and change the settings of LTM4700.

- 1. Download and install the LTpowerPlay GUI: LTpowerPlay
- 2. Launch the LTpowerPlay GUI.
 - a. The GUI should automatically identify the DC2784B-A. The system tree on the left hand side should look like this:



b. A green message box shows for a few seconds in the lower left hand corner, confirming that LTM4700 is communicating:



c. In the Toolbar, click the "R" (RAM to PC) icon to read the RAM from the LTM4700. This reads the configuration from the RAM of LTM4700 and loads it into the GUI.



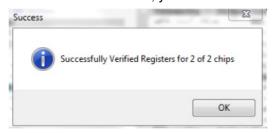
d. If you want to change the output voltage to a different value, like 0.8V. In the Config tab, type in 0.8 in the VOUT COMMAND box, like this:



Then, click the "W" (PC to RAM) icon to write these register values to the LTM4700. After finishing this step, you will see the output voltage will change to 0.8V.



If the write is successful, you will see the following message:



e. You can save the changes into the NVM. In the tool bar, click "RAM to NVM" button, as following



f. Save the demo board configuration to a (*.proj) file.

Click the Save icon and save the file. Name it what-ever you want.

PARTS LIST

I T E M	Q T Y	REFERENCE	PART DESCRIPTI ON	MANUFACTUR ER/PART NUM BER
Re	quir	red Circuit Components		
1	4	CIN1, CIN16, CIN37, CIN38	CAP., 180µF, ALU M. POLY., 25V, 20 %, 8mm × 12mm S MD, E12	PANASONIC, 2 5SVPF180M
2	1 6	CIN-CIN15, CIN17, CIN18	CAP., 22μF X6S 25 V 10% 1210;	MURATA, GRM32EC81E2 26KE15L
3	3 8	COUT1-COUT3, COUT6-COUT8, COUT14- COUT16, COUT1 8-COUT27, COUT29-COUT33, COUT35, COUT37, COUT40, COUT41, COUT43, COUT46, COUT47, COUT93, COUT97-C OUT102	CAP., 100μF X6S 6 .3V 20%1210	MURATA, GRM32EC80J10 7ME20L
4	1 6	COUT4, COUT5, COUT9-COUT13, COUT17, COUT28, COU T34, COUT36, COUT38, COUT39, COUT42, COUT44, COUT 45,	CAP., 470μF, ALU M POLY, 2.5V, 20% SMD D3L	PANASONIC, E EF-GX0E471R
5	1	C15	CAP., X7R, 0.015µ F, 25V, 5%, 0603	AVX, 06033C15 3JAT2A
6	3	C28, C73, C74	CAP., X7R, 0.01μF, 25V, 5%, 0603	AVX, 06033C10 3JAT2A
7	4	C33, C35, C45, C57	CAP., X6S, 22μF, 6 .3V, 20%, 0603	MURATA, GRM188C80J22 6ME15D
8	1	C64	CAP., X5R, 4.7μF, 16V, 10%, 0603	TDK, C1608X5R1C47 5K080AC

9	2	C65, C68	CAP., X7R, 1µF, 25 V, 10%, 1206	KEMET, C1206 C105K3RACTU
1 0	1	C66	CAP., X7R, 0.22μF, 16V, 10%, 0805	TDK, C2012X7R1C22 4K
1	1	C67	CAP., X5R, 0.1μF, 16V, 10%, 1206	AVX, 1206YD10 4KAT2A
1 2	1	C69	CAP., X7R, 1800pF , 25V, 5%, 0603	AVX, 06033C18 2JAT2A
1 3	1	C70	CAP., X7R, 1μF, 25 V, 10%, 0805	AVX, 08053C10 5KAT2A
1 4	1	C71	CAP., X5R, 0.1μF, 16V, 10%, 0603	AVX, 0603YD10 4KAT2A
1 5	1	D1	LED, SUPER RED, WATERCLEAR, 06 03	WURTH ELEKT RONIK, 150060 SS75000
1 6	1	D3	DIODE SCHOTTK Y 20V 500MA SOD 882	NXP, PMEG200 5AEL
1 7	2	D4, D5	LED, GREEN, WAT ERCLEAR, 0603	WURTH ELEKT RONIK, 150060 GS75000
1 8	1	Q1	XSTR., MOSFET, P-CH, 20V, 5.9A, T O-236 (SOT-23)	VISHAY, Si2365 EDS-T1-GE3
1 9	2	Q2, Q3	MOSFET N-CHAN NEL 30V 90A TO2 52	TAIWAN SEMIC ONDUCTOR C ORPORATION, TSM040N03CP ROG
2 0	1	Q4	XSTR., MOSFET, P-CH, 30V, 3.5A, S OT-23, AEC-Q101	DIODES INC., D MP3130L-7
2	2	Q5, Q6	XSTR., MOSFET, N-CH, 60V, 300mA , SOT-23	FAIRCHILD SE MI., 2N7002K
2 2	1 5	R10-R15, R18, R24, R94, R106, R116, R126, R142, R154, R2	RES., 10k, 5%, 1/1 0W, 0603	VISHAY, CRCW 060310K0JNEA
2	5	R25, R32, R70, R125, R237	RES., 10Ω, 1%, 1/1 0W, 0603	VISHAY, CRCW 060310R0FKEA
2 4	1	R26	RES., 787Ω, 1%, 1/ 10W, 0603	VISHAY, CRCW 0603787RFKEA
2 5	1	R86	RES., 127Ω, 1%, 1/ 10W, 0603	VISHAY, CRCW 0603127RFKEA

2 6	4	R90, R108, R133, R159	RES., 0.001Ω, 2W, 1%, 2512, SENSE, AEC-Q200	BOURNS INC., CRF-2512-FZ-R 001ELF
2 7	1	R110	RES., 5k, 10%, 1/2 W, THT 3/8" SQUA RE, SINGLE TURN, TOP ADJ., TRIMPOT	BOURNS, 3386 P-1-502-LF
2 8	2	R111, R115	RES., 20k, 5%, 1/1 0W, 0603, AEC-Q2 00	VISHAY, CRCW 060320K0JNEA
2 9	1	R113	RES., 1.65k, 1%, 1 /10W, 0603, AEC- Q200	VISHAY, CRCW 06031K65FKEA
3	2	R118, R232	RES., 0.003Ω 1% 1 /2W 2010	VISHAY, WSL20 103L000FEA
3	1	R123	RES., 1M, 5%, 1/1 0W, 0603, AEC-Q2 00	VISHAY, CRCW 06031M00JNEA
3 2	2	R124, R224	RES., 200Ω, 1%, 1/ 10W, 0603	VISHAY, CRCW 0603200RFKEA
3	1	R137	RES., 2.43k, 1%, 1 /10W, 0603, AEC- Q200	VISHAY, CRCW 06032K43FKEA
3 4	1	R148	RES., 3.24k, 1%, 1 /10W, 0603, AEC- Q200	VISHAY, CRCW 06033K24FKEA
3 5	1	R203	RES., 681k, 1%, 1/ 10W, 0603	VISHAY, CRCW 0603681KFKEA
3 6	1	R204	RES., 3.3Ω, 1%, 1/ 10W, 0603, AEC-Q 200	VISHAY, CRCW 06033R30FKEA
3 7	1	R205	RES., 82.5Ω, 1%, 1 /10W, 0603, AEC- Q200	VISHAY, CRCW 060382R5FKEA
3 8	2	R213, R223	RES., 4.99k, 1%, 1 /10W, 0603, AEC- Q200	VISHAY, CRCW 06034K99FKEA
3 9	1	R220	RES., 15.8k, 1%, 1 /10W, 0603, AEC- Q200	VISHAY, CRCW 060315K8FKEA
4 0	1	R229	RES., 100k, 1%, 1/ 10W, 0603, AEC-Q 200	VISHAY, CRCW 0603100KFKEA
4	1	R233	RES., 100Ω, 1%, 1 W, 0603, AEC-Q20 0	VISHAY, CRCW 0603100RFKEA

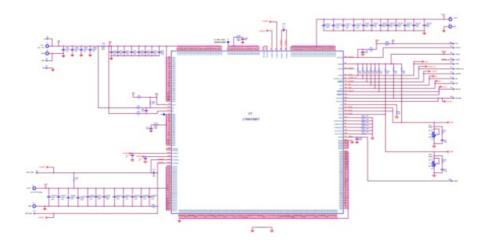
4 2	1	R234	RES., 1.4k, 1%, 1/1 0W, 0603, AEC-Q2 00	VISHAY, CRCW 06031K40FKEA
4 3	1	R235	RES., 154k, 5%, 1/ 10W, 0603, AEC-Q 200	VISHAY, CRCW 0603154KJNEA
4 4	1	R236	RES., 2Ω, 1%, 1/10 W, 0603, AEC-Q20 0	VISHAY, CRCW 06032R00FKEA
4 5	2	SW1, SW2	SWITCH, SUBMINI ATURE SLIDE	C&K COMPON ENTS, JS20201 1CQN
4 6	2	U1, U2	IC, LTM4700EY	
4 7	1	U5	IC, MEMORY, EEP ROM, 2kb (256×8), TSSOP-8, 400kHz	MICROCHIP, 24 LC025-I/ST
4 8	1	U6	IC, µPWR LDO RE G W/SHUTDOWN, SO-8	ANALOG DEVI CES, LT1129CS 8-5#PBF
4 9	1	U7	IC, TIMERBLOX: V OLTAGE-CTRL. P WM, TSOT23-6	ANALOG DEVI CES, LTC6992C S6-1#PBF
5	1	U8	IC, SINGLE 100V, 85MHz, OP AMP, T SOT-23-5	ANALOG DEVI CES, LT1803IS5 #PBF
Ad	ditic	onal Demo Board Circuit Components		
1	0	C14, C16, C17, C29, C31, C32, C34, C36, C40- C44, C46-C56, C58,	CAP., OPTION 060	OPTION
2	0	COUT48-COUT51, COUT53-COUT57, COUT59, COUT61, C OUT64, COUT65, COUT67, COUT70-COUT73, COUT76, CO UT77, COUT79, COUT82-COUT87, COUT89, COUT90-COUT 92, COUT94, CIN19-CIN24, CIN26-CIN32, CIN34-CIN36	CAP., OPT, 1210	OPT
3	0	COUT52, COUT58, COUT60, COUT62, COUT63, COUT66, C OUT68, COUT69, COUT74, COUT75, COUT78, COUT80, CO UT81, COUT88, COUT95, COUT96,	OPT, D3L	OPTION
4	0	D2	DIODE, OPTION, SOD-323	OPTION
5	0	R8, R27-R31, R109, R117, R120, R121, R127, R130, R134, R 138-R140, R144, R145, R150-R152, R155, R157, R160, R208 , R216, R218, R219, R221, R226, R238-R245	RES., OPTION, 06 03	OPTION
6	1 7	R9, R91, R92, R112, R114, R132, R135, R136, R146, R147, R149, R158, R202, R211, R215, R222, R227	RES., 0Ω, 1/10W, 0 603	VISHAY, CRCW 06030000Z0EA
7	0	R104, R105, R122, R128, R141, R143, R153, R156	RES., OPTION, 08 05	OPTION

^			
0	R230, R231	RES., OPTION, 25	OPTION
0	U3, U4	IC, OPT	ОРТ
dw	are: For Demo Board Only		
2 5	E1-E21, E23-E26	TEST POINT, TUR RET, 0.064", MTG. HOLE	MILL-MAX, 2308 -2-00-80-00-00- 07-0
2	JP2, JP4	CONN., HEADER, 1×3, 2mm	WURTH ELEKT RONIK, 620003 11121
2	XJP2, XJP4	CONN., SHUNT, F EMALE, 2 POS, 2 mm	WURTH ELEKT RONIK, 608002 13421
1	J1, J2, J5-J8, J16, J18-J20	STUD, FASTENER , #10-32	PENNENGINEE RING, KFH-032- 10ET
2	J1, J2, J5-J8, J16, J18-J20 (×2)	NUT, HEX, STEEL, ZINC PLATE, 10-3 2	KEYSTONE, 47 05
1	J1, J2, J5,-J8, J16, J18-J20	RING, LUG, CRIM P, #10, NON-INSU LATED, SOLDERL ESS TERMINALS	KEYSTONE, 82 05
1	J1, J2, J5-J8, J16, J18-J20	WASHER, FLAT, S TEEL, ZINC PLAT E, OD: 0.436 [11.1]	KEYSTONE, 47 03
2	J15, J21	CONN., RF, BNC, RCPT, THT, STR, 5 -PIN	AMPHENOL CO NNEX, 112404
1	J17	CONN., HDR, MAL E, 2×7, 2mm, R/A THT	MOLEX, 87760- 1416
1	J22	CONN., HDR, FEM ALE, 2×7, 2mm, R/ A THT	SULLINS CON., NPPN072FJFN- RC
1	J23	CONN., HDR, SHR OUDED, 2×6, 2mm , THT, VERT	FCI, 98414-G06 -12ULF
1	J24	CONN., HDR, SHR OUDED, 1×4, 2mm , R/A THT STR	HIROSE ELECT RIC CO., LTD., DF3A-4P-2DSA
	dwa 2 5 2 1 0 2 0 1 0 1 1 1 1	dware: For Demo Board Only 2	Description Description

1 3	4 MH1-MH4	STANDOFF, NYLO N, SNAP-ON, 0.50"	WURTH ELEKT RONIK, 702935 000
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SCHEMATIC DIAGRAM

REVISION HISTORY						
ECO REV DESCRIPTION		APPROVED	DATE			
_	1	PRODUCTION	SG	07-26-2021		



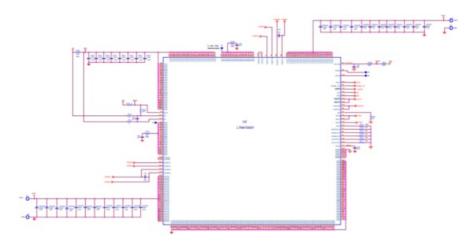
A S S Y	U 3	U 4	C 1 4	C 1 5	COUT61, COUT59, C OUT57, COUT70, CO UT71, COUT72, COU T50, COUT56 COUT6 4, COUT67, COUT65, COUT55, COUT54, C OUT53, COUT51, CO UT49	COUT73, COUT94, C OUT92, COUT82, CO UT83, COUT84, COU T86, COUT48 COUT7 6, COUT77, COUT79, COUT89, COUT91, C OUT90, COUT85, CO UT87	CIN19, CIN20, CIN21, CIN22, CIN2 3, CIN 24, CI N26, C IN27	CIN28, CIN29, CIN30, CIN31, CIN32, CIN34, CIN35, CIN36	COUT52, COUT58, COUT60, COUT62, COUT63, COUT66, COUT68, COUT69	COUT74, COUT75, COUT78, COUT80, COUT81, COUT88, COUT95, COUT96
- A	O P T	O P T	O P T	1 5 n F	OPT	OPT	OPT	OPT	OPT	OPT
- B	L T M 4 7 0	O P T	O P T	2 2 n F	100uF	OPT	22uF	OPT	470uF	OPT
- C	L T M 4 7 0	L T M 4 7 0	5 6 0 p F	1 0 n F	100uF	100uF	22uF	22uF	470uF	470uF

NOTE: UNLESS OTHERWISE SPECIFIED

- 1. ALL RESISTORS AND CAP ARE 0603.
- 2. WHEN VIN < 5.75V, CHANGE R8,R109,R134,R160 = 0 OHM

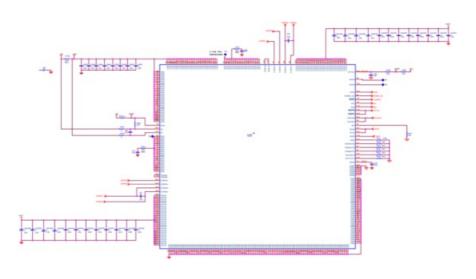
AF RC AL	VC		
РСвово.	L T	ANALOG DEVICES	
APPENG.	S G		
		TITLE: SCH	

CUSTOMER NOTICE ANALOG DEVICES HAS MADE A BEST EFFORT TO DESIGN A CIRCUIT THAT ME ETS CUSTOMER-SUPPLIED SPECIFICATIONS; HOWEVER, IT REMAINS THE CUS TOMER'S RESPONSIBILITY TO VERIFY PROPER AND RELIABLE OPERATION IN THE ACTUAL APPLICATION. COMPONENT SUBSTITUTION AND PRINTED CIRCUIT BOARD LAYOUT MAY SIGNIFICANTLY AFFECT CIRCUIT PERFORMANCE OR R ELIABILITY. CONTACT ANALOG DEVICES APPLICATIONS ENGINEERING FOR AS SISTANCE.	ENT I HASE -DOV WER ULE S Y WIT TAL I R MA		GH C ASE S OWN ER u LE SU WITH	CURR POLYP STEP IN PO UMOD SUPPL H DIGI POWE NAGE	
		. L 470 Y SK NC C2). D 784 A,B	SC HE MA TIC NO. AN D R EVI SIO N: 710 -DC 278 4B(A,B ,C) _R EVO 1	
THIS CIRCUIT IS PROPRIETARY TO ANALOG DEVICES AND SUPPLIED FOR USE WITH ANALOG DEVICES PARTS.		S I Z E : N / A	D AT E: M on da y, Jul y 2 6, 20 21	SH EE T 1 OF 6	



	R	PP OV LS		
	P C B D E S	L T	ANA	ALOG /ICES
	A P P E N G	S G		
			TITLE: S	
CUSTOMER NOTICE ANALOG DEVICES HAS MADE A BEST EFFORT TO DESIGN A CIRCUIT THAT ME ETS CUSTOMER-SUPPLIED SPECIFICATIONS; HOWEVER, IT REMAINS THE CUS TOMER'S RESPONSIBILITY TO VERIFY PROPER AND RELIABLE OPERATION IN THE ACTUAL APPLICATION. COMPONENT SUBSTITUTION AND PRINTED CIRCUIT BOARD LAYOUT MAY SIGNIFICANTLY AFFECT CIRCUIT PERFORMANCE OR R ELIABILITY. CONTACT ANALOG DEVICES APPLICATIONS ENGINEERING FOR AS SISTANCE.			HIGH C ENT PC HASE S -DOWN WER ul ULE SU Y WITH TAL PC R MAN MEN	DLYP STEP I PO MOD JPPL DIGI DWE AGE
			IC NO	SC HE MA TIC NO. AN

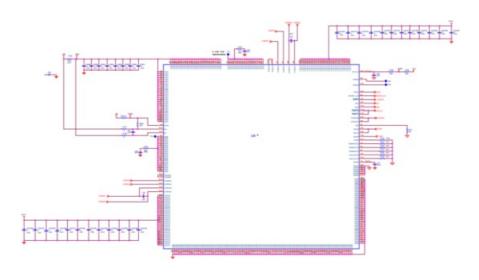
		47 Y SK NC C2). D 784 A,B	D R EVI SIO N: 710 -DC 278 4B(A,B ,C) _R EV0 1	
THIS CIRCUIT IS PROPRIETARY TO ANALOG DEVICES AND SUPPLIED FOR USE WITH ANALOG DEVICES PARTS.		S I Z E : N / A	D AT E: M on da y, Jul y 2 6, 20 21	SH EE T2 OF 6	



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	P C B D E S .	L T	ANALOG DEVICES
P P E N	A P P E N G .	S G	
			TITLE: SCH EMATIC HIGH CURR ENT POLYP HASE STEP -DOWN PO WER UMOD ULE SUPPL Y WITH DIGI TAL POWE R MANAGE MENT
			SC HE MA TIC NO. IC NO AN . LTM D R 4700E EVI Y SIO N: SKU NO. D 710 C2784 -DC B(A,B ,C) _R EV0 1

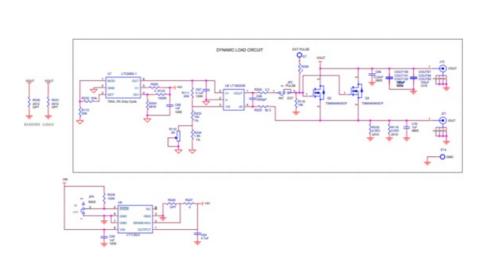
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CUSTOMER NOTICE			TITLE: SCH EMATIC HIGH CURR ENT POLYP

ANALOG DEVICES HAS MADE A BEST EFFORT TO DESIGN A CIRCUIT THAT ME ETS CUSTOMER-SUPPLIED SPECIFICATIONS; HOWEVER, IT REMAINS THE CUS TOMER'S RESPONSIBILITY TO VERIFY PROPER AND RELIABLE OPERATION IN THE ACTUAL APPLICATION. COMPONENT SUBSTITUTION AND PRINTED CIRCUIT BOARD LAYOUT MAY SIGNIFICANTLY AFFECT CIRCUIT PERFORMANCE OR R ELIABILITY. CONTACT ANALOG DEVICES APPLICATIONS ENGINEERING FOR AS SISTANCE.		HASE STEI -DOWN PO WER UMOD ULE SUPPI Y WITH DIG TAL POWE R MANAGE MENT		
		. L 470 Y SK NC C2). D 784 A,B	SC HE MA TIC NO. AN D R EVI SIO N: 710 -DC 278 4B(A,B ,C) _R EVO 1
THIS CIRCUIT IS PROPRIETARY TO ANALOG DEVICES AND SUPPLIED FOR USE WITH ANALOG DEVICES PARTS.		S I Z E : N / A	D AT E: M on da y, Jul y 2 6, 20 21	SH EE T 4 OF 6

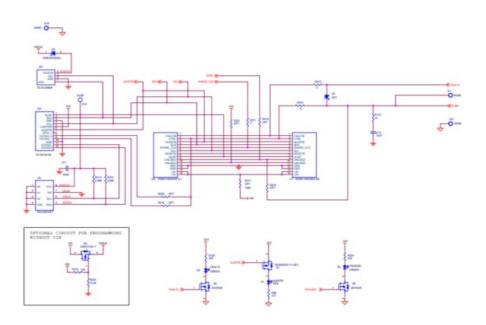
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		47 Y SK NC C2	D. D 784 A,B	D R EVI SIO N: 710 -DC 278 4B(A,B ,C) _R EVO 1	
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	P C B D E S . A P P E N G	L T	► ANA	ALOG VICES
CUSTOMER NOTICE ANALOG DEVICES HAS MADE A BEST EFFORT TO DESIGN A CIRCUIT THAT ME ETS CUSTOMER-SUPPLIED SPECIFICATIONS; HOWEVER, IT REMAINS THE CUS TOMER'S RESPONSIBILITY TO VERIFY PROPER AND RELIABLE OPERATION IN THE ACTUAL APPLICATION. COMPONENT SUBSTITUTION AND PRINTED CIRCUIT BOARD LAYOUT MAY SIGNIFICANTLY AFFECT CIRCUIT PERFORMANCE OR R ELIABILITY. CONTACT ANALOG DEVICES APPLICATIONS ENGINEERING FOR AS	•		TITLE: SEMATIC HIGH CENT POHASE SENDOWN WER UNDER SUMMER UNDER SUMER UNDER SUMER UNDER SUMER UNDER SUMER MANAMEN	URR DLYP STEP I PO MOD IPPL DIGI DWE AGE
SISTANCE.			IC NO . LTM 4700E Y SKU NO. D C2784 B(A,B ,C)	SC HE MA TIC NO. AN D R EVI SIO N: 710 -DC 278 4B(A,B ,C) _R EVO 1

THIS CIRCUIT IS PROPRIETARY TO ANALOG DEVICES AND SUPPLIED FOR USE WITH ANALOG DEVICES PARTS.		SIZE:N/A	D AT E: M on da y, Jul y 2 6, 20 21	SH EE T 6 OF 6	
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REVISION HISTORY

REV	DATE	DESCRIPTION	PAGE NUMBER
0	10/24	Initial Release.	_

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Documents / Resources



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References

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