

## hp Motherboard Layout and Jumper Setting for the 440BX Chipset Model 8290 User Guide

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**hp Motherboard Layout and Jumper Setting for the 440BX Chipset Model 8290**

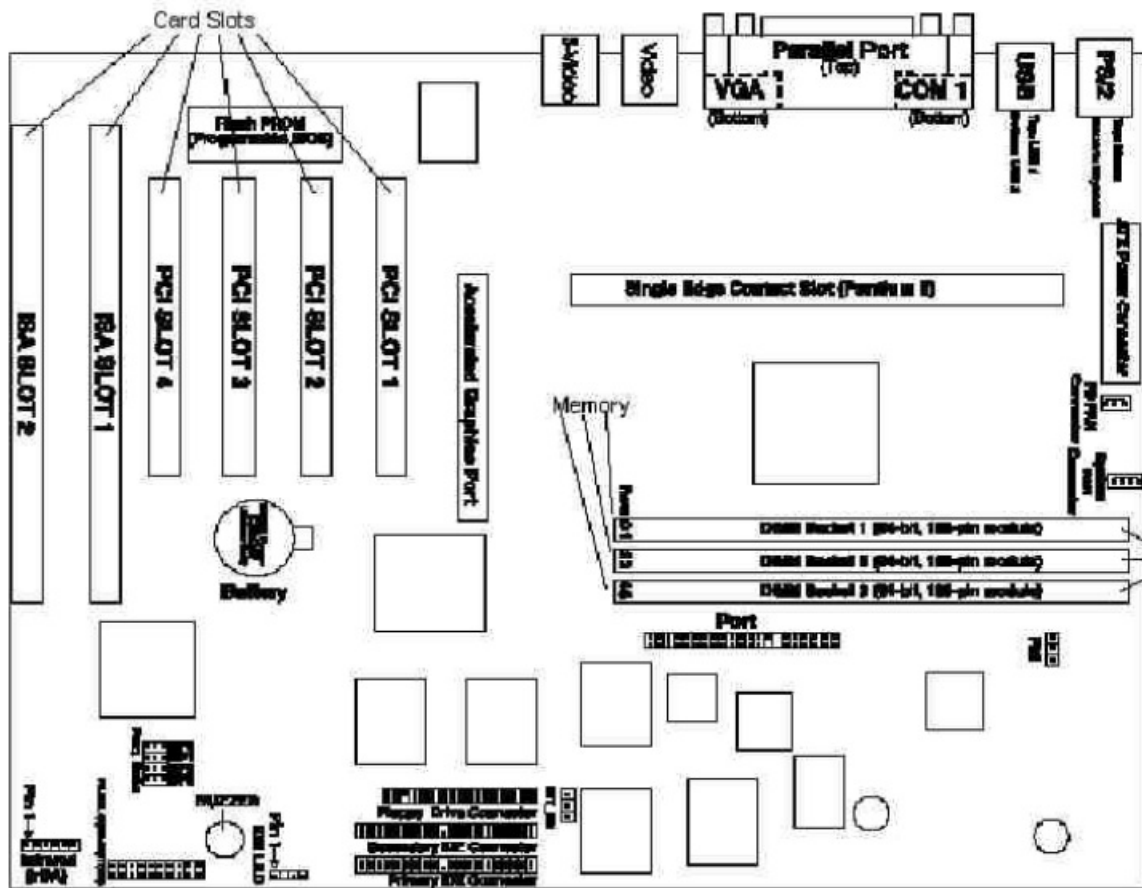


## INSTRUCTION

- System Board Layout
- CPU Frequency Ratio
- Power Supply/System Fan Connector
- Front Panel Connector
- Back Panel I/O Connectors
- Serial Port Connector
- Parallel Port
- Floppy Connector

### System Board Layout

Figure 1: System board



### Tips

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### CPU Frequency Ratio

Figure 2: Frequency ratio

CPU Freq. (MHz)	Ratio	Bus Freq. (MHz)	BUS Frequency FSB	Frequency Ratio			
				BFO	BF1	BF2	BF3
200	2.0x	100	1-2	2-3	2-3	2-3	2-3
250	2.5x	100	1-2	1-2	2-3	2-3	2-3
300	3.0x	100	1-2	2-3	1-2	2-3	2-3
350	3.5x	100	1-2	1-2	1-2	2-3	2-3
400 *	4.0x	100	1-2 *	2-3 *	2-3 *	1-2 *	2-3 *
450	4.5x	100	1-2	1-2 *	2-3	1-2	2-3
500	5.0x	100	1-2	2-3	1-2	1-2	2-3
133	2.0x	66	2-3	2-3	2-3	2-3	2-3
166	2.5x	66	2-3	1-2	2-3	2-3	2-3
200	3.0x	66	2-3	2-3	1-2	2-3	2-3
233	3.5x	66	2-3	1-2	1-2	2-3	2-3
266	4.0x	66	2-3	2-3	2-3	1-2	2-3
300	4.5x	66	2-3	1-2 *	2-3	1-2	2-3
333	5.0x	66	2-3	2-3	1-2	1-2	2-3

## Power Supply/System Fan Connector

Figure 3: Connector tables

### POWER SUPPLY FAN CONNECTOR

Pin	Signal Name
1	Ground
2	+12 V
3	Ground

### Power Supply Fan Connector

### SYSTEM FAN CONNECTOR

Pin	Signal Name
1	+12 V
2	Ground
3	FAN_SEN

## Front Panel Connector

The front panel connectors include headers for the following connections, Power Led, Power Switch, and IDE Led.

Figure 4: Front panel connector table

<i>Pin</i>	<i>Connector</i>
1	Power Switch
2	
3	no connection
4	no connection
5	no connection
6	Power no connection LED
7	
8	
9	no connection
10	no connection
11	no connection
12	no connection
13	no connection
14	no connection
15	no connection
16	no connection
17	no connection
18	no connection
19	no connection
20	no connection

### Back Panel I/O Connectors

Figure 5: Back Panel I/O connectors table

<i>Pin</i>	<i>Signal Name</i>
1	Data
2	No Connect
3	Ground
4	+5 Vcc (fused)
5	Clock
6	No Connect

### PS/2 Keyboard and Mouse Ports

### USB CONNECTORS (USB 0 AND USB 1)

<i>Pin</i>	<i>Signal Name</i>
1	Power
2	USBP0+ (USBP1+)
3	USBP0 (USBP1)
4	Ground

### Serial Port Connector

Figure 6: Serial port connector table

Pin	Signal Name
1	Data Carrier Detect (DCD)
2	Receive Data (RXD)
3	Transmit Data (TXD)
4	Data Terminal Ready (DTR)
5	Ground
6	Data Set Ready (DSR)
7	Request to Send (RTS)
8	Clear to Send (CTS)
9	Ring indicator

### Parallel Port

Figure 7: Parallel port connector table

Signal Name	Pin	Pin	Signal Name
STROBE-	1	14	AUTO FEED
Data Bit 0	2	15	ERROR*
Data Bit 1	3	16	INIT*
Data Bit 2	4	17	SELECT IN*
Data Bit 3	5	18	Ground
Data Bit 4	6	19	Ground
Data Bit 5	7	20	Ground
Data Bit 6	8	21	Ground
Data Bit 7	9	22	Ground
ACK*	10	23	Ground
BUSY	11	24	Ground
Error	12	25	Ground
SELECT	13		

### Floppy Connector

Figure 8: Floppy connector table

Signal Name	Pin	Pin	Signal Name
Ground	1	2	DENSEL
Ground	3	4	Reserved
Key	5	6	FDIEDIN
Ground	7	8	Index
Ground	9	10	Motor Enable A
Ground	11	12	Drive Select B
Ground	13	14	Drive Select A
Ground	15	16	Motor Enable B
MSEN1	17	18	DIR
Ground	19	20	STEP
Ground	21	22	Write Data
Ground	23	24	Write Enable
Ground	25	26	Track 00
MSEN0	27	28	Write Protect
Ground	29	30	Read Data
Ground	31	32	Side 1 Select
Ground	33	34	Diskette Change

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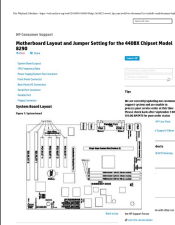
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Motherboard Layout and Jumper Setting for the 440BX Chipset Model 8290, Motherboard, Motherboard Layout and Jumper Setting for the 440BX Chipset

## References

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